

# High Voltage Thyristor \ Diode Module

$$V_{RRM} = 2 \times 2200 \text{ V}$$

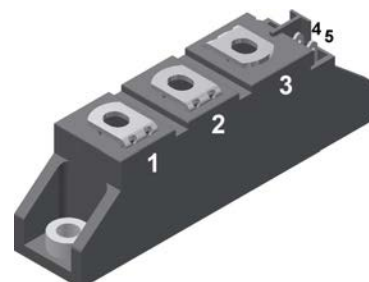
$$I_{TAV} = 95 \text{ A}$$

$$V_T = 1,24 \text{ V}$$

Phase leg

Part number

**MCNA95PD2200TB**



Backside: isolated

 E72873



## Features / Advantages:

- Thyristor for line frequency
- Planar passivated chip
- Long-term stability
- Direct Copper Bonded Al<sub>2</sub>O<sub>3</sub>-ceramic

## Applications:

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- AC power control
- Lighting and temperature control

## Package: TO-240AA

- Isolation Voltage: 4800 V~
- Industry standard outline
- RoHS compliant
- Soldering pins for PCB mounting
- Base plate: DCB ceramic
- Reduced weight
- Advanced power cycling

## Disclaimer Notice

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| Rectifier      |                                                             |                                                                                                                                                                                                       |                                | Ratings |      |      |                   |
|----------------|-------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------|---------|------|------|-------------------|
| Symbol         | Definition                                                  | Conditions                                                                                                                                                                                            |                                | min.    | typ. | max. | Unit              |
| $V_{RSM/DSM}$  | <b>max. non-repetitive reverse/forward blocking voltage</b> | $T_{VJ} = 25^{\circ}\text{C}$                                                                                                                                                                         |                                |         |      | 2300 | V                 |
| $V_{RRM/DRM}$  | <b>max. repetitive reverse/forward blocking voltage</b>     | $T_{VJ} = 25^{\circ}\text{C}$                                                                                                                                                                         |                                |         |      | 2200 | V                 |
| $I_{R/D}$      | <b>reverse current, drain current</b>                       | $V_{R/D} = 2200\text{ V}$                                                                                                                                                                             | $T_{VJ} = 25^{\circ}\text{C}$  |         |      | 100  | $\mu\text{A}$     |
|                |                                                             | $V_{R/D} = 2200\text{ V}$                                                                                                                                                                             | $T_{VJ} = 140^{\circ}\text{C}$ |         |      | 10   | mA                |
| $V_T$          | <b>forward voltage drop</b>                                 | $I_T = 95\text{ A}$                                                                                                                                                                                   | $T_{VJ} = 25^{\circ}\text{C}$  |         |      | 1,26 | V                 |
|                |                                                             | $I_T = 190\text{ A}$                                                                                                                                                                                  |                                |         |      | 1,54 | V                 |
|                |                                                             | $I_T = 95\text{ A}$                                                                                                                                                                                   | $T_{VJ} = 125^{\circ}\text{C}$ |         |      | 1,24 | V                 |
|                |                                                             | $I_T = 190\text{ A}$                                                                                                                                                                                  |                                |         |      | 1,63 | V                 |
| $I_{TAV}$      | <b>average forward current</b>                              | $T_C = 85^{\circ}\text{C}$                                                                                                                                                                            | $T_{VJ} = 140^{\circ}\text{C}$ |         |      | 95   | A                 |
| $I_{T(RMS)}$   | <b>RMS forward current</b>                                  | 180° sine                                                                                                                                                                                             |                                |         |      | 149  | A                 |
| $V_{T0}$       | <b>threshold voltage</b>                                    | } <b>for power loss calculation only</b>                                                                                                                                                              | $T_{VJ} = 140^{\circ}\text{C}$ |         |      | 0,84 | V                 |
| $r_T$          | <b>slope resistance</b>                                     |                                                                                                                                                                                                       |                                |         |      | 4,1  | m $\Omega$        |
| $R_{thJC}$     | <b>thermal resistance junction to case</b>                  |                                                                                                                                                                                                       |                                |         |      | 0,3  | K/W               |
| $R_{thCH}$     | <b>thermal resistance case to heatsink</b>                  |                                                                                                                                                                                                       |                                |         | 0,2  |      | K/W               |
| $P_{tot}$      | <b>total power dissipation</b>                              |                                                                                                                                                                                                       | $T_C = 25^{\circ}\text{C}$     |         |      | 383  | W                 |
| $I_{TSM}$      | <b>max. forward surge current</b>                           | $t = 10\text{ ms}; (50\text{ Hz}), \text{ sine}$                                                                                                                                                      | $T_{VJ} = 45^{\circ}\text{C}$  |         |      | 1,70 | kA                |
|                |                                                             | $t = 8,3\text{ ms}; (60\text{ Hz}), \text{ sine}$                                                                                                                                                     | $V_R = 0\text{ V}$             |         |      | 1,84 | kA                |
|                |                                                             | $t = 10\text{ ms}; (50\text{ Hz}), \text{ sine}$                                                                                                                                                      | $T_{VJ} = 140^{\circ}\text{C}$ |         |      | 1,45 | kA                |
|                |                                                             | $t = 8,3\text{ ms}; (60\text{ Hz}), \text{ sine}$                                                                                                                                                     | $V_R = 0\text{ V}$             |         |      | 1,56 | kA                |
| $I^2t$         | <b>value for fusing</b>                                     | $t = 10\text{ ms}; (50\text{ Hz}), \text{ sine}$                                                                                                                                                      | $T_{VJ} = 45^{\circ}\text{C}$  |         |      | 14,5 | kA <sup>2</sup> s |
|                |                                                             | $t = 8,3\text{ ms}; (60\text{ Hz}), \text{ sine}$                                                                                                                                                     | $V_R = 0\text{ V}$             |         |      | 14,0 | kA <sup>2</sup> s |
|                |                                                             | $t = 10\text{ ms}; (50\text{ Hz}), \text{ sine}$                                                                                                                                                      | $T_{VJ} = 140^{\circ}\text{C}$ |         |      | 10,4 | kA <sup>2</sup> s |
|                |                                                             | $t = 8,3\text{ ms}; (60\text{ Hz}), \text{ sine}$                                                                                                                                                     | $V_R = 0\text{ V}$             |         |      | 10,1 | kA <sup>2</sup> s |
| $C_J$          | <b>junction capacitance</b>                                 | $V_R = 700\text{ V}$ $f = 1\text{ MHz}$                                                                                                                                                               | $T_{VJ} = 25^{\circ}\text{C}$  |         | 50   |      | pF                |
| $P_{GM}$       | <b>max. gate power dissipation</b>                          | $t_p = 30\text{ }\mu\text{s}$                                                                                                                                                                         | $T_C = 140^{\circ}\text{C}$    |         |      | 10   | W                 |
|                |                                                             | $t_p = 300\text{ }\mu\text{s}$                                                                                                                                                                        |                                |         |      | 5    | W                 |
| $P_{GAV}$      | <b>average gate power dissipation</b>                       |                                                                                                                                                                                                       |                                |         |      | 0,5  | W                 |
| $(di/dt)_{cr}$ | <b>critical rate of rise of current</b>                     | $T_{VJ} = 140^{\circ}\text{C}; f = 50\text{ Hz}$ repetitive, $I_T = 285\text{ A}$                                                                                                                     |                                |         |      | 150  | A/ $\mu\text{s}$  |
|                |                                                             | $t_p = 200\text{ }\mu\text{s}; di_G/dt = 0,45\text{ A}/\mu\text{s};$<br>$I_G = 0,45\text{ A}; V_D = \frac{2}{3} V_{DRM}$ non-repet., $I_T = 95\text{ A}$                                              |                                |         |      | 500  | A/ $\mu\text{s}$  |
| $(dv/dt)_{cr}$ | <b>critical rate of rise of voltage</b>                     | $V_D = \frac{2}{3} V_{DRM}$ $T_{VJ} = 140^{\circ}\text{C}$<br>$R_{GK} = \infty$ ; method 1 (linear voltage rise)                                                                                      |                                |         |      | 1000 | V/ $\mu\text{s}$  |
| $V_{GT}$       | <b>gate trigger voltage</b>                                 | $V_D = 6\text{ V}$                                                                                                                                                                                    | $T_{VJ} = 25^{\circ}\text{C}$  |         |      | 1,5  | V                 |
|                |                                                             |                                                                                                                                                                                                       | $T_{VJ} = -40^{\circ}\text{C}$ |         |      | 1,6  | V                 |
| $I_{GT}$       | <b>gate trigger current</b>                                 | $V_D = 6\text{ V}$                                                                                                                                                                                    | $T_{VJ} = 25^{\circ}\text{C}$  |         |      | 150  | mA                |
|                |                                                             |                                                                                                                                                                                                       | $T_{VJ} = -40^{\circ}\text{C}$ |         |      | 200  | mA                |
| $V_{GD}$       | <b>gate non-trigger voltage</b>                             | $V_D = \frac{2}{3} V_{DRM}$                                                                                                                                                                           | $T_{VJ} = 140^{\circ}\text{C}$ |         |      | 0,2  | V                 |
| $I_{GD}$       | <b>gate non-trigger current</b>                             |                                                                                                                                                                                                       |                                |         |      | 10   | mA                |
| $I_L$          | <b>latching current</b>                                     | $t_p = 10\text{ }\mu\text{s}$                                                                                                                                                                         | $T_{VJ} = 25^{\circ}\text{C}$  |         |      | 200  | mA                |
|                |                                                             | $I_G = 0,45\text{ A}; di_G/dt = 0,45\text{ A}/\mu\text{s}$                                                                                                                                            |                                |         |      |      |                   |
| $I_H$          | <b>holding current</b>                                      | $V_D = 6\text{ V}$ $R_{GK} = \infty$                                                                                                                                                                  | $T_{VJ} = 25^{\circ}\text{C}$  |         |      | 200  | mA                |
| $t_{gd}$       | <b>gate controlled delay time</b>                           | $V_D = \frac{1}{2} V_{DRM}$ $T_{VJ} = 25^{\circ}\text{C}$<br>$I_G = 0,45\text{ A}; di_G/dt = 0,45\text{ A}/\mu\text{s}$                                                                               |                                |         |      | 2    | $\mu\text{s}$     |
| $t_q$          | <b>turn-off time</b>                                        | $V_R = 100\text{ V}; I_T = 95\text{ A}; V_D = \frac{2}{3} V_{DRM}$ $T_{VJ} = 125^{\circ}\text{C}$<br>$di/dt = 10\text{ A}/\mu\text{s}; dv/dt = 20\text{ V}/\mu\text{s}; t_p = 200\text{ }\mu\text{s}$ |                                |         | 500  |      | $\mu\text{s}$     |

| Package TO-240AA |                                                                     |                             |                                     | Ratings |      |      |      |
|------------------|---------------------------------------------------------------------|-----------------------------|-------------------------------------|---------|------|------|------|
| Symbol           | Definition                                                          | Conditions                  |                                     | min.    | typ. | max. | Unit |
| $I_{RMS}$        | <i>RMS current</i>                                                  | per terminal                |                                     |         |      | 200  | A    |
| $T_{VJ}$         | <i>virtual junction temperature</i>                                 |                             |                                     | -40     |      | 140  | °C   |
| $T_{op}$         | <i>operation temperature</i>                                        |                             |                                     | -40     |      | 125  | °C   |
| $T_{stg}$        | <i>storage temperature</i>                                          |                             |                                     | -40     |      | 125  | °C   |
| <b>Weight</b>    |                                                                     |                             |                                     |         | 81   |      | g    |
| $M_D$            | <i>mounting torque</i>                                              |                             |                                     | 2,5     |      | 4    | Nm   |
| $M_T$            | <i>terminal torque</i>                                              |                             |                                     | 2,5     |      | 4    | Nm   |
| $d_{Spp/App}$    | <i>creepage distance on surface   striking distance through air</i> | <i>terminal to terminal</i> | 13,0                                | 9,7     |      |      | mm   |
| $d_{Spb/Apb}$    |                                                                     | <i>terminal to backside</i> | 16,0                                | 16,0    |      |      | mm   |
| $V_{ISOL}$       | <i>isolation voltage</i>                                            | t = 1 second                | 50/60 Hz, RMS; $I_{ISOL} \leq 1$ mA | 4800    |      |      | V    |
|                  |                                                                     | t = 1 minute                |                                     | 4000    |      |      | V    |



### Part description

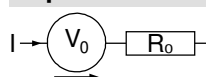
M = Module  
 C = Thyristor (SCR)  
 N = High Voltage Thyristor  
 A = ( $\geq 2000V$ )  
 95 = Current Rating [A]  
 PD = Phase leg  
 2200 = Reverse Voltage [V]  
 TB = TO-240AA-1B

| Ordering | Ordering Number | Marking on Product | Delivery Mode | Quantity | Code No. |
|----------|-----------------|--------------------|---------------|----------|----------|
| Standard | MCNA95PD2200TB  | MCNA95PD2200TB     | Box           | 36       | 523300   |

### Equivalent Circuits for Simulation

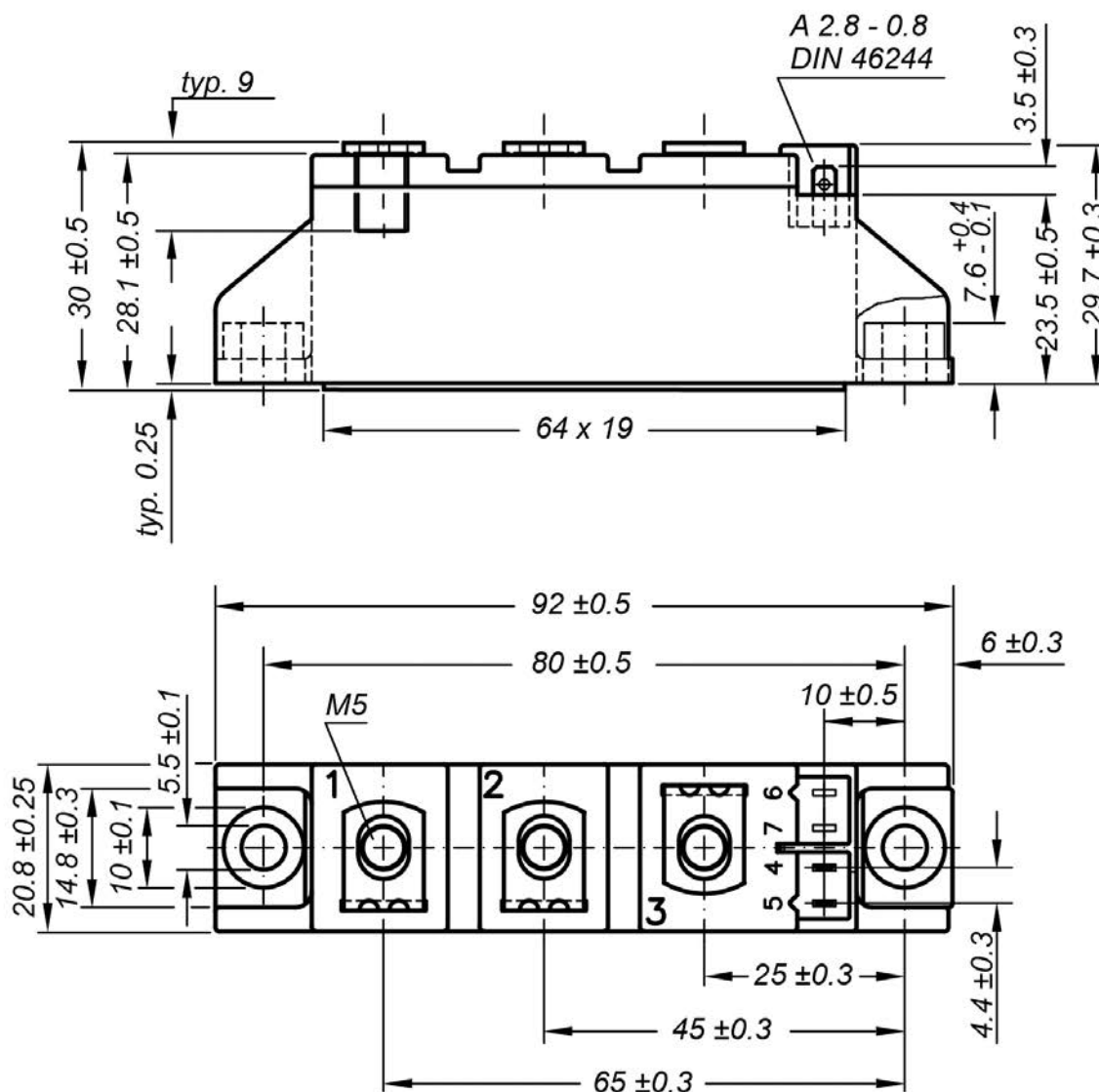
\* on die level

$T_{VJ} = 140^{\circ}C$



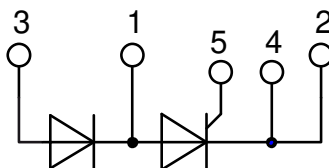
Thyristor

|              |                           |      |    |
|--------------|---------------------------|------|----|
| $V_{0\ max}$ | <i>threshold voltage</i>  | 0,84 | V  |
| $R_{0\ max}$ | <i>slope resistance *</i> | 2,9  | mΩ |

**Outlines TO-240AA**


Optional accessories for modules

Keyed gate/cathode twin plugs with wire length = 350 mm, gate = white, cathode = red  
 Type ZY 200L (L = Left for pin pair 4/5) UL 758, style 3751



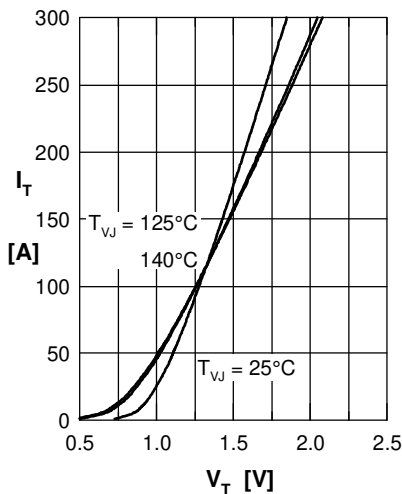
**Thyristor**


Fig. 1 Forward characteristics

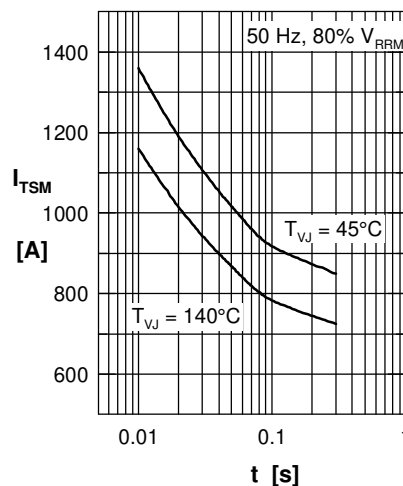
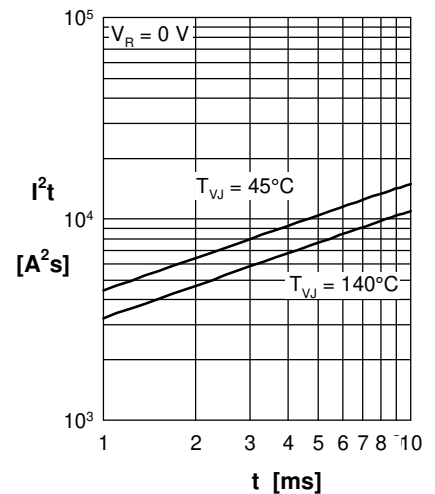
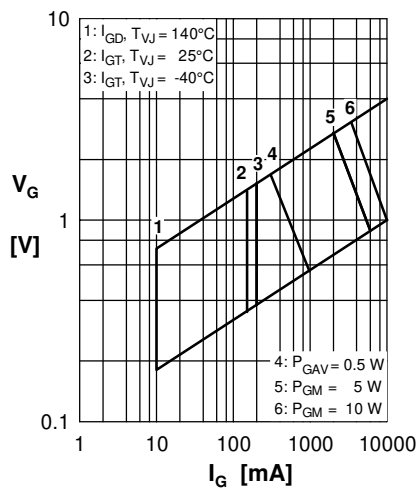

 Fig. 2 Surge overload current  
 $I_{TSM}$ : crest value,  $t$ : duration

 Fig. 3  $I^2t$  versus time (1-10 s)


Fig. 4 Gate voltage &amp; gate current

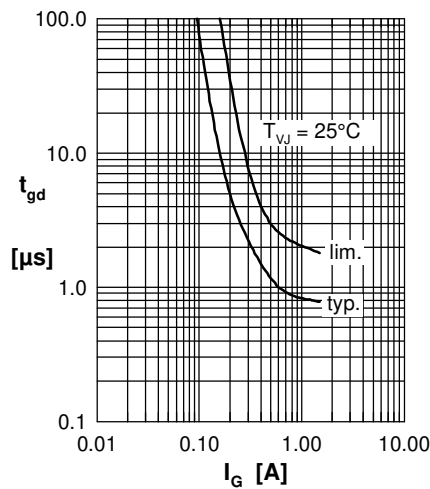
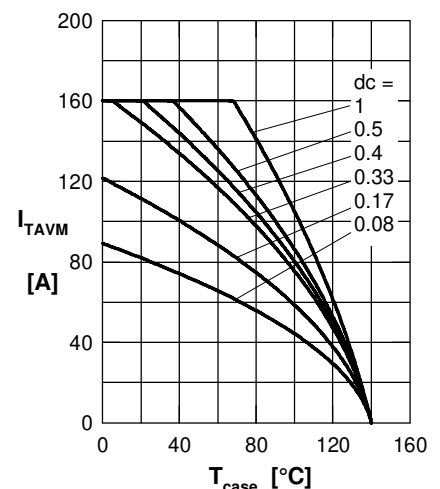

 Fig. 5 Gate controlled delay time  $t_{gd}$ 


Fig. 6 Max. forward current at case temperature

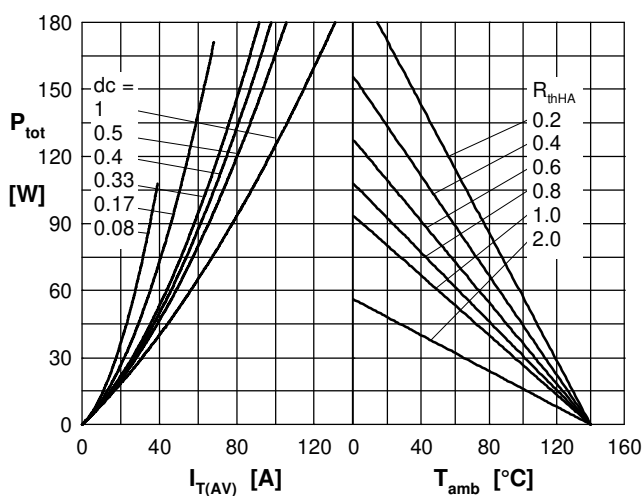
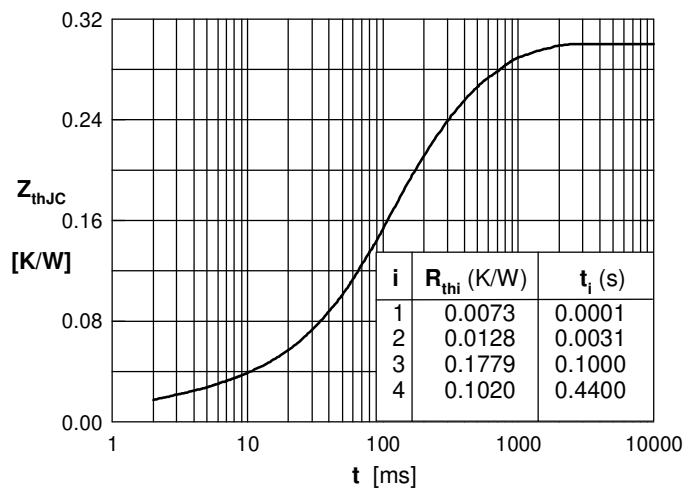

 Fig. 7a Power dissipation versus direct output current  
 Fig. 7b and ambient temperature


Fig. 8 Transient thermal impedance junction to case