

1. General description

Planar passivated high commutation three quadrant triac in a TO263 (D²PAK) surface mountable plastic package intended for use in circuits where high static and dynamic dV/dt and high dI_T/dt can occur. This triac will commute the full RMS current at the maximum rated junction temperature ($T_{j(max)} = 150\text{ °C}$) without the aid of a snubber. It is used in applications where high junction operating temperature capability is required.

2. Features and benefits

- 3Q technology for improved noise immunity
- High commutation capability with maximum false trigger immunity
- High junction operating temperature capability ($T_{j(max)} = 150\text{ °C}$)
- High voltage capability
- High current capability
- Less sensitive gate for highest noise immunity
- Triggering in three quadrants only
- Very high immunity to false turn-on by dV/dt and fast transients
- Surface mountable plastic package
- Package is RoHS compliant

3. Applications

- Heating controls
- High power motor control
- High power switching
- Applications subject to high temperature ($T_{j(max)} = 150\text{ °C}$)

4. Quick reference data

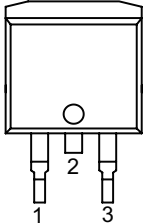
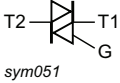
Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	-	800	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{mb} \leq 120\text{ °C}$; Fig. 1 ; Fig. 2 ; Fig. 3	-	-	30	A
I_{TSM}	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25\text{ °C}$; $t_p = 20\text{ ms}$; Fig. 4 ; Fig. 5	-	-	270	A
		full sine wave; $T_{j(init)} = 25\text{ °C}$; $t_p = 16.7\text{ ms}$	-	-	297	A
T_j	junction temperature		-	-	150	°C
Static characteristics						
I_{GT}	gate trigger current	$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2+ G+; $T_j = 25\text{ °C}$; Fig. 7	-	-	50	mA

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
		$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2+ G-; $T_j = 25\text{ }^\circ\text{C}$; Fig. 7		-	-	50	mA
		$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2- G-; $T_j = 25\text{ }^\circ\text{C}$; Fig. 7		-	-	50	mA
I_H	holding current	$V_D = 12\text{ V}$; $T_j = 25\text{ }^\circ\text{C}$; Fig. 9		-	-	75	mA
V_T	on-state voltage	$I_T = 42\text{ A}$; $T_j = 25\text{ }^\circ\text{C}$; Fig. 10		-	1.2	1.5	V
Dynamic characteristics							
dV_D/dt	rate of rise of off-state voltage	$V_{DM} = 536\text{ V}$; $T_j = 125\text{ }^\circ\text{C}$; ($V_{DM} = 67\%$ of V_{DRM}); exponential waveform; gate open circuit		4000	-	-	V/ μs
		$V_{DM} = 536\text{ V}$; $T_j = 150\text{ }^\circ\text{C}$; ($V_{DM} = 67\%$ of V_{DRM}); exponential waveform; gate open circuit		2000	-	-	V/ μs
dI_{com}/dt	rate of change of commutating current	$V_D = 400\text{ V}$; $T_j = 125\text{ }^\circ\text{C}$; $I_{T(RMS)} = 30\text{ A}$; $dV_{com}/dt = 20\text{ V}/\mu\text{s}$; (snubberless condition); gate open circuit		20	-	-	A/ms
		$V_D = 400\text{ V}$; $T_j = 150\text{ }^\circ\text{C}$; $I_{T(RMS)} = 30\text{ A}$; $dV_{com}/dt = 20\text{ V}/\mu\text{s}$; (snubberless condition); gate open circuit		15	-	-	A/ms

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1		 sym051
2	T2	main terminal 2		
3	G	gate		
mb	T2	mounting base; main terminal 2		

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
BTA330B-800BT	TO263	BTA330B-800BTJ	Reel	800	TO263E	26-May-2017

7. Marking

Table 4. Marking codes

Type number	Marking codes
BTA330B-800BT	BTA330B-800BT

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage			-	800	V
$I_{\text{T(RMS)}}$	RMS on-state current	full sine wave; $T_{\text{mb}} \leq 120\text{ }^{\circ}\text{C}$; Fig. 1 ; Fig. 2 ; Fig. 3		-	30	A
I_{TSM}	non-repetitive peak on-state current	full sine wave; $T_{\text{J(init)}} = 25\text{ }^{\circ}\text{C}$; $t_{\text{p}} = 20\text{ ms}$; Fig. 4 ; Fig. 5		-	270	A
		full sine wave; $T_{\text{J(init)}} = 25\text{ }^{\circ}\text{C}$; $t_{\text{p}} = 16.7\text{ ms}$		-	297	A
I^2t	I^2t for fusing	$t_{\text{p}} = 10\text{ ms}$; sine wave pulse		-	364.5	A^2s
dI_{T}/dt	rate of rise of on-state current	$I_{\text{G}} = 70\text{ mA}$		-	100	$\text{A}/\mu\text{s}$
I_{GM}	peak gate current			-	2	A
P_{GM}	peak gate power			-	5	W
$P_{\text{G(AV)}}$	average gate power	over any 20 ms period		-	0.5	W
T_{stg}	storage temperature			-40	150	$^{\circ}\text{C}$
T_{J}	junction temperature			-	150	$^{\circ}\text{C}$

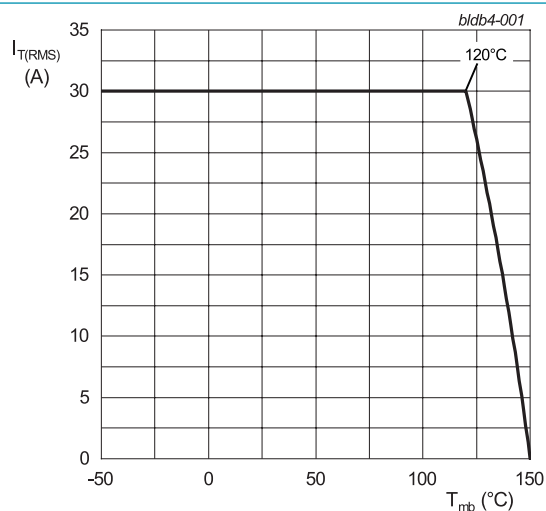
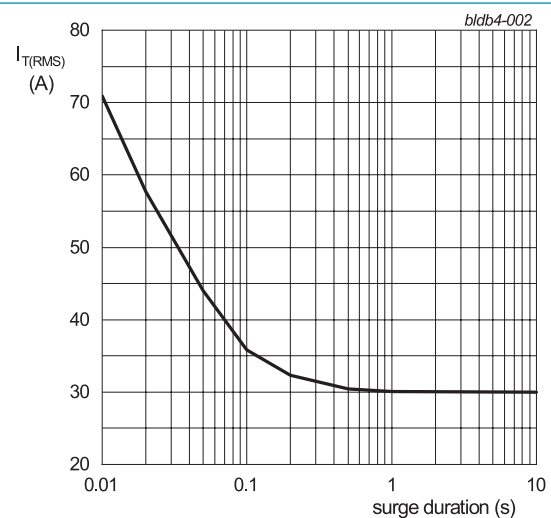


Fig. 1. RMS on-state current as a function of mounting base temperature; maximum values



$f = 50\text{ Hz}$; $T_{\text{mb}} = 120\text{ }^{\circ}\text{C}$

Fig. 2. RMS on-state current as a function of surge duration; maximum values

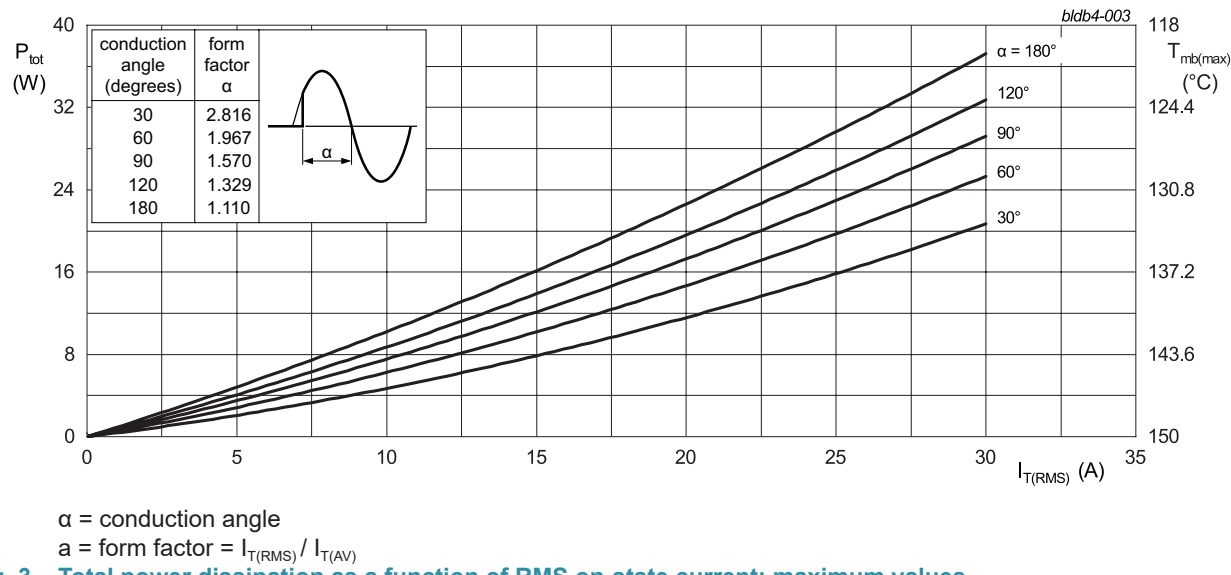


Fig. 3. Total power dissipation as a function of RMS on-state current; maximum values

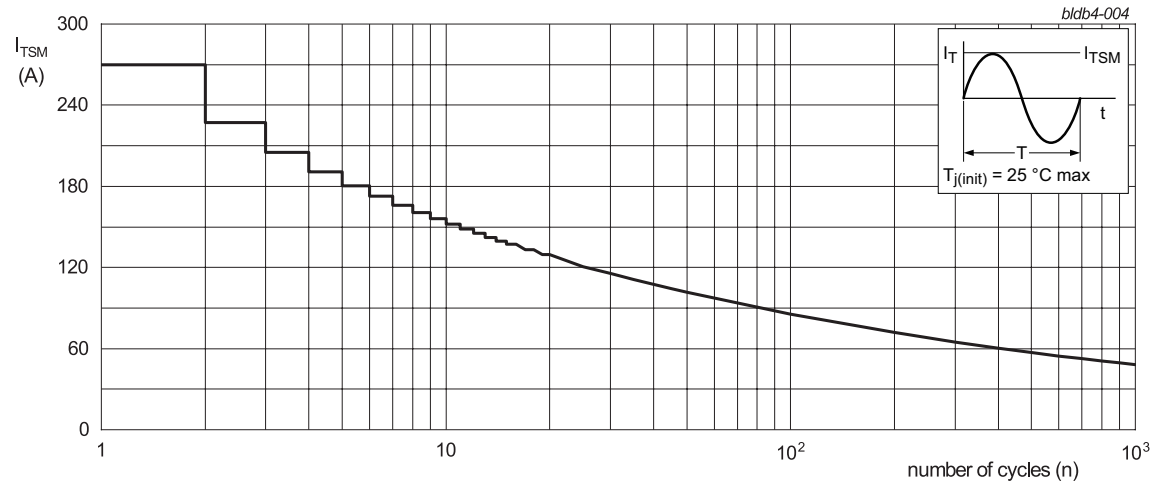


Fig. 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values

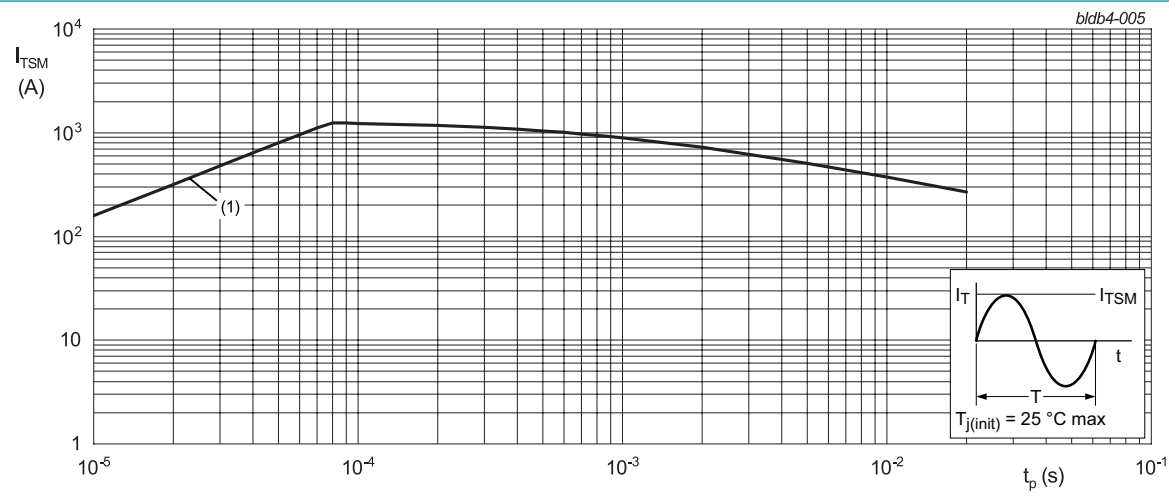


Fig. 5. Non-repetitive peak on-state current as a function of pulse duration; maximum values

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	full cycle; Fig 6	-	-	0.8	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	printed circuit board mounted; minimum footprint	-	55	-	K/W

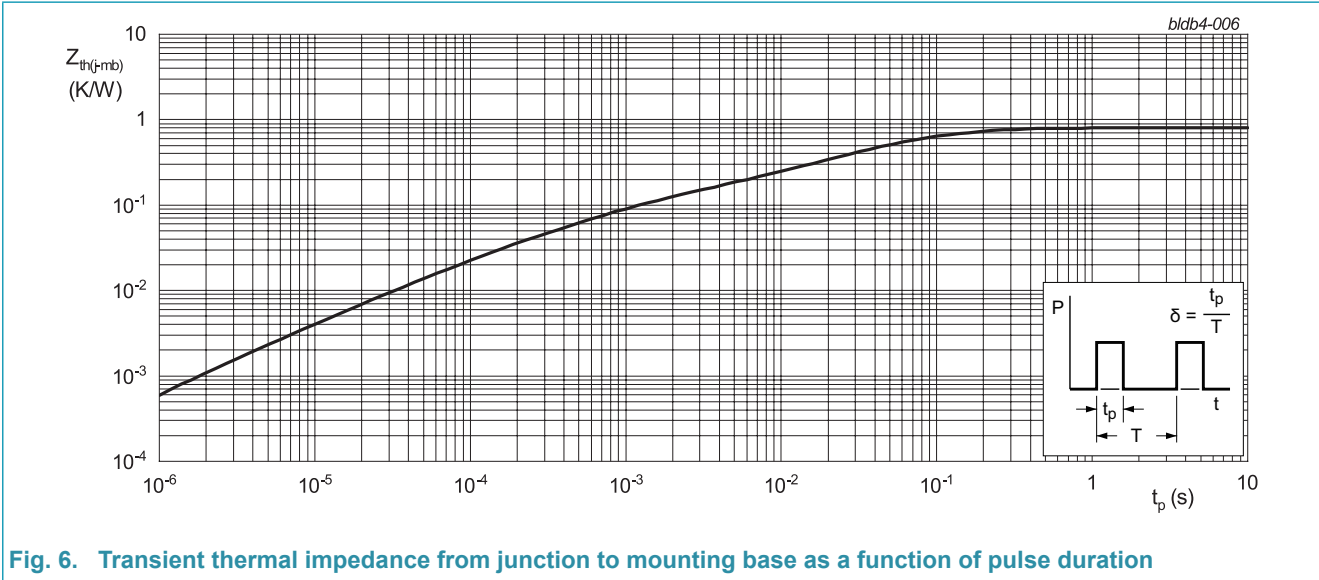
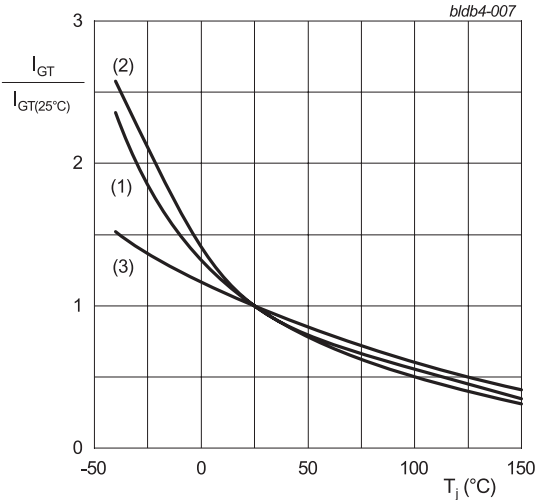


Fig. 6. Transient thermal impedance from junction to mounting base as a function of pulse duration

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
I _{GT}	gate trigger current	V _D = 12 V; I _T = 0.1 A; T2+ G+; T _J = 25 °C; Fig. 7		-	-	50	mA
		V _D = 12 V; I _T = 0.1 A; T2+ G-; T _J = 25 °C; Fig. 7		-	-	50	mA
		V _D = 12 V; I _T = 0.1 A; T2- G-; T _J = 25 °C; Fig. 7		-	-	50	mA
I _L	latching current	V _D = 12 V; I _G = 0.1 A; T2+ G+; T _J = 25 °C; Fig. 8		-	-	80	mA
		V _D = 12 V; I _G = 0.1 A; T2+ G-; T _J = 25 °C; Fig. 8		-	-	100	mA
		V _D = 12 V; I _G = 0.1 A; T2- G-; T _J = 25 °C; Fig. 8		-	-	80	mA
I _H	holding current	V _D = 12 V; T _J = 25 °C; Fig. 9		-	-	75	mA
V _T	on-state voltage	I _T = 42 A; T _J = 25 °C; Fig. 10		-	1.2	1.5	V
V _{GT}	gate trigger voltage	V _D = 12 V; I _T = 0.1 A; T _J = 25 °C; Fig. 11		-	0.9	1.3	V
		V _D = 400 V; I _T = 0.1 A; T _J = 150 °C		0.2	0.45	-	V
I _D	off-state current	V _D = 800 V; T _J = 25 °C		-	-	10	μA
		V _D = 800 V; T _J = 125 °C		-	0.4	2	mA
Dynamic characteristics							
dV _D /dt	rate of rise of off-state voltage	V _{DM} = 536 V; T _J = 125 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit		4000	-	-	V/μs
		V _{DM} = 536 V; T _J = 150 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit		2000	-	-	V/μs
dI _{com} /dt	rate of change of commutating current	V _D = 400 V; T _J = 125 °C; I _{T(RMS)} = 30 A; dV _{com} /dt = 20 V/μs; (snubberless condition); gate open circuit		20	-	-	A/ms
		V _D = 400 V; T _J = 150 °C; I _{T(RMS)} = 30 A; dV _{com} /dt = 20 V/μs; (snubberless condition); gate open circuit		15	-	-	A/ms



- (1) T2- G-
- (2) T2+ G-
- (3) T2+ G+

Fig. 7. Normalized gate trigger current as a function of junction temperature

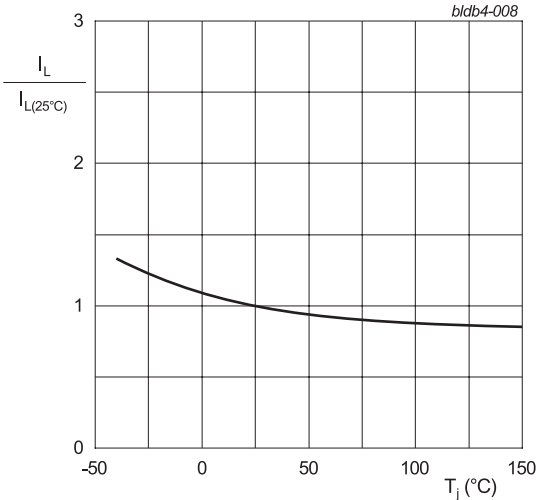


Fig. 8. Normalized latching current as a function of junction temperature

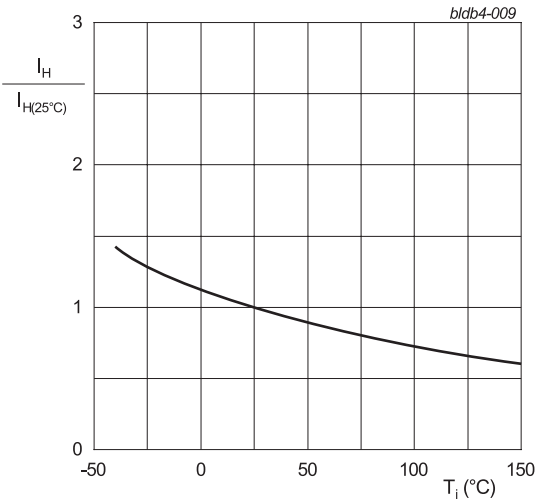
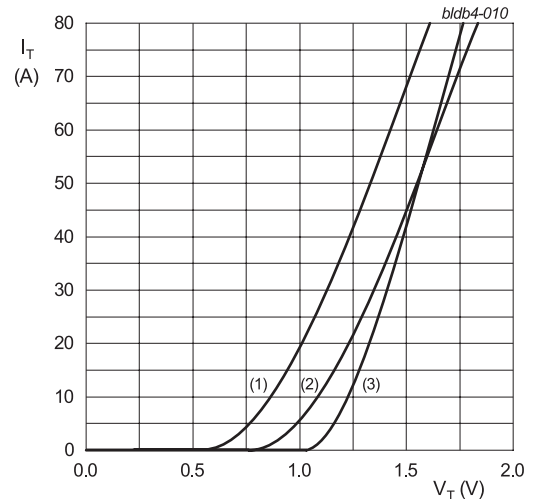


Fig. 9. Normalized holding current as a function of junction temperature



- $V_o = 1.011\text{ V}; R_s = 0.0110\text{ }\Omega$
(1) $T_j = 150^{\circ}\text{C}$; typical values
(2) $T_j = 150^{\circ}\text{C}$; maximum values
(3) $T_j = 25^{\circ}\text{C}$; maximum values

Fig. 10. On-state current as a function of on-state voltage

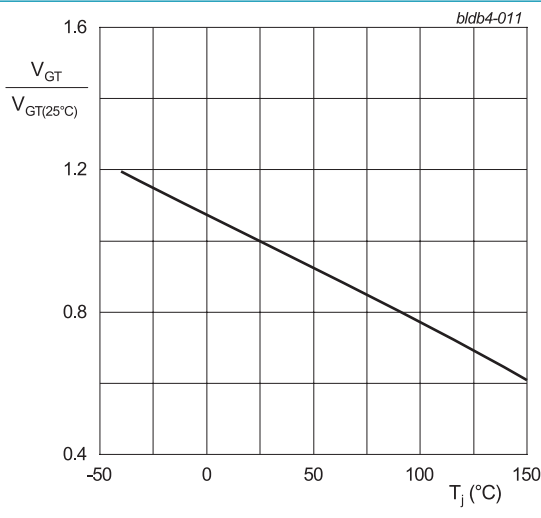
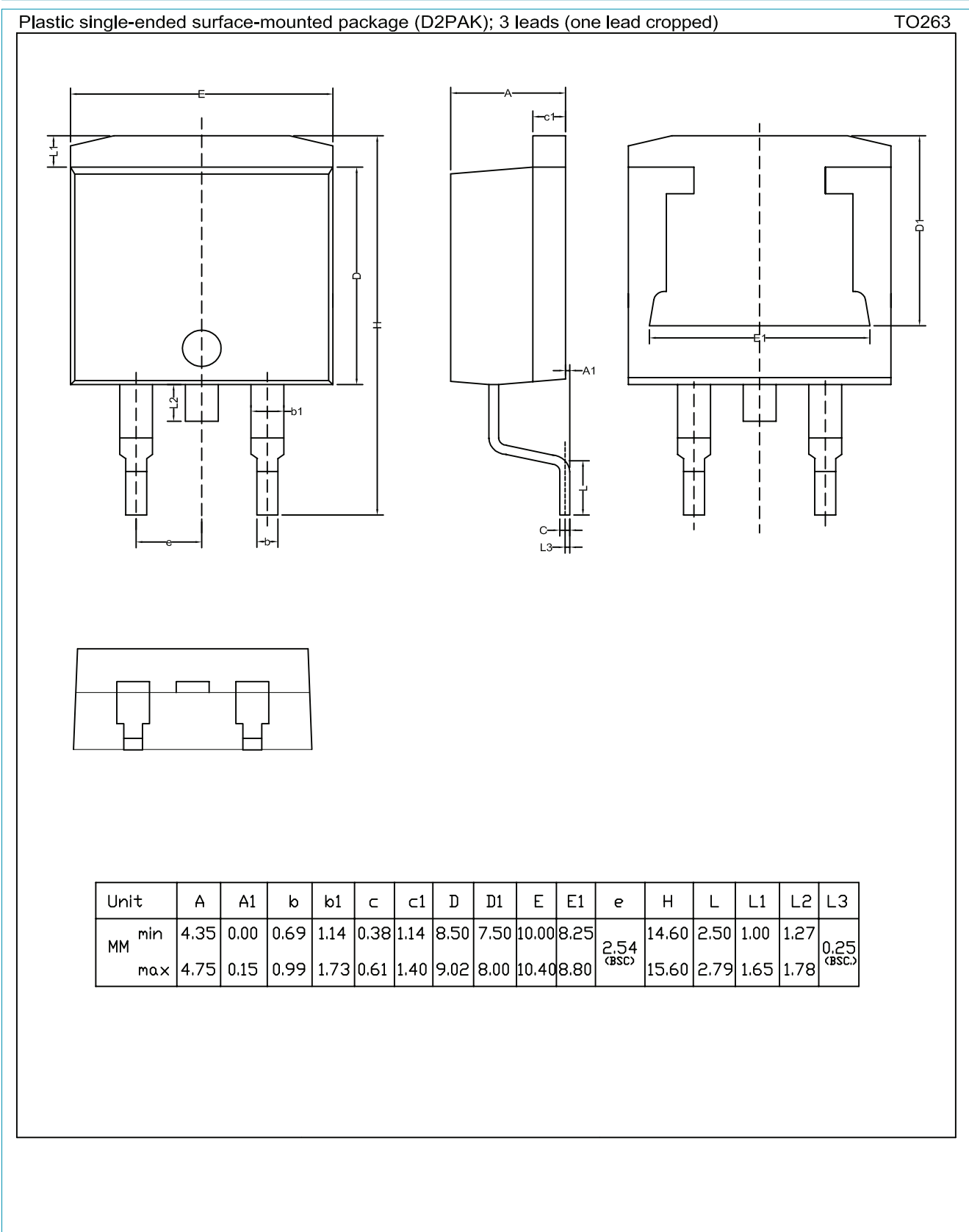


Fig. 11. Normalized gate trigger voltage as a function of junction temperature

11. Package outline



12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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