

Ultra-low Quiescent Current ($I_Q = 0.3 \mu A$), 300 mA, Buck-Boost DC/DC Converter Evaluation Board

No. EEV-415-Z331B-190212

RP604Z331B-EV is the evaluation board for RP604 which has the below features, benefits and specifications.

OVERVIEW

The RP604Z is a buck-boost converter featuring a minimum supply current and a high efficiency at low-load. The device operates at the low operating quiescent current ($I_Q = 0.3 \mu A$) to make the most of battery life for the battery driver operated intermittently.

KEY BENEFITS

- The low supply current ($I_Q = 0.3 \mu A$) can achieve making battery life longer and battery's size-reduction.
- Wide range of input voltage (1.8 V to 5.5 V) can support for every batteries from a coin-type battery to a USB port.

KEY SPECIFICATIONS

- Input Voltage: 1.8 V to 5.5 V
- Output Voltage: 1.6 V to 5.2 V, 0.1 V step
- Output Voltage Accuracy: $\pm 1.5\%$
- Maximum Output Current: 300 mA at Buck
- Built-in Driver On-resistance ($V_{IN} = 3.6 V$): PMOS = Typ. 0.12 Ω , NMOS = Typ. 0.12 Ω
- Operating Quiescent Current (I_Q): 0.3 μA
- Standby Current: 0.01 μA
- Protection Features: UVLO, OVP, LX Peak Current, and Thermal Shutdown
- For more details on RP604 IC, please refer to <https://www.e-devices.ricoh.co.jp/en/products/power/dcdc/rp604/rp604-ea.pdf>.

Part Number Information

Product Name	Package
RP604Zxx1\$	WLCSP-20-P2

xx: Specify the set output voltage (V_{SET}).

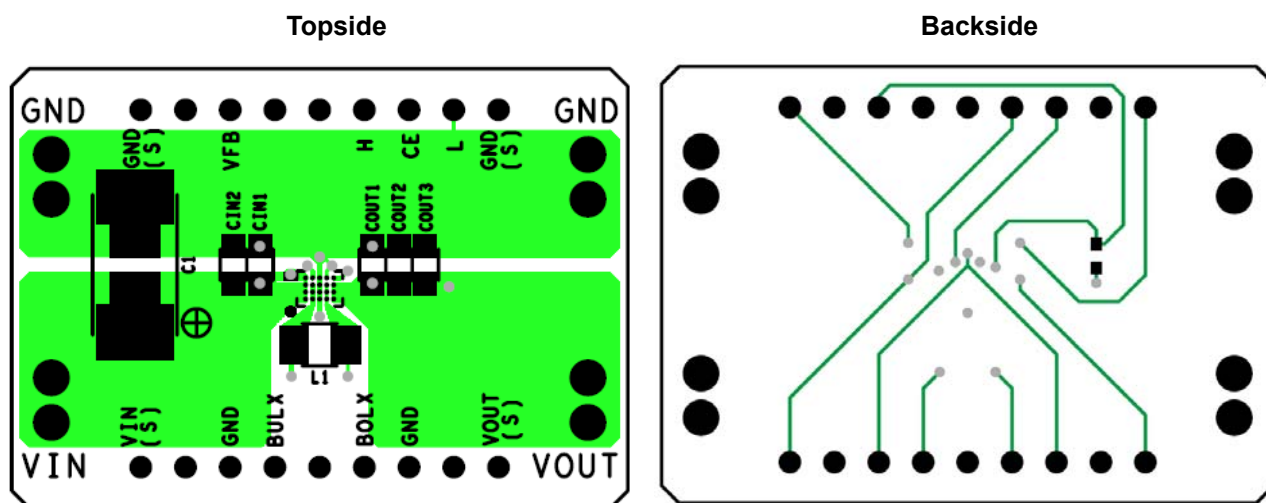
xx: Fixed Output Voltage Type , 33: $V_{SET} = 3.3\text{ V}$

\$: Specify the auto-discharge function.

Version	Auto-discharge Function
B	Enable

PCB LAYOUT

RP604Z (Package: WLCSP-20-P2) PCB Layout



ABSOLUTE MAXIMUM RATINGS

Absolute Maximum Ratings

(GND = 0 V)

Symbol	Parameter			Rating	Unit
V_{IN}	A/PVIN Pin Voltage			-0.3 to 6.5	V
V_{BULX}	BULX Pin Voltage			-0.3 to $V_{IN} + 0.3$	V
V_{BOLX}	BOLX Pin Voltage			-0.3 to $V_{OUT} + 0.3$	V
V_{CE}	CE Pin Voltage			-0.3 to 6.5	V
V_{OUT}	VOUT Pin Voltage			-0.3 to 6.5	V
V_{FB}	VFB Pin Voltage			-0.3 to 6.5	V
I_{LX}	BULX/BOLX Pin Output Current			900	mA
P_D	Power Dissipation ⁽¹⁾	WLCSP-20-P2	JEDEC STD. 51-9	1490	mW
T_j	Junction Temperature Range			-40 to 125	°C
T_{stg}	Storage Temperature Range			-55 to 125	°C

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings are not assured.

RECOMMENDED OPERATING CONDITIONS

Recommended Operating Conditions

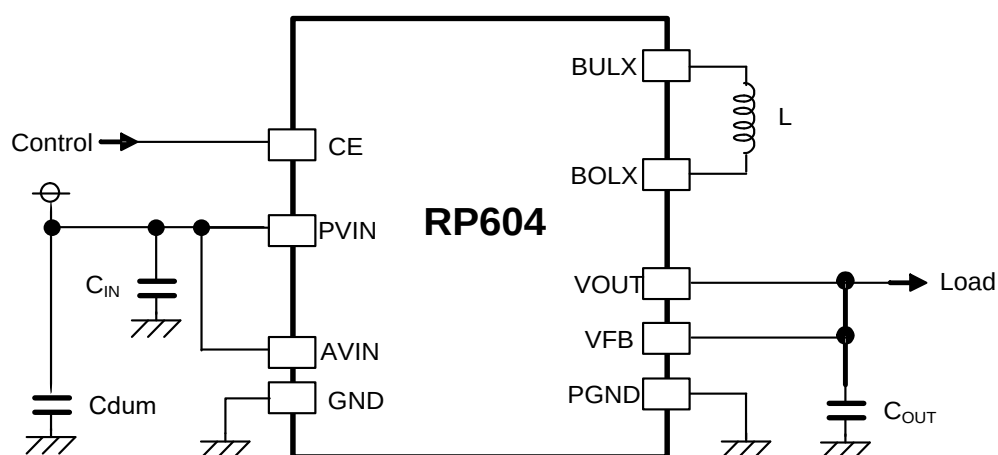
Symbol	Parameter	Rating	Unit
V_{IN}	Input Voltage	1.8 to 5.5	V
T_a	Operating Temperature Range	-40 to 85	°C

RECOMMENDED OPERATING CONDITIONS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

⁽¹⁾ Refer to *POWER DISSIPATION* for detailed information.

TYPICAL APPLICATION



RP604 Typical Application Circuit

※Testing with this EV board, an external attachment might be necessary for evaluation of the correct performance of the RP604 and already has been attached as Cdum.
For evaluation, wiring for power supply or GND will be used. Considering the voltage drop or noise by the wiring, Cdum has been mounted on the EV board to obtain the right performance of the RP604 in the actual PCB layout or measurement unit's wire is very short, and Cdum will be unnecessary.

Recommended External Components*1

Symbol	Value
L	2.2 μ H
C _{IN}	10 μ F
C _{OUT}	22 μ F
C _{dum}	10 μ F $\times$ 2

*1 The bill of materials will be attached on the shipment of each purchased evaluation board.

PIN DESCRIPTIONS



WLCSP-20-P2 Pin Configuration

WLCSP-20-P2 Pin Description

Pin No.	Pin Name	Description
A5, B5, C5	VOUT	Output Voltage Pin
A4, B4, C4	BOLX	Boost Switching Output Pin
A3, B3, C3, D3	PGND	Power GND Pin
A2, B2, C2	BULX	Buck Switching Output Pin
A1, B1, C1	PVIN	Power Input Voltage Pin
D1	AVIN	Analog Power Input Voltage Pin
D2	CE	Chip Enable Pin, Active-high
D4	AGND	Analog GND Pin
D5	VFB	Output Voltage Feedback Pin

TECHNICAL NOTES

The performance of a power source circuit using this device is highly dependent on a peripheral circuit. A peripheral component or the device mounted on PCB should not exceed a rated voltage, a rated current or a rated power. When designing a peripheral circuit, please be fully aware of the following points. Refer to *PCB Layout* below.

- When the built-in switches are turned off, the inductor may generate a spike-shaped high voltage. Use the high-breakdown voltage capacitor (C_{OUT}) which output voltage is 1.5 times or more than the set output voltage.
- Use an inductor that has a low DC resistance, has an enough tolerable current and is less likely to cause magnetic saturation. If the inductance value is extremely small, the peak current of L_X may increase. When the peak current of L_X reaches to the L_X limit current (I_{LXLIM}), overcurrent protection circuit starts to operate. When selecting the inductor, consider the maximum output current of L_X pin (I_{LXMAX}). For details, refer to *Calculation Method of Maximum Output Current of L_X Pin (I_{LXMAX}) in Continuous Mode* on the RP604 IC data sheet.
- When an intermediate voltage other than V_{IN} or GND is input to the CE pin, a supply current may be increased with a through current of a logic circuit in the IC. The CE pin is neither pulled up nor pulled down, therefore an operation is not stable at open.



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Sales & Support Offices

Ricoh Electronic Devices Co., Ltd.

Shin-Yokohama Office (International Sales)

2-3, Shin-Yokohama 3-chome, Kohoku-ku, Yokohama-shi, Kanagawa, 222-8530, Japan
Phone: +81-50-3814-7687 Fax: +81-45-474-0074

Ricoh Americas Holdings, Inc.

675 Campbell Technology Parkway, Suite 200 Campbell, CA 95008, U.S.A.
Phone: +1-408-610-3105

Ricoh Europe (Netherlands) B.V.

Semiconductor Support Centre

Prof. W.H. Keesomlaan 1, 1183 DJ Amstelveen, The Netherlands
Phone: +31-20-5474-309

Ricoh International B.V. - German Branch

Semiconductor Sales and Support Centre

Oberrather Strasse 6, 40472 Düsseldorf, Germany
Phone: +49-211-6546-0

Ricoh Electronic Devices Korea Co., Ltd.

3F, Haesung Bldg, 504, Teheran-ro, Gangnam-gu, Seoul, 135-725, Korea
Phone: +82-2-2135-5700 Fax: +82-2-2051-5713

Ricoh Electronic Devices Shanghai Co., Ltd.

Room 403, No.2 Building, No.690 Bibo Road, Pu Dong New District, Shanghai 201203,
People's Republic of China
Phone: +86-21-5027-3200 Fax: +86-21-5027-3299

Ricoh Electronic Devices Shanghai Co., Ltd.

Shenzhen Branch

1205, Block D(Jinlong Building), Kingkey 100, Hongbao Road, Luohu District,
Shenzhen, China
Phone: +86-755-8348-7600 Ext 225

Ricoh Electronic Devices Co., Ltd.

Taipei office

Room 109, 10F-1, No.51, Hengyang Rd., Taipei City, Taiwan
Phone: +886-2-2313-1621/1622 Fax: +886-2-2313-1623

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