LTC3636EUFD
Dual 6A, 20V Monolithic Synchronous Step-Down Regulator

DESCRIPTION

Demonstration circuit DC2335A is a dual output regulator consisting of two constant-frequency step-down converters, based on the LTC®3636 monolithic dual channel synchronous buck regulator. The DC2335A has an input voltage range of 3.1V to 20V, with each regulator capable of delivering up to 6A of output current. The DC2335A can operate in either Burst Mode® operation or forced continuous mode. In shutdown, the DC2335A can run off of 13µA typical total input current at 12V input. The DC2335A is a very efficient circuit: up to 92% for 3.3V output at 6A load, 5V input. The DC2335A uses the 28-Pin QFN package LTC3636EUFD, which has exposed pads on the bottom side of the IC for better thermal performance. These features, plus a programmable operating frequency range from 500kHz to 4MHz (2MHz switching frequency with the RT pin connected to $INTV_{CC}$), make the DC2335A demo board an ideal circuit for use in industrial or distributed power applications.

Design files for this circuit board are available at http://www.linear.com/demo/DC2335A

PERFORMANCE SUMMARY

Specifications are at T_A = 25°C

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Input Voltage Range V_{IN1}, V_{IN2}		3.1		20	V
Output Voltage, V_{OUT1}	V_{IN1} = 3.1V to 20V, I_{OUT1} = 0A to 6A		Default: 1.5		V
Output Voltage, V_{OUT2}	V_{IN2} = 5V to 20V, I_{OUT2} = 0A to 6A		Default: 3.3		V
Maximum Output Current, I_{OUT1}, I_{OUT2}			6		A
V_{OUT1} Typical Efficiency	V_{IN1} = 5V, V_{OUT1} = 1.5V, I_{OUT1} = 6A V_{IN1} = 12V, V_{OUT1} = 1.5V, I_{OUT1} = 6A		87 86.5		% %
V_{OUT1} Peak Efficiency	V_{IN1} = 5V, V_{OUT1} = 1.5V V_{IN1} = 12V, V_{OUT1} = 1.5V		92.7 89.4		% %
V_{OUT2} Typical Efficiency	V_{IN2} = 5V, V_{OUT2} = 3.3V, I_{OUT2} = 6A V_{IN2} = 12V, V_{OUT2} = 3.3V, I_{OUT2} = 6A		92 91.6		% %
V_{OUT2} Peak Efficiency	V_{IN2} = 5V, V_{OUT2} = 3.3V V_{IN2} = 12V, V_{OUT2} = 3.3V		96.4 93.3		% %
Switching Frequency			1		MHz

Notes:

1. V_{IN} range for each output voltage needs to consider minimum t_{ON} and minimum t_{OFF}. Please refer to page 15 of LTC3636/LTC3636-1 data sheet for details.
2. The DC2335A offers three different output voltage options with a jumper selection for each output (V_{OUT1}, V_{OUT2}). Inductance of L1, L2 on the demo board is designed for default V_{OUT1} = 1.5V, V_{OUT2} = 3.3V respectively. For a specific application, please design the inductance to have around 2A ripple current for optimal performance.

QUICK START PROCEDURE

Demonstration circuit 2335A is easy to set up to evaluate the performance of the LTC3636. Refer to Figure 1 for the proper measurement equipment setup and follow the procedure below.

1. With power off, connect the input power supply to V_{IN1} and GND terminals. Note that V_{IN1} and V_{IN2} are same nodes shorted by R20 (0Ω) on the board.
2. Connect the output loads between V_{OUT1}, V_{OUT2} and GND (Initial load: no load). Refer to Figure 1 for proper setup.
3. Connect the DVMs to the input and output.
4. Check the default jumper/switch position: JP1, JP2: ON; JP3, JP4: SS; JP6: 1.5V; JP19: 3.3V; JP8: FCM.
5. Turn on the input power supply and adjust voltage from 0V to 12V.

   NOTE. Make sure that the input voltage does not exceed 20V.
6. Check for the proper output voltages from V_{OUT1} to GND (P5, P6), V_{OUT2} to GND (P7, P8).
7. Once the proper output voltage is established, adjust the loads within the operating range (0A to 6A) and observe the output voltage regulation, ripple voltage and other parameters.
8. When finished tests, power off the input power supply.

NOTES:

1. There is an option to disconnect V_{IN1} and V_{IN2} by removing R20. The LTC3636 internal control circuitry is powered from V_{IN1}. When V_{IN1} and V_{IN2} are not shorted together, the power on and power off should follow the sequence: power on V_{IN1} before V_{IN2} power-on, power off V_{IN1} after V_{IN2} power-off.
2. When measuring the output or input voltage ripple, do not use the long ground lead on the oscilloscope probe. See Figure 2 for the proper scope probe technique. Short, stiff leads need to be soldered to the (+) and (–) terminals of an output capacitor. The probe's ground ring needs to touch the (–) lead and the probe tip needs to touch the (+) lead.

QUICK START PROCEDURE

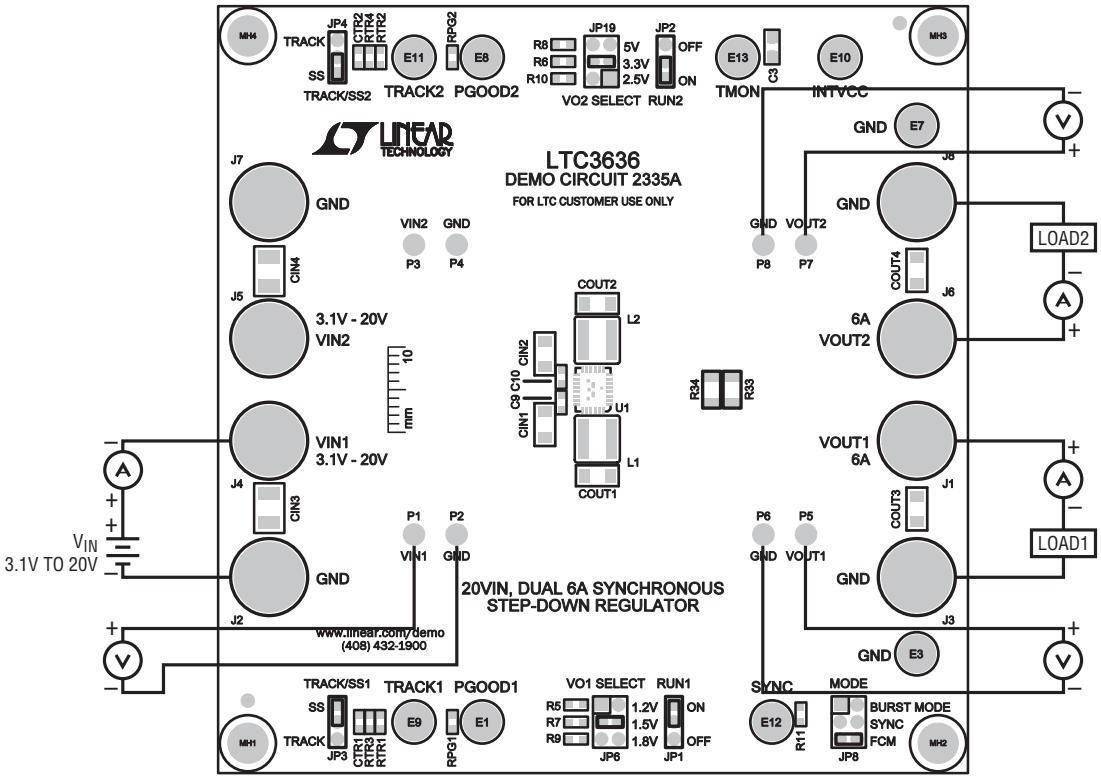


Figure 1. Proper Measurement Equipment Setup

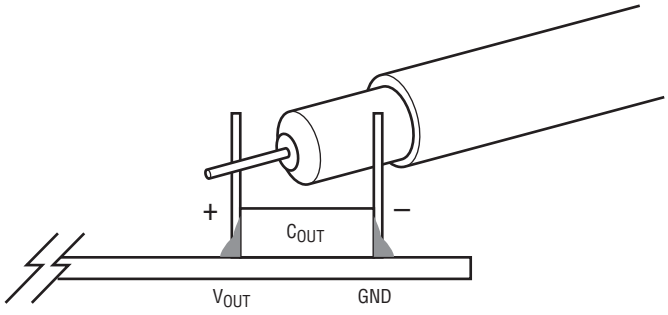


Figure 2. Measuring Output Voltage Ripple

QUICK START PROCEDURE

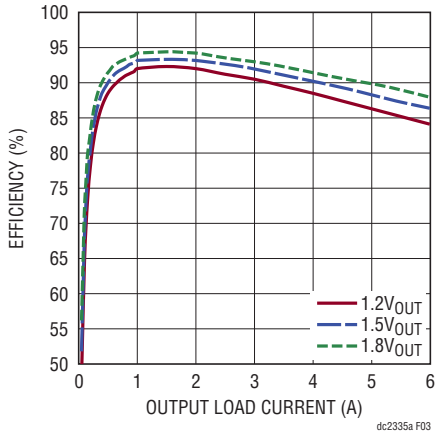


Figure 3. Efficiency vs Load Current at V_{IN1} = V_{IN2} = 3.3V, f_{SW} = 1MHz (FCM)

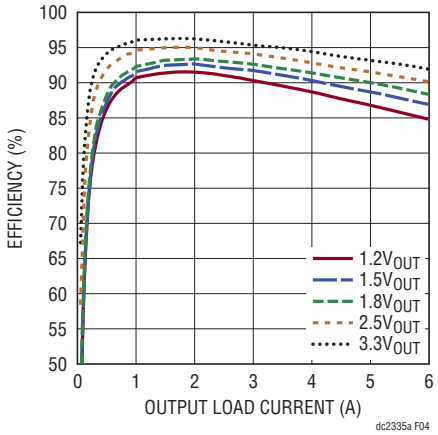


Figure 4. Efficiency vs Load Current at V_{IN1} = V_{IN2} = 5V, f_{SW} = 1MHz (FCM)

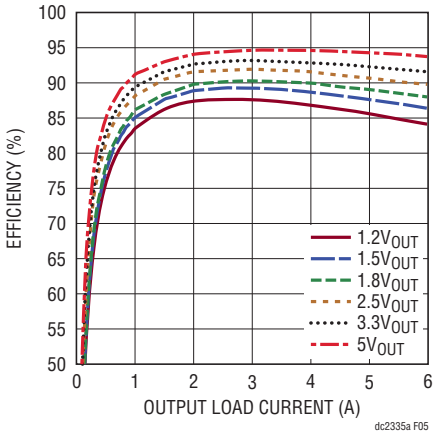


Figure 5. Efficiency vs Load Current at V_{IN1} = V_{IN2} = 12V, f_{SW} = 1MHz (FCM)

QUICK START PROCEDURE

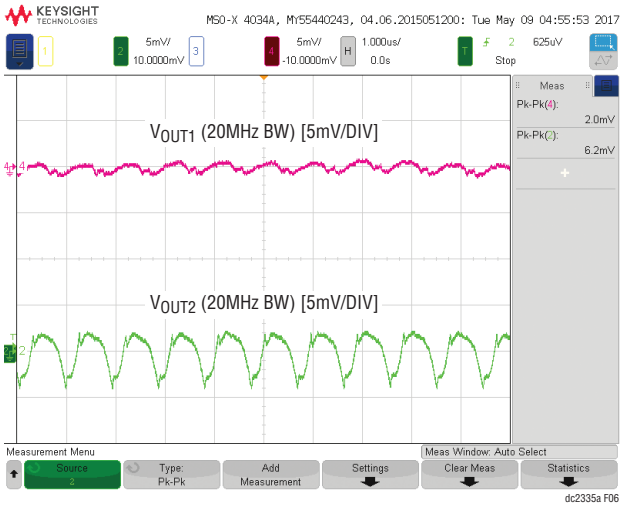


Figure 6. Output Voltage Ripple at $V_{IN1} = V_{IN2} = 12V$, $V_{OUT1} = 1.5V$, $V_{OUT2} = 3.3V$, $I_{OUT1} = I_{OUT2} = 6A$

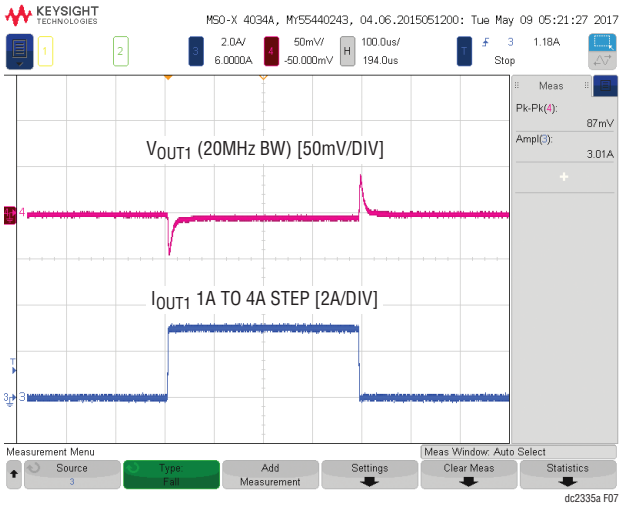


Figure 7. V_{OUT1} Transient Response at $V_{IN1} = 12V$, $V_{OUT1} = 1.5V$

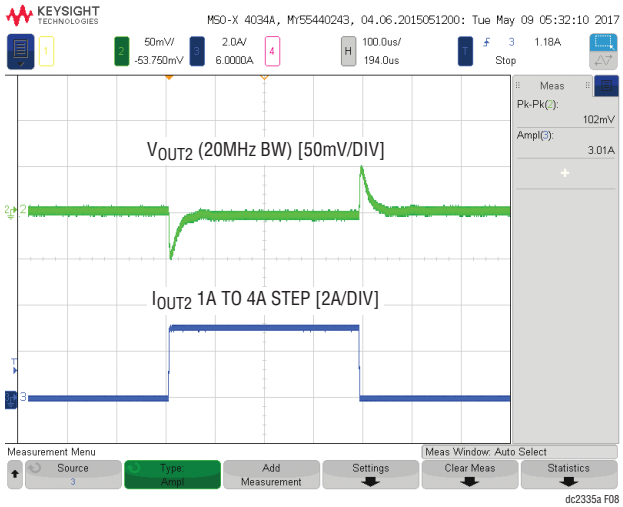


Figure 8. V_{OUT2} Transient Response at $V_{IN2} = 12V$, $V_{OUT2} = 3.3V$

QUICK START PROCEDURE

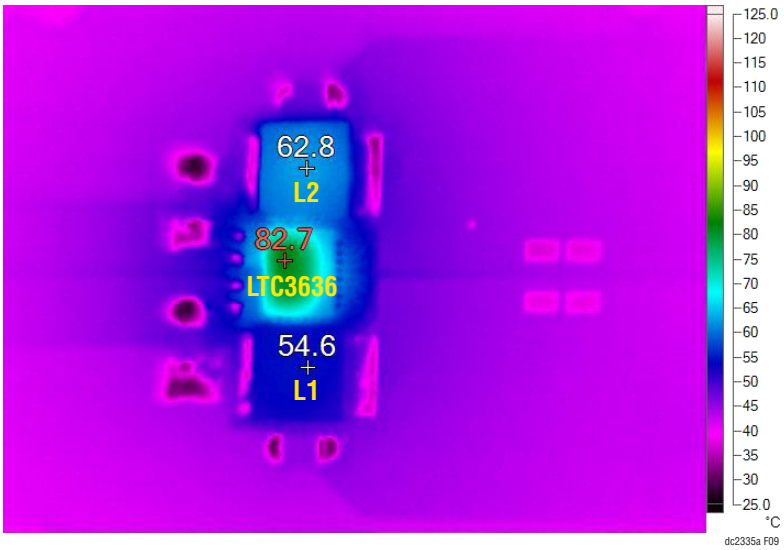


Figure 9. Thermal Performance at V_{IN1} = V_{IN2} = 12V, V_{OUT1} = 1.5V, V_{OUT2} = 3.3V, I_{OUT1} = I_{OUT2} = 6A, T_A = 25°C, No Airflow

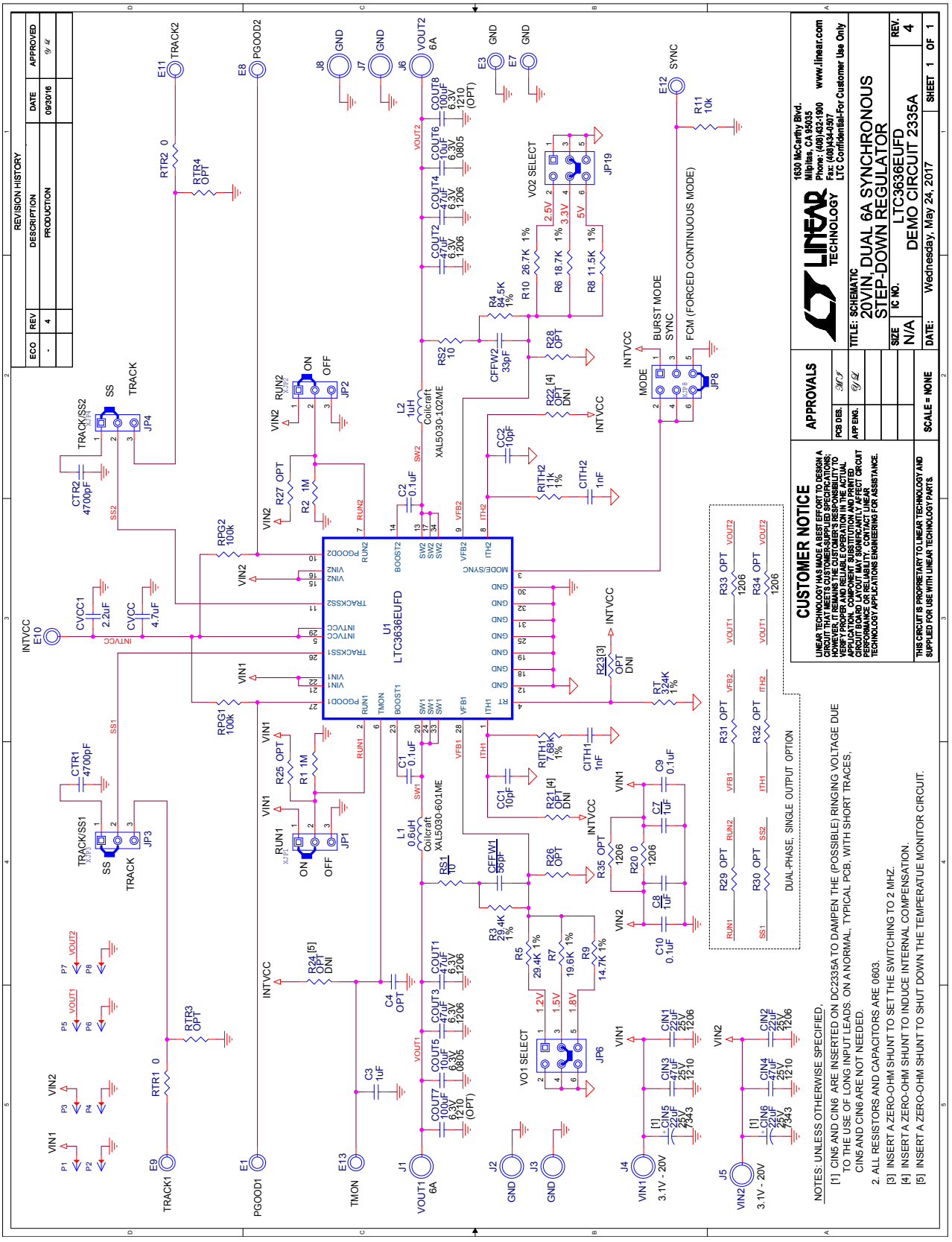
PARTS LIST

ITEM	QTY	REFERENCE	PART DESCRIPTION	MANUFACTURER/PART NUMBER
Required Circuit Components				
1	2	CC1, CC2	CAP, 0603 10pF 5% 50V C0G	NIC NMC0603NP0100J50TRPF
2	1	CFFW1	CAP., 56pF, C0G, 50V, 5%, 0603	MURATA GRM1885C1H560JA01D
3	1	CFFW2	CAP, 0603 33pF 10% 25V C0G	AVX 06033A330KAT2A
4	2	CIN1, CIN2	CAP, 1206 22µF 20% 25V X5R	MURATA GRM31CR61E226ME15L
5	2	CIN3, CIN4	CAP, 1210 47µF 10% 25V X7R	TAIYO YUDEN TMK325ABJ476MM-T
6	2	CIN5, CIN6	CAP, 7343 22µF 20% 25V POSCAP	PANASONIC 25TQC22MV
7	2	CITH1, CITH2	CAP, 0603 1nF 10% 50V X7R	MURATA GRM188R71H102KA01D
8	4	COUT1, COUT2, COUT3, COUT4	CAP, 1206 47µF 20% 6.3V X5R	TAIYO YUDEN TMK325ABJ476MM
9	2	COUT5, COUT6	CAP, 0805 10µF 20% 6.3V X5R	TDK C2012X5R0J106M
10	2	CTR1, CTR2	CAP, 0603 4700pF 10% 50V X7R	TDK C1608C0G1H472K
11	1	CVCC	CAP, 0603 4.7µF 10% 16V X5R	MURATA GRM188R61C475KAAJD
12	1	CVCC1	CAP, 0603 2.2µF 10% 16V X5R	MURATA GRM188R61C225KE15D
13	4	C1, C2, C9, C10	CAP, 0603 0.1µF 10% 50V X7R	TDK C1608X7R1H104K
14	3	C3, C7, C8	CAP, 0603 1µF 10% 25V X5R	TDK C1608X5R1E105K080AC
15	1	L1	IND, 0.6µH	COILCRAFT XAL5030-601ME
16	1	L2	IND, 1µH	COILCRAFT XAL5030-102ME
17	1	RITH1	RES, 0603 7.68k 1% 1/10W	VISHAY CRCW06037K68FKEA
18	1	RITH2	RES, 0603 11k 1% 1/10W	VISHAY CRCW060311K0FKEA
19	2	RPG1, RPG2	RES, 0603 100k 5% 1/10W	VISHAY CRCW0603100KJNEA
20	2	RS1, RS2	RES, 0603 10Ω 1% 0.1W	VISHAY CRCW060310R0FKEA
21	1	RT	RES, 0603 324k 1% 1/10W	VISHAY CRCW0603324KFKEA
22	2	RTR1, RTR2	RES, 0603 0Ω JUMPER	VISHAY CRCW06030000Z0EA
23	2	R1, R2	RES, 0603 1MΩ 5% 1/10W	PANASONIC ERJ-3GEYJ105V
24	2	R3, R5	RES, 0603 29.4k 1% 1/10W	VISHAY CRCW060329K4FKEA
25	1	R4	RES, 0603 84.5k 1% 1/10W	VISHAY CRCW060384K5FKEA
26	1	R6	RES, 0603 18.7k 1% 1/10W	VISHAY CRCW060318K7FKEA
27	1	R7	RES, 0603 19.6k 1% 1/10W	VISHAY CRCW060319K6FKEA
28	1	R8	RES, 0603 11.5k 1% 1/10W	VISHAY CRCW060311K5FKEA
29	1	R9	RES, 0603 14.7k 1% 1/10W	VISHAY CRCW060314K7FKEA
30	1	R10	RES, 0603 26.7k 1% 1/10W	VISHAY CRCW060326K7FKEA
31	1	R11	RES, 0603 10k 5% 1/10W	VISHAY CRCW060310K0JNEA
32	1	R20	RES, 1206 0Ω JUMPER	VISHAY CRCW12060000Z0EAH
33	1	U1	IC, MONOLITHIC SYNCHRONOUS BUCK REGULATOR	LINEAR TECH. LTC3636EUFD

PARTS LIST

Additional Demo Board Circuit Components				
1	0	COUT7, COUT8	CAP, 1210 100µF 20% 6.3V X5R OPTION	TDK C3225X5R0J107M OPTION
2	0	C4, C5, C6	CAP, 0603 OPTION	OPTION
3	0	RTR3, RTR4, R21, R22, R23, R24, R25, R26, R27, R28, R29, R30, R31, R32	RES, 0603 OPTION	OPTION
4	0	R33, R34, R35	RES, 1206 OPTION	OPTION
Hardware: For Demo Board Only				
1	9	E1, E3, E7, E8, E9, E10, E11, E12, E13	TURRET	MILL-MAX 2501-2-00-80-00-00-07-0
2	4	JP1, JP2, JP3, JP4	HEADER, 3PIN, 2mm	WURTH 620 003 111 21
3	3	JP6, JP8, JP19	HEADER, 3PIN, DBL ROW 2mm	SAMTEC TMM 103-02-L-D
4	8	J1, J2, J3, J4, J5, J6, J7, J8	JACK, BANANA	KEYSTONE 575-4
5	4	MH1, MH2, MH3, MH4	STANDOFF, SNAP ON	KEYSTONE 8834
6	8	P1, P2, P3, P4, P5, P6, P7, P8	CONNECTOR, SWAGE MOUNT	KEYSTONE 1425-2

SCHEMATIC DIAGRAM



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Mailing Address:

Linear Technology
1630 McCarthy Blvd.
Milpitas, CA 95035

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