

Evaluating the ADL8140 GaAs, pHEMT, MMIC, Low Noise Amplifier, 10 GHz to 18 GHz

FEATURES

- ▶ 4-layer, Rogers 4003C and Isola 370HR evaluation board
- ▶ End launch, 2.9 mm RF connectors
- ▶ Through calibration path (depopulated)

EVALUATION KIT CONTENTS

- ▶ ADL8140-EVALZ evaluation board

EQUIPMENT NEEDED

- ▶ RF signal generator
- ▶ RF spectrum analyzer
- ▶ RF network analyzer
- ▶ 2 V, 100 mA power supply

GENERAL DESCRIPTION

The ADL8140-EVALZ is a 4-layer printed circuit board (PCB) fabricated from 8 mil thick, Rogers 4003C and Isola 370HR, copper clad, forming a nominal thickness of 62 mils. The RFIN and RFOUT ports on the ADL8140-EVALZ are populated with female coaxial 2.9 mm connectors, and the corresponding RF traces have a 50 Ω characteristic impedance. The ADL8140-EVALZ is populated with components suitable for use over the entire -55°C to $+125^{\circ}\text{C}$ operating temperature range of the [ADL8140](#). To calibrate board trace losses, a through calibration path is provided between the J1 and J2 connectors. J1 and J2 must be populated with 2.9 mm RF connectors to use the through calibration path. Refer to [Table 1](#) and [Figure 3](#) for the through calibration path performance.

Connect ground and power to ADL8140-EVALZ using the surface-mount technology (SMT) test point connectors, GND and VDD. A supplementary test point for VBIAS is included for simple access on the RBIAS pin (see [Figure 6](#) for the test point assembly).

The package ground leads and the exposed pad connect directly to the ground plane. Multiple vias connect the top and bottom ground planes with particular focus on the area directly beneath the ground paddle to provide adequate electrical conduction and thermal conduction.

The power supply decoupling capacitors on the ADL8140-EVALZ represent the configuration used to characterize and qualify the device.

For full details on the ADL8140, see the ADL8140 data sheet, which must be consulted in conjunction with this user guide when using the ADL8140-EVALZ evaluation board.

EVALUATION BOARD PHOTOGRAPHS

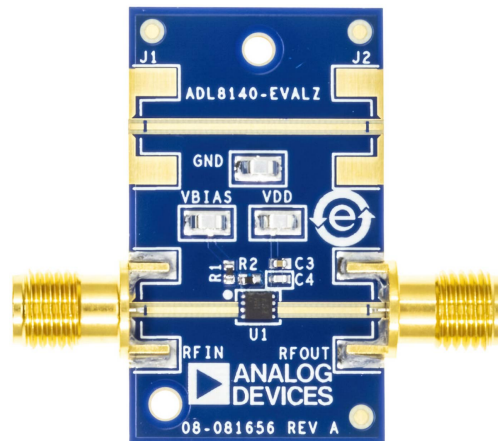


Figure 1. ADL8140-EVALZ Top

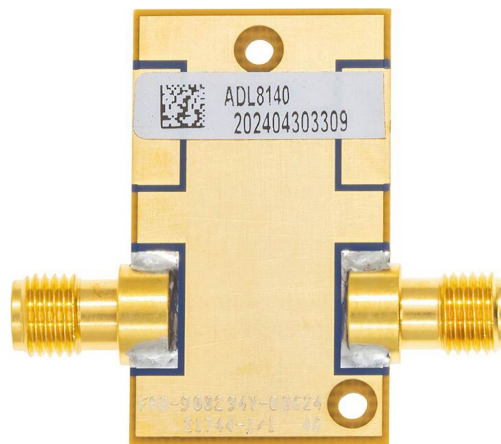


Figure 2. ADL8140-EVALZ Bottom

TABLE OF CONTENTS

Features.....	1	Operating the ADL8140-EVALZ.....	3
Evaluation Kit Contents.....	1	Recommended Bias Sequencing.....	3
Equipment Needed.....	1	Evaluation Board Schematic and Artwork.....	5
General Description.....	1	Ordering Information.....	6
Evaluation Board Photographs.....	1	Bill of Materials.....	6

REVISION HISTORY

7/2024—Revision 0: Initial Version

OPERATING THE ADL8140-EVALZ

A 2 V, 100 mA power supply is required to provide the bias to the ADL8140 when using the ADL8140-EVALZ. Connect the 2 V power supply to the SMT test point, VDD. Connect the ground reference to the GND test point. Apply the RF power after VDD is turned on, and remove the RF power before VDD is turned off.

Refer to the ADL8140 data sheet for the recommended resistor values to achieve different supply currents. The following bias conditions are recommended to achieve optimal performance: VDD = 1.5 V and quiescent current (IDQ) = 35 mA. IDQ is typically achieved using a 562 Ω value for the bias resistance (RBIAS).

Figure 3. Insertion Loss and Return Loss of the Through Calibration Path, 10 GHz to 18 GHz

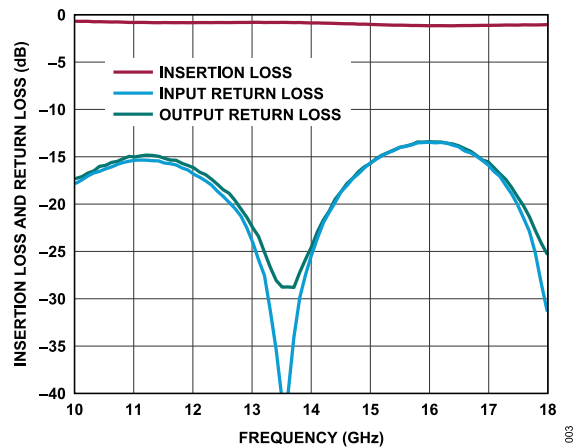


Table 1. Insertion Loss and Return Loss of the Through Calibration Path

Frequency (GHz)	Insertion Loss (dB)
10	-0.69
10.5	-0.75
11	-0.81
12	-0.83
12.5	-0.82
13	-0.80
13.5	-0.80
14	-0.87
14.5	-0.94
15	-1.0
15.5	-1.1
16	-1.2
16.5	-1.2
17	-1.1
17.5	-1.1
18	-1.0

RECOMMENDED BIAS SEQUENCING

During Power-Up

To power up the ADL8140-EVALZ, take the following bias sequencing steps:

- 1. Connect the VDD power supply.
- 2. Set the VDD supply to 1.5 V.
- 3. Apply the RF input signal.

OPERATING THE ADL8140-EVALZ

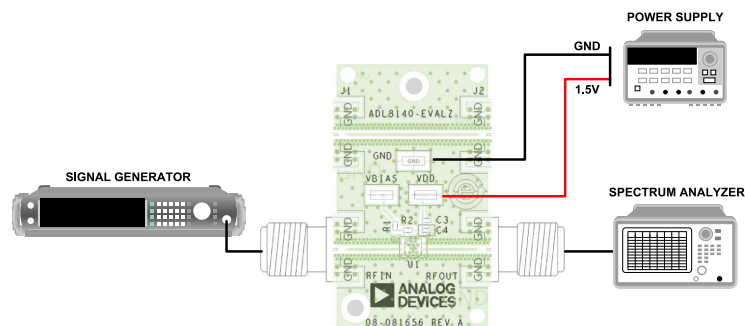


Figure 4. ADL8140-EVALZ Operating Block Diagram

During Power-Down

To power down the ADL8140-EVALZ, take the following bias sequencing steps:

1. Turn off the RF input signal.
2. Set the VDD supply to 0 V.

EVALUATION BOARD SCHEMATIC AND ARTWORK

The bias current (see [Figure 5](#)) can either be set by placing a resistor in position R2 or by placing a resistor in position R1 and by applying an adjustable voltage to the VBIAS pin (in this case R2 must be open).

Figure 5. ADL8140-EVALZ Evaluation Board Schematic

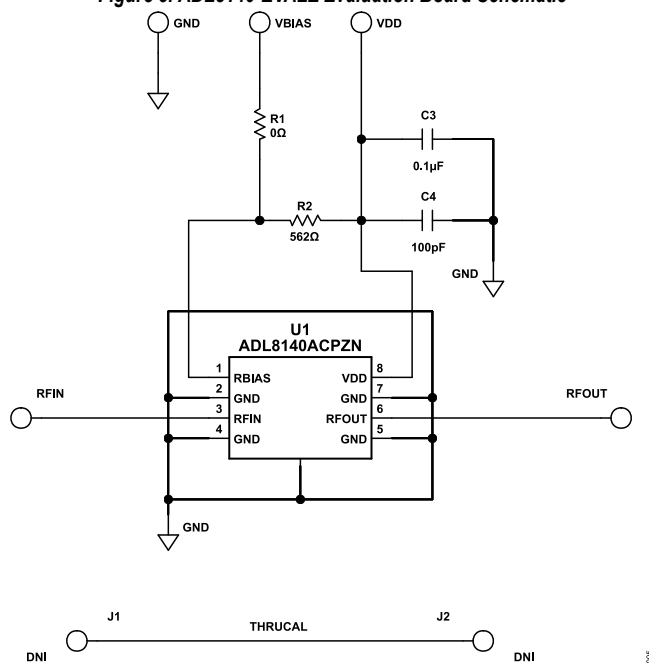
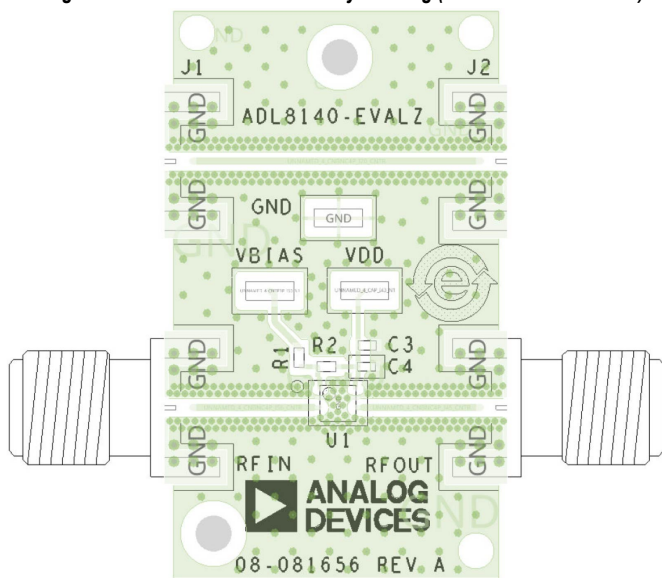


Figure 6. ADL8140-EVALZ Assembly Drawing (J1 and J2 Not Installed)




ORDERING INFORMATION

BILL OF MATERIALS

Table 2. Bill of Materials

Reference Designator	Description	Manufacturer	Part Number
C3	0.1 µF ceramic capacitor, 16 V, 10%, X7R, 0402	Samsung	CL05B104K05NNNC
C4	100 pF ceramic capacitor, 50 V, 5%, C0G, 0402, extreme low equivalent series resistance (ESR)	Kemet	C0402C101J5GACTU
GND, VBIAS, VDD	Connector, printed circuit boards (PCBs), surface-mounted technology (SMT) test points	Keystone Electronics	5015
R2	Resistor SMD, 562 Ω, 1%, 1/10 W, 0402	Panasonic	ERJ-2RKF5620X
RFIN, RFOUT	Connector PCBs, 2.9 mm coax for frequency test measurements, 50 Ω	SRI Connector Gage Co.	25-146-1000-92
J1, J2	Connector PCBs, 2.9 mm coax for frequency test measurements, 50 Ω, (do not install (DNI))	SRI Connector Gage Co.	25-146-1000-92
R1	Resistor SMD, 0 Ω jumper, 1/10 W, 0402 (do not install (DNI))	Panasonic	ERJ-2GE0R00X
U1	10 GHz to 18 GHz Low Noise Amplifier	Analog Devices, Inc.	ADL8140ACPZN



ESD Caution
ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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