

## DATA SHEET

# SKY67189-396LF: 400 to 6000 MHz Broadband Low-Noise Amplifier

## Applications

- 2G/3G/4G/5G TDD and FDD systems
- Active antenna array and massive MIMO
- Receive LNA for micro-cell, macro-cell, and small-cell base stations
- Land mobile radios and military communications
- Low-noise broadband gain block and driver amplifier

## Features

- Excellent broadband flat gain performance
- Low noise figure
- High IP3 performance over voltage
- Minimal external components
- Low current I<sub>DD</sub>: 50 mA @ 5.0 V
- Fast rise/fall time ENABLE function suitable for 5G TDD applications
- Temperature and process-stable active bias up to +115 °C
- Miniature DFN (8-pin, 2 x 2 mm) package (MSL1 @ 260 °C per JEDEC J-STD-020)



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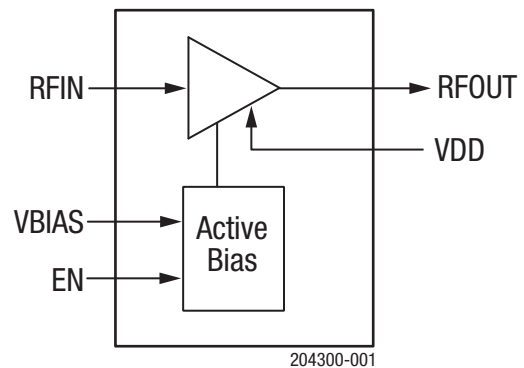


Figure 1. SKY67189-396LF Block Diagram

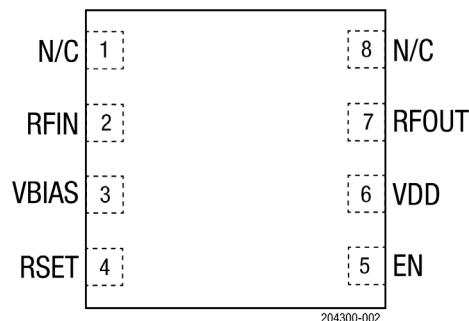
## Description

The SKY67189-396LF is a wide-band low-noise amplifier with superior gain flatness and exceptional linearity.

The compact 2 x 2 mm, 8-pin Dual Flat No Lead packaged LNA is designed for 2G/3G/4G/5G TDD and FDD infrastructure systems operating from 400 MHz to 6 GHz.

The internal active bias circuitry provides stable performance over temperature and process variation.

A functional block diagram is shown in Figure 1. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.



**Figure 2. SKY67189-396LF Pinout  
(Top View)**

**Table 1. SKY67189-396LF Signal Descriptions**

Pin	Name	Description	Pin	Name	Description
1	N/C	No connection (may be connected to ground with no change in performance)	5	EN	Enable voltage to LNA
2	RFIN	RF input (DC blocking capacitor required)	6	VDD	VDD voltage to LNA
3	VBIAS	Bias voltage for input gate	7	RFOUT	RF output. DC blocking capacitor is required.
4	RSET	External resistor to set the bias current	8	N/C	No connection (may be connected to ground with no change in performance)

## Electrical and Mechanical Specifications

The absolute maximum ratings of the SKY67189-396LF are provided in Table 2.

Electrical specifications are provided in Tables 3 and 4.

**Table 2. SKY67189-396LF Absolute Maximum Ratings<sup>1</sup>**

Parameter	Symbol	Minimum	Maximum	Units
Supply voltage	VDD	0	5.5	V
Quiescent supply current	I <sub>DD</sub>		100	mA
RF input power (C/W)	P <sub>IN</sub>		+22	dBm
Storage temperature	T <sub>STG</sub>	-40	+150	°C
Operating temperature	T <sub>A</sub>	-40	+115	°C
Junction temperature	T <sub>J</sub>		+150	°C
Electrostatic discharge:	ESD			
Charged Device Model (CDM)			1000	V
Human Body Model (HBM)			250	V

<sup>1</sup> Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

**ESD HANDLING:** Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. This device must be protected at all times from ESD when handling or transporting. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD handling precautions should be used at all times.

**Table 3. SKY67189-396LF Electrical Specifications: Thermal Data<sup>1</sup>****(V<sub>DD</sub> = 5.0 V, Enable = GND, T<sub>A</sub> = +25 °C, P<sub>IN</sub> = -20 dBm, Characteristic Impedance [Z<sub>0</sub>] = 50 Ω, Unless Otherwise Noted)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Thermal resistance	θ <sub>JC</sub>			56.9		°C/W
Channel temperature @ +115 °C reference (package heat slug)	T <sub>J</sub>	V <sub>DD</sub> = 5.0 V, I <sub>bq</sub> = 56 mA, no RF applied		131		°C

<sup>1</sup> Performance is guaranteed only under the conditions listed in this table.**Table 4. SKY67189-396LF Electrical Specifications: 2500 to 6000 MHz Optimized Tuning<sup>1</sup>****(V<sub>DD</sub> = 5.0 V, Enable = GND, T<sub>A</sub> = +25 °C, P<sub>IN</sub> = -20 dBm, Characteristic Impedance [Z<sub>0</sub>] = 50 Ω, Unless Otherwise Noted)**

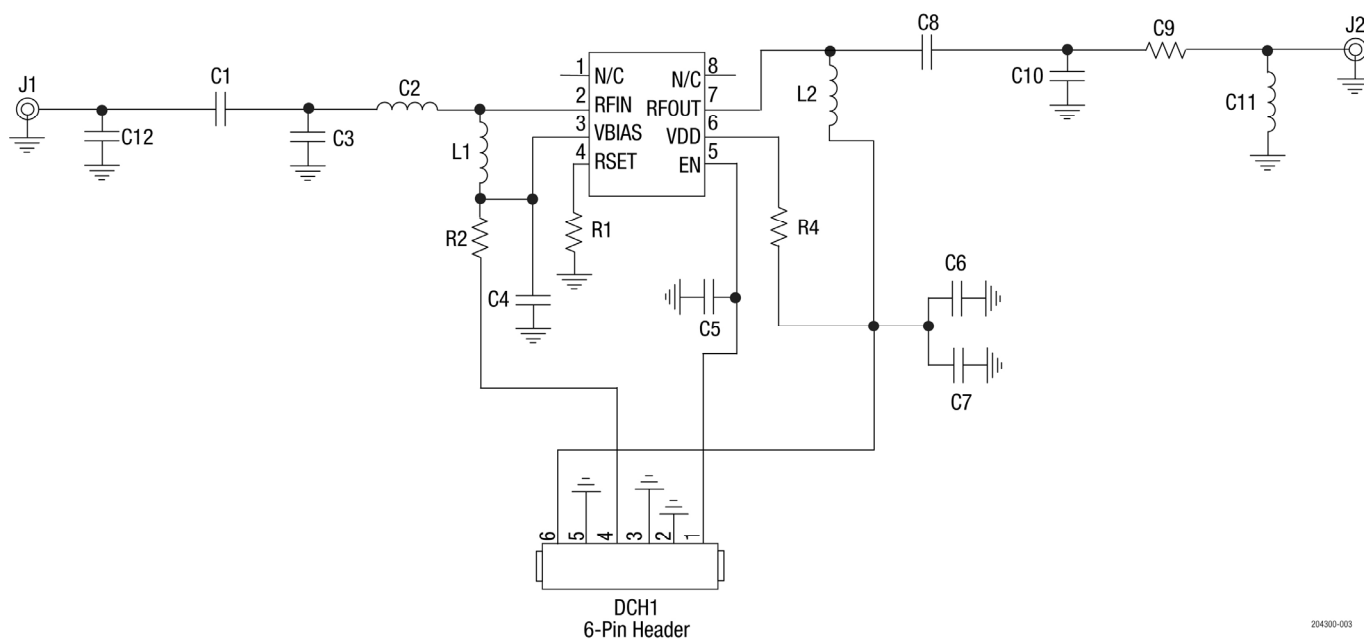
Parameter	Symbol	Test Condition	Min	Typ	Max	Units
<b>RF Specifications</b>						
Noise figure	NF	@ 3300 MHz @ 3800 MHz @ 4400 MHz @ 5000 MHz		0.75 0.65 0.65 0.75	1 0.9 1 1	dB dB dB dB
Gain	S <sub>21</sub>	@ 3300 MHz @ 3800 MHz @ 4400 MHz @ 5000 MHz	16	18.6 18.3 17.8 17.3		dB dB dB dB
Input return loss	S <sub>11</sub>	@ 3300 MHz @ 3800 MHz @ 4400 MHz @ 5000 MHz		15 15 14 18		dB dB dB dB
Output return loss	S <sub>22</sub>	@ 3300 MHz @ 3800 MHz @ 4400 MHz @ 5000 MHz		12 12 10 9		dB dB dB dB
Third order output intercept point	OIP3	P <sub>IN</sub> = -25 dBm, Δ Tone = 2 MHz @ 4400 MHz: @ 3300 MHz @ 3800 MHz @ 4400 MHz @ 5000 MHz	+24	+29 +28 +27 +27		dBm dBm dBm dBm
1 dB output compression point	OP1dB	@ 3300 MHz @ 3800 MHz @ 4400 MHz @ 5000 MHz	+15	+19 +19 +19 +19		dBm dBm dBm dBm
<b>DC Specifications</b>						
Supply voltage	V <sub>DD</sub>			5.0		V
Quiescent current	I <sub>DD</sub>			52	60	mA
Settling time 0.3 dB <sup>2</sup> Settling time 0.1 dB <sup>3</sup>	t <sub>ON</sub>	@ 4400 MHz		0.2 0.2	1 1	us us

<sup>1</sup> Performance is guaranteed only under the conditions listed in this table.<sup>2</sup> Settling time 0.3 dB is measured from the time the PA enable reaches 50% of PA enable "on" level to the time at which the RF output power achieves within 0.3 dB of the average steady-state "on" level.<sup>3</sup> Settling time 0.1 dB is measured from the time the PA enable reaches 50% of PA enable "on" level to the time at which the RF output power achieves within 0.1 dB of the average steady-state "on" level.

## Evaluation Board Description

The SKY67189-396LF Evaluation Board is used to test the performance of the SKY67189-396LF LNA. An Evaluation Board schematic (optimized for 2500 to 6000 MHz tuning) diagram is provided in Figure 3.

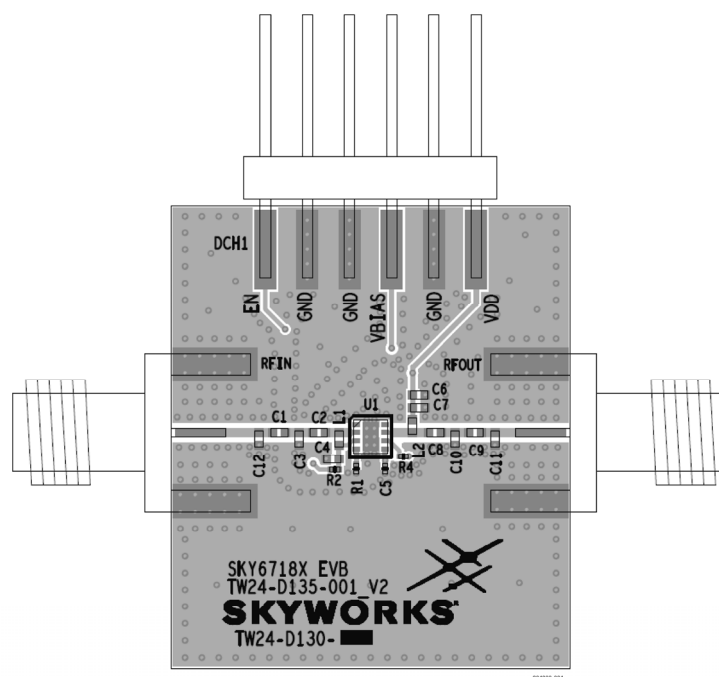
Table 5 lists the Bill of Materials (BOM) for the Evaluation Board optimized for 2500 to 6000 MHz tuning. An EVB assembly diagram is shown in Figure 4. EVB layout detail information is provided in Figure 5.



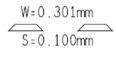
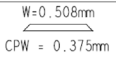
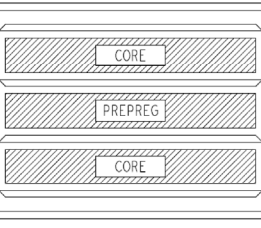
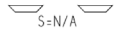
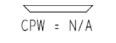
**Figure 3. SKY67189-396LF Evaluation Board Schematic for 2500 to 6000 MHz Tuning**

**Table 5. SKY67189-396LF Evaluation Board Bill of Materials (BOM) for 2500 to 6000 MHz Tuning**

Component	Description	Value	Size	Manufacturer	Part Number
C1	Capacitor	4.3 pF	0402	muRata	GJM series
C2	Inductor	0.4 nH	0402	TDK	MLG series
C3	Capacitor	0.4 pF	0402	muRata	GJM series
C4	DNI				
C5	DNI				
C6	Capacitor	5.6 pF	0402	muRata	GRM series
C7	Capacitor	4.7 uF	0402	muRata	GRM series
C8	Capacitor	2.2 pF	0402	muRata	GRM series
C9	Resistor	0 $\Omega$	0402		
C10	DNI				
C11	DNI				
C12	DNI				
L1	Inductor	6.8 nH	0402	muRata	LQW series
L2	Inductor	2.4 nH	0402	TDK	MLG series
R1	Resistor	5.1 k $\Omega$ (1%)	0201		
R2	DNI				
R4	Resistor	20 $\Omega$	0201		



**Figure 4. SKY67189-396LF EVB Assembly Diagram**

50-OHM TRACE	50-OHM TRACE	CROSS SECTION	NAME	THICKNESS	MATERIALS
			TMASK	0.020mm	SOLDER RESIST
			L1	0.047mm	FINISHED Cu.
			DIELECTRIC	0.254mm	RO4350B
			L2	0.018mm	Cu-0.5oz.
			DIELECTRIC	0.900mm	FR4 (4.34)
			L3	0.018mm	Cu-0.5oz.
			DIELECTRIC	0.254mm	FR4 (4.34)
			L4	0.047mm	FINISHED Cu.
			BMASK	0.020mm	SOLDER RESIST
			TOTAL THICKNESS	1.578mm	TOL: +/- 10%

204300-005

Figure 5. SKY67189-396LF EVB Layer Details

## Package Dimensions

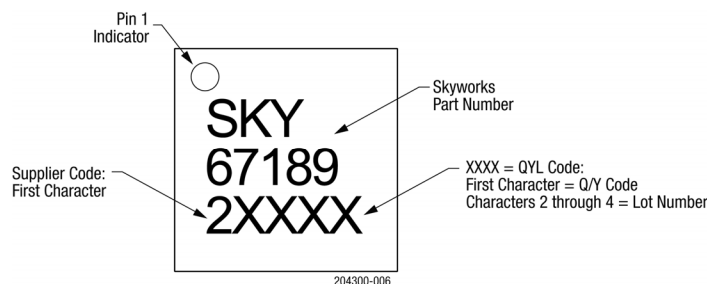
A typical part marking for the SKY67189-396LF is shown in Figure 6. The PCB layout footprint for the SKY67189-396LF is provided in Figure 7. Package dimensions are shown in Figure 8. Tape and reel dimensions are shown in Figure 9.

## Package and Handling Information

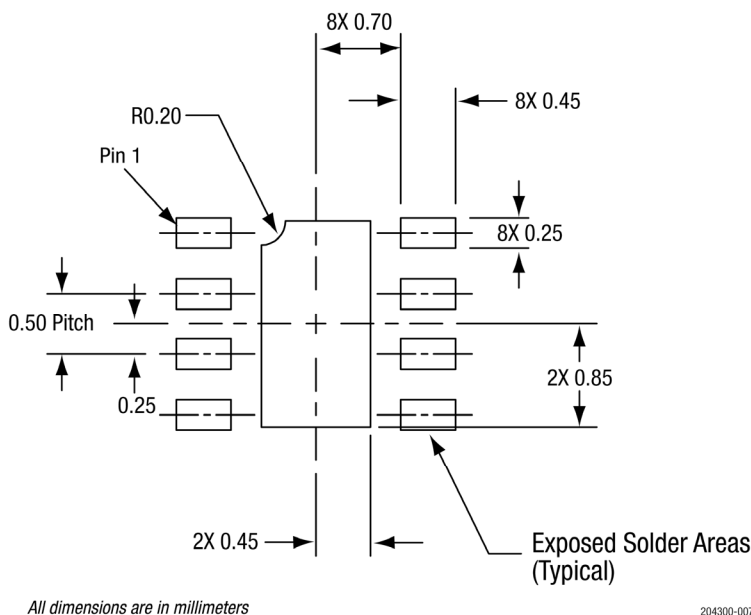
Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SKY67189-396LF is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164.

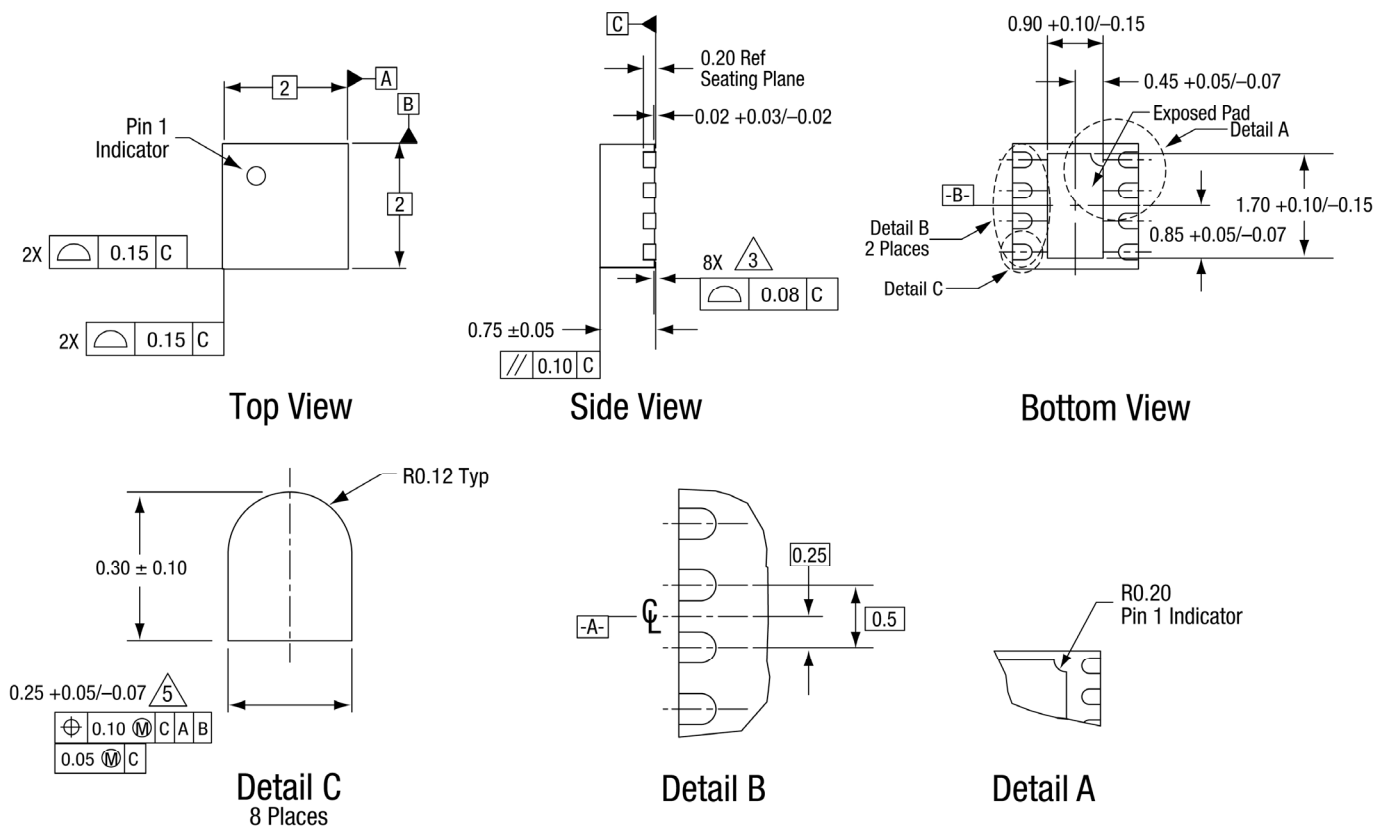
Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.



**Figure 6. SKY67189-396LF Typical Part Marking**



**Figure 7. SKY67189-396LF PCB Layout Footprint**



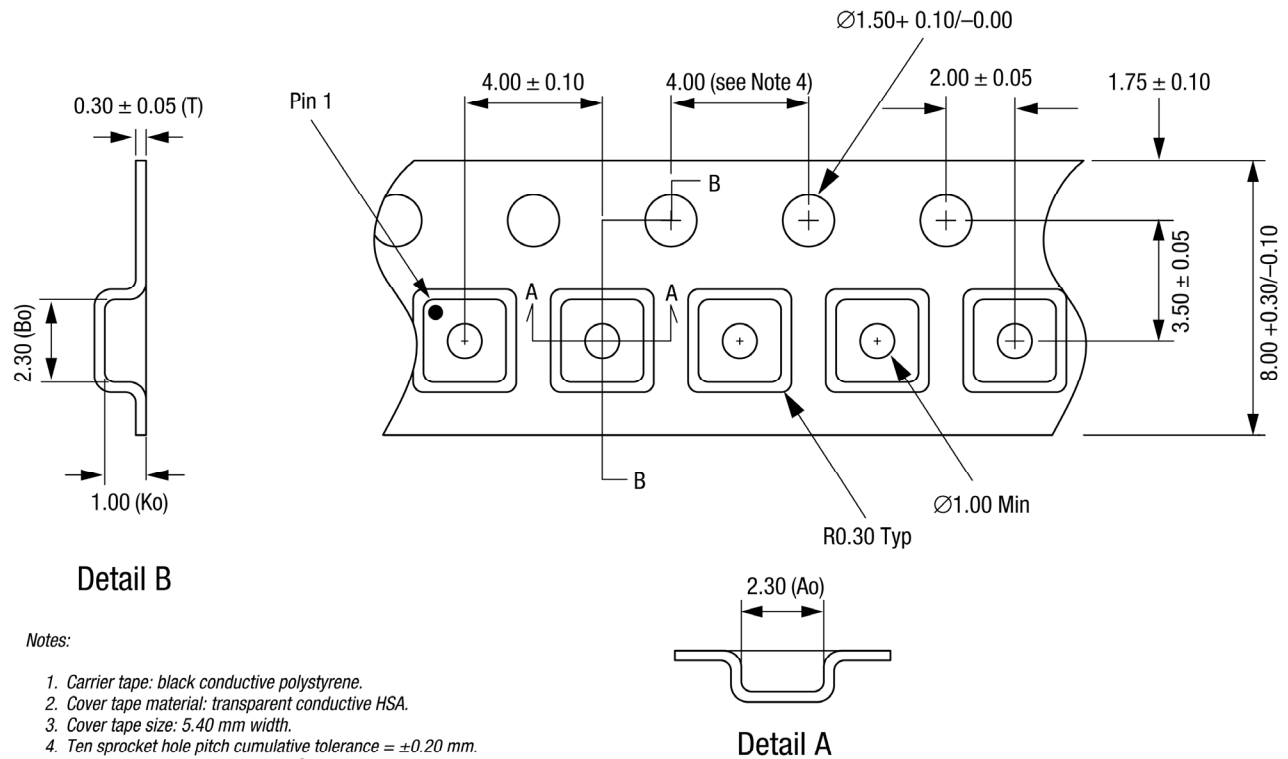
Notes:

1. All measurements are in millimeters.
2. Dimensions and tolerances according to ASME Y14.5M-1994.
3. Coplanarity applies to the exposed heat sink ground pad as well as the terminals.
4. Plating requirement per source control drawing (SCD) 2504.
5. Dimension applies to metallized terminal and is measured between 0.15 mm and 0.30 mm from terminal tip.

204300-008

Figure 8. SKY67189-396LF Package Dimensions





204300-009

**Figure 9. SKY67189-396LF Tape and Reel Dimensions**

## Ordering Information

Part Number	Product Description	Evaluation Board Part Number
SKY67189-396LF	400 to 6000 MHz Broadband LNA	SKY67189-396LFEK1 (2.5 to 6.0 GHz Tuning)

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