

MOSFET - Power, Single N-Channel, TOLL 80 V, 1.1 mΩ, 299 A NVBLS1D2N08X

Features

- Low Q_{RR} , Soft Recovery Body Diode
- Low $R_{DS(on)}$ to Minimize Conduction Losses
- Low Q_G and Capacitance to Minimize Driver Losses
- AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Typical Applications

- Synchronous Rectification (SR) in DC-DC and AC-DC
- Primary Switch in Isolated DC-DC Converter
- Motor Drives
- Automotive 48 V System

MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

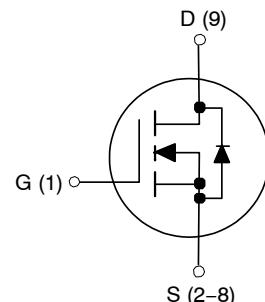
Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	80	V
Gate-to-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	299	A
		211	
Power Dissipation	P_D	197	W
Pulsed Drain Current	I_{DM}	1941	A
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to +175	°C
Source Current (Body Diode)	I_S	332	A
Single Pulse Avalanche Energy ($I_{PK} = 94$ A)	E_{AS}	441	mJ
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)	T_L	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
2. Actual continuous current will be limited by thermal & electromechanical application board design.
3. E_{AS} is based on started $T_J = 25^\circ\text{C}$, rated I_{AS} , $V_{DD} = 64$ V, $V_{GS} = 10$ V, 100% avalanche tested.

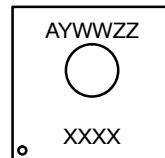
$V_{(BR)DSS}$	$R_{DS(ON)} \text{ MAX}$	$I_D \text{ MAX}$
80 V	1.1 mΩ @ $V_{GS} = 10$ V	299 A

N-CHANNEL MOSFET



H-PSOF8L
CASE 100CU

MARKING DIAGRAM



A = Assembly Location
 Y = Year
 WW = Work Week
 ZZ = Assembly Lot Code
 XXXX = Specific Device Code

ORDERING INFORMATION

Device	Package	Shipping [†]
NVBLS1D2N08XTXG	H-PSOF8L (Pb-Free)	TBD / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

NVBLS1D2N08X

Table 1. THERMAL CHARACTERISTICS

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.76	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	30	

Table 2. ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit			
OFF CHARACTERISTICS									
Drain-to-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0 \text{ V}$, $I_D = 1 \text{ mA}$, $T_J = 25^{\circ}\text{C}$	80			V			
Drain-to-Source Breakdown Voltage Temperature Coefficient	$\Delta V_{(\text{BR})\text{DSS}}/\Delta T_J$	$I_D = 1 \text{ mA}$, Referenced to 25°C		33		$\text{mV}/^{\circ}\text{C}$			
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 80 \text{ V}$, $T_J = 25^{\circ}\text{C}$			1.0	μA			
		$V_{\text{DS}} = 80 \text{ V}$, $T_J = 125^{\circ}\text{C}$			250				
Gate-to-Source Leakage Current	I_{GSS}	$V_{\text{GS}} = 20 \text{ V}$, $V_{\text{DS}} = 0 \text{ V}$			100	nA			
ON CHARACTERISTICS									
Drain-to-Source On Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10 \text{ V}$, $I_D = 95 \text{ A}$, $T_J = 25^{\circ}\text{C}$		0.95	1.1	$\text{m}\Omega$			
		$V_{\text{GS}} = 6 \text{ V}$, $I_D = 95 \text{ A}$, $T_J = 25^{\circ}\text{C}$		1.4					
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}} = V_{\text{DS}}$, $I_D = 475 \mu\text{A}$, $T_J = 25^{\circ}\text{C}$	2.4		3.6	V			
Gate Threshold Voltage Temperature Coefficient	$\Delta V_{\text{GS}(\text{th})}/\Delta T_J$	$V_{\text{GS}} = V_{\text{DS}}$, $I_D = 475 \mu\text{A}$		-7		$\text{mV}/^{\circ}\text{C}$			
Forward Transconductance	g_{FS}	$V_{\text{DS}} = 10 \text{ V}$, $I_D = 95 \text{ A}$		294		S			
CHARGES, CAPACITANCES & GATE RESISTANCE									
Input Capacitance	C_{iss}	$V_{\text{DS}} = 40 \text{ V}$, $V_{\text{GS}} = 0 \text{ V}$, $f = 1 \text{ MHz}$		8618		pF			
				2458					
				37					
				175					
Output Charge	Q_{oss}	$V_{\text{DD}} = 64 \text{ V}$, $I_D = 95 \text{ A}$, $V_{\text{GS}} = 10 \text{ V}$		121		nC			
				26					
				40					
				19					
			$f = 1 \text{ MHz}$	0.67		Ω			
SWITCHING CHARACTERISTICS									
Turn-On Delay Time	$t_{\text{d}(\text{on})}$	Resistive Load, $V_{\text{GS}} = 0/10 \text{ V}$, $V_{\text{DD}} = 64 \text{ V}$, $I_D = 95 \text{ A}$, $R_G = 2.5 \Omega$		40		ns			
				23					
				65					
				12					
SOURCE-TO-DRAIN DIODE CHARACTERISTICS									
Forward Diode Voltage	V_{SD}	$I_S = 95 \text{ A}$, $V_{\text{GS}} = 0 \text{ V}$, $T_J = 25^{\circ}\text{C}$		0.83	1.2	V			
		$I_S = 95 \text{ A}$, $V_{\text{GS}} = 0 \text{ V}$, $T_J = 125^{\circ}\text{C}$		0.67					
Reverse Recovery Time	t_{rr}	$V_{\text{GS}} = 0 \text{ V}$, $I_S = 95 \text{ A}$ $dl/dt = 1000 \text{ A}/\mu\text{s}$, $V_{\text{DD}} = 64 \text{ V}$, $T_J = 25^{\circ}\text{C}$		32		ns			
				17					
				15					
				307					
Product Information									
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.									

TYPICAL CHARACTERISTICS

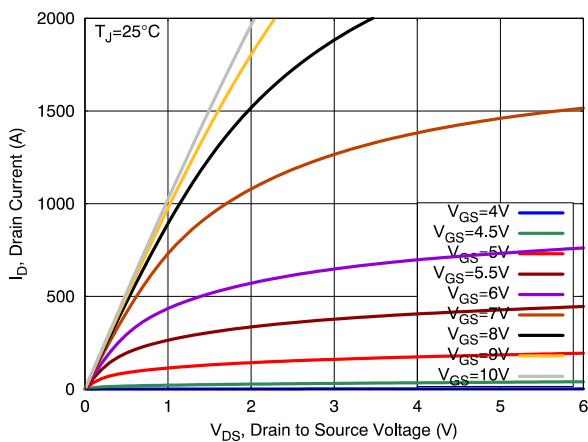


Figure 1. On-Region Characteristics

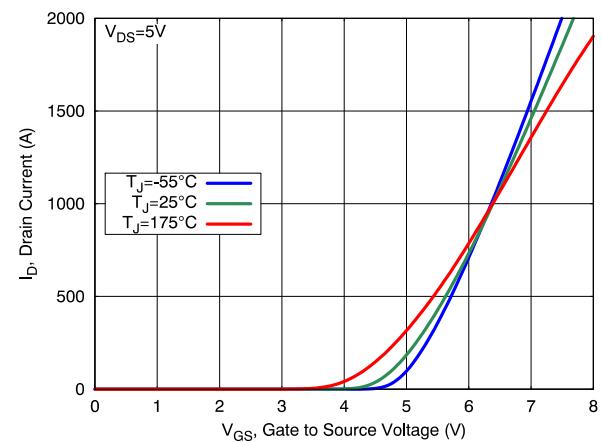


Figure 2. Transfer Characteristics

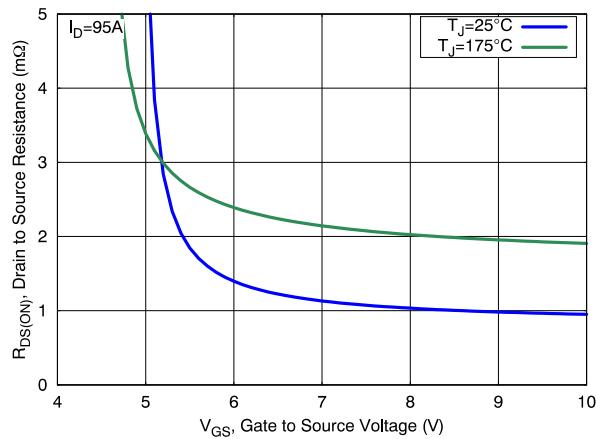


Figure 3. On-Resistance vs. Gate Voltage

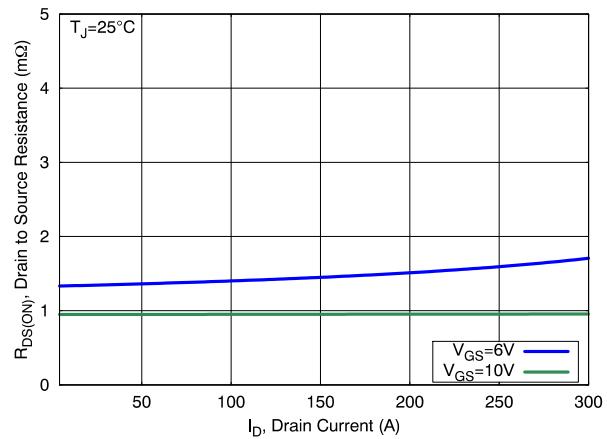


Figure 4. On-Resistance vs. Drain Current

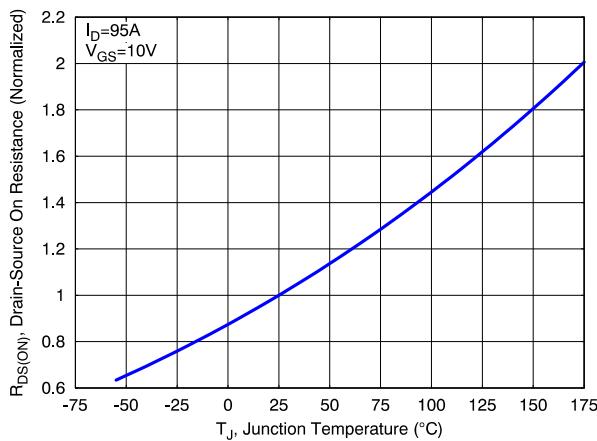


Figure 5. Normalized ON Resistance vs. Junction Temperature

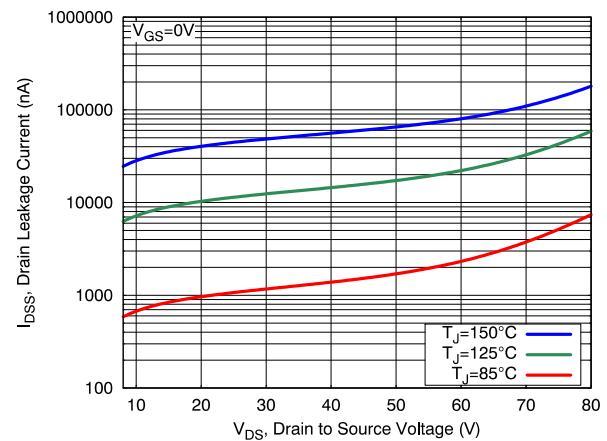


Figure 6. Drain Leakage Current vs Drain Voltage

TYPICAL CHARACTERISTICS

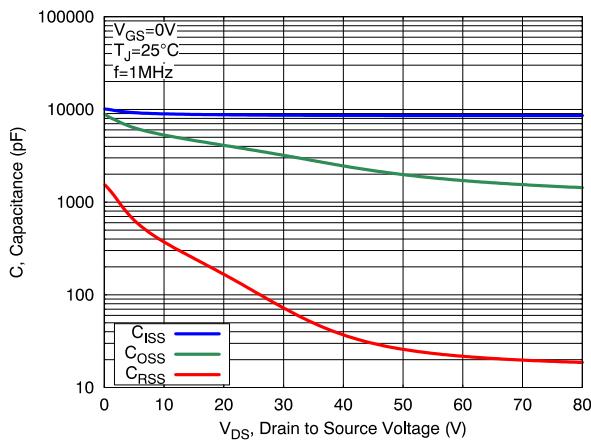


Figure 7. Capacitance Characteristics

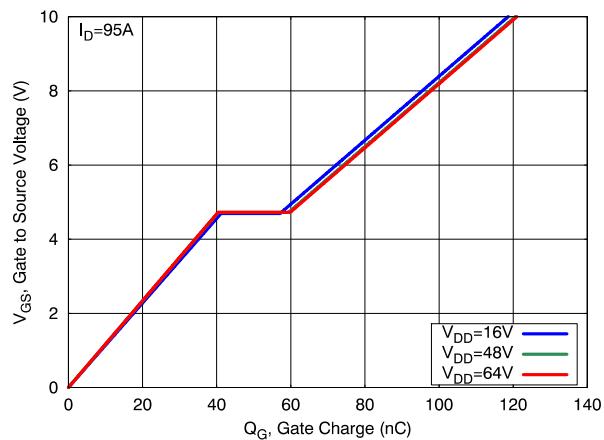


Figure 8. Gate Charge Characteristics

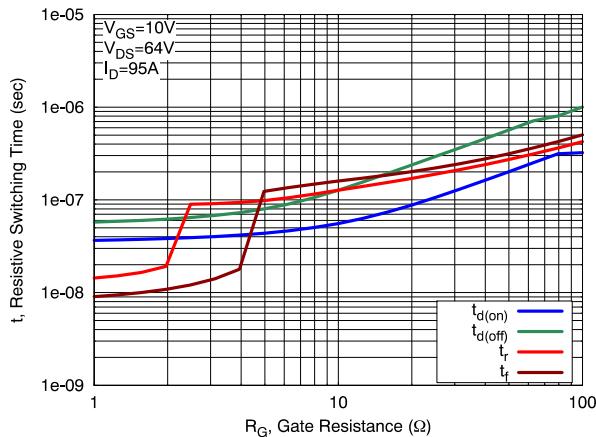


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

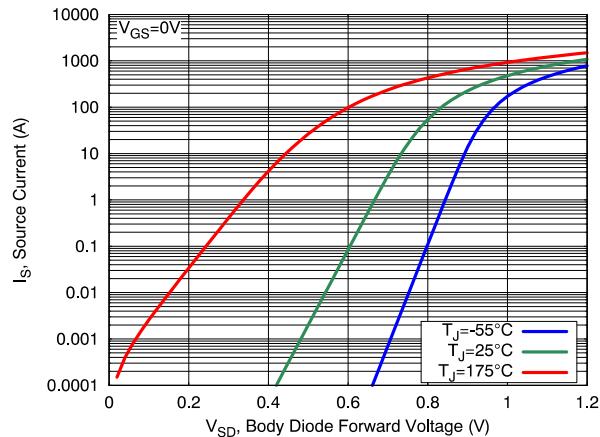


Figure 10. Diode Forward Characteristics

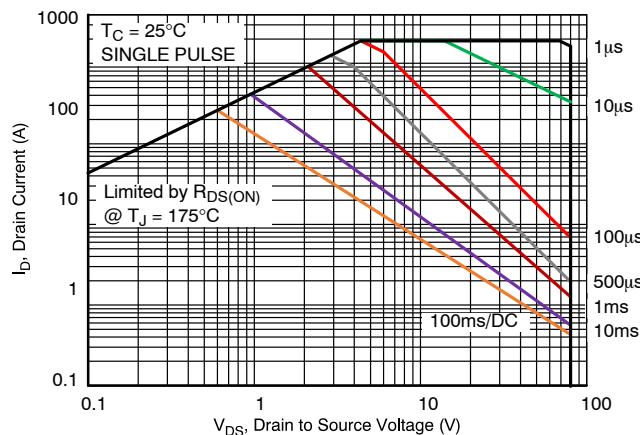


Figure 11. Safe Operating Area (SOA)

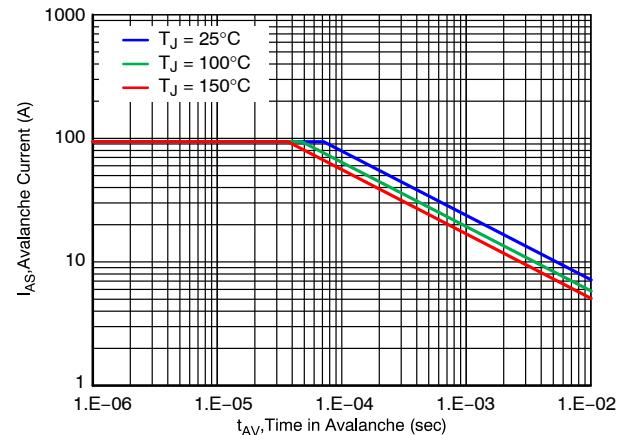


Figure 12. Avalanche Current vs Pulse Time (UIS)

TYPICAL CHARACTERISTICS

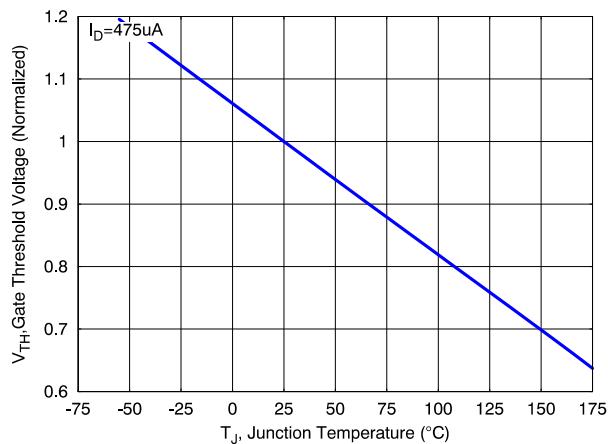


Figure 13. Gate Threshold Voltage vs Junction Temperature

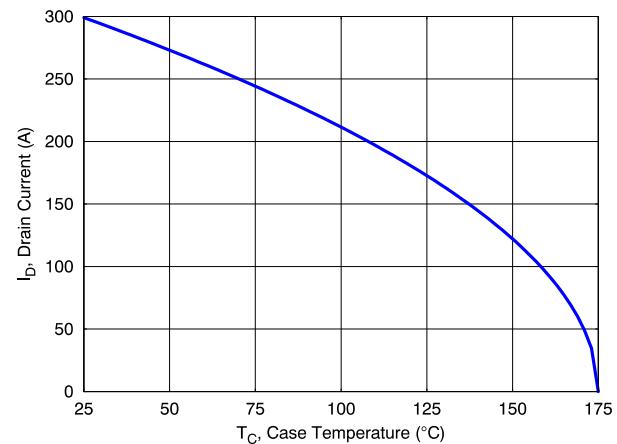


Figure 14. Maximum Current vs. Case Temperature

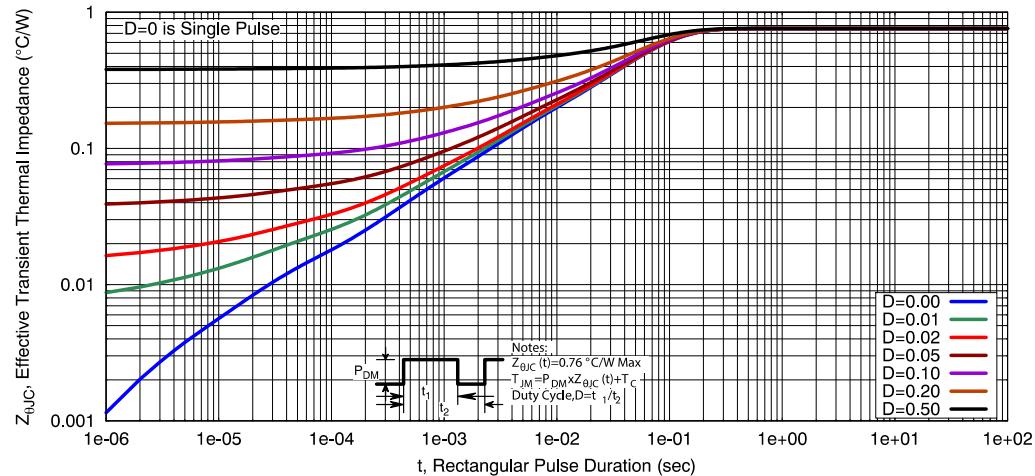


Figure 15. Transient Thermal Response

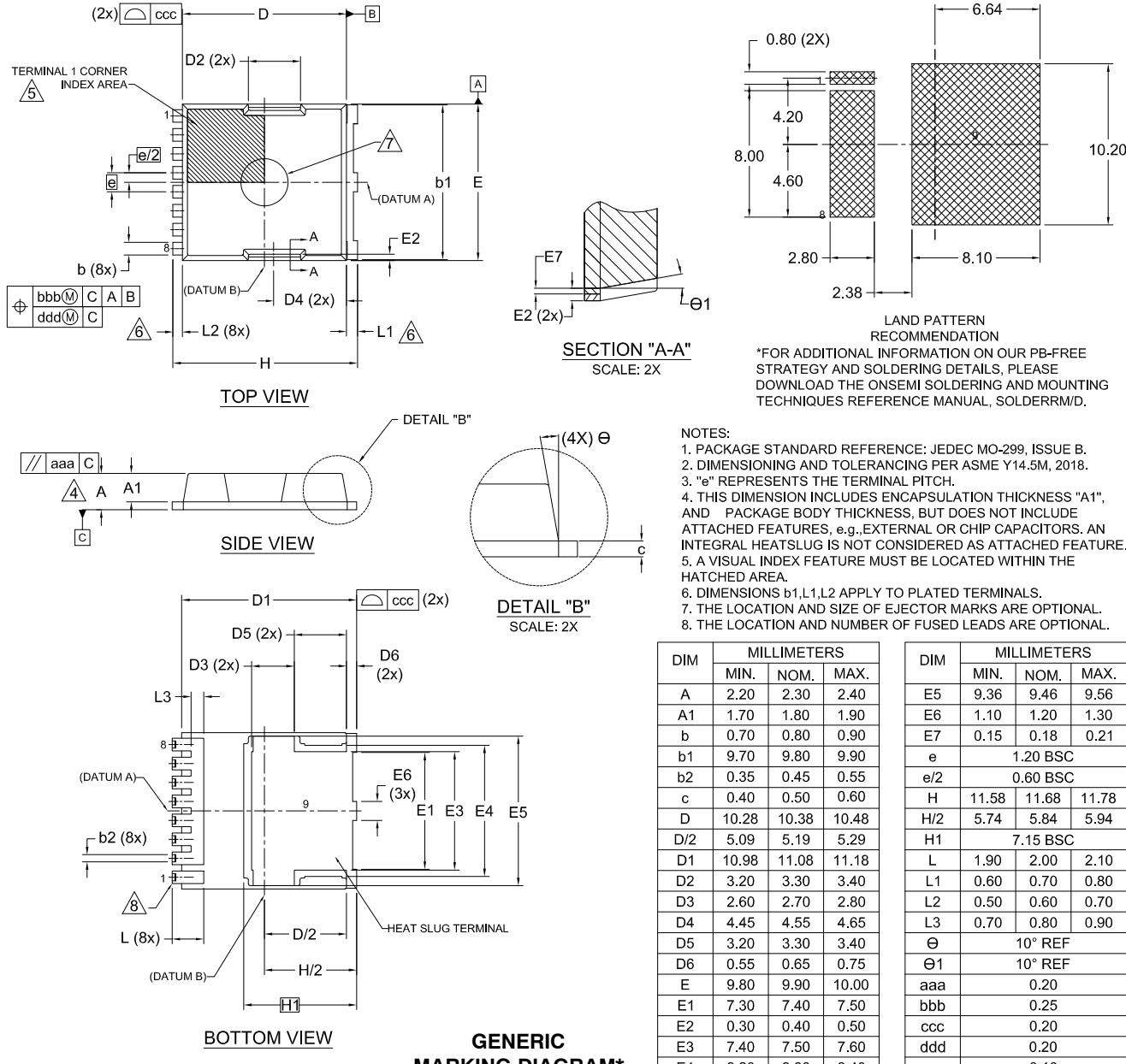
NVBL1D2N08X

PACKAGE DIMENSIONS



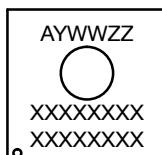
**H-PSOF8L 11.68x9.80x2.30, 1.20P
CASE 100CU
ISSUE F**

DATE 30 JUL 2024



DIM	MILLIMETERS			DIM	MILLIMETERS		
	MIN.	NOM.	MAX.		MIN.	NOM.	MAX.
A	2.20	2.30	2.40	E5	9.36	9.46	9.56
A1	1.70	1.80	1.90	E6	1.10	1.20	1.30
b	0.70	0.80	0.90	E7	0.15	0.18	0.21
b1	9.70	9.80	9.90	e	1.20 BSC		
b2	0.35	0.45	0.55	e/2	0.60 BSC		
c	0.40	0.50	0.60	H	11.58	11.68	11.78
D	10.28	10.38	10.48	H/2	5.74	5.84	5.94
D/2	5.09	5.19	5.29	H1	7.15 BSC		
D1	10.98	11.08	11.18	L	1.90	2.00	2.10
D2	3.20	3.30	3.40	L1	0.60	0.70	0.80
D3	2.60	2.70	2.80	L2	0.50	0.60	0.70
D4	4.45	4.55	4.65	L3	0.70	0.80	0.90
D5	3.20	3.30	3.40	Θ	10° REF		
D6	0.55	0.65	0.75	Θ1	10° REF		
E	9.80	9.90	10.00	aaa	0.20		
E1	7.30	7.40	7.50	bbb	0.25		
E2	0.30	0.40	0.50	ccc	0.20		
E3	7.40	7.50	7.60	ddd	0.20		
E4	8.20	8.30	8.40	eee	0.10		

A = Assembly Location
Y = Year
WW = Work Week
ZZ = Assembly Lot Code
XXXX = Specific Device Code



*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

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