

Description

IS31FL3730 is a LED matrix driver which features an audio modulation display mode and a general LED dot matrix display mode. The default configuration of IS31FL3730 is to drive a single 8×8 LED matrix. However, IS31FL3730 may be configured to drive either one or two 8×8, 7×9, 6×10, or 5×11 dot matrix display(s). The intensity of any matrix picture can be modulated by an audio signal.

In the general purpose mode, the ON or OFF condition of each individual LED in the display matrix is programmed via an I2C interface.

Features

- 2.7V to 5.5V supply
- I2C interface, automatic address increment function
- One-time programming, internal scan
- Audio modulated display intensity with digitally programmable input gain
- Internal registers to digitally adjust display intensity
- Modulate LED brightness with 128 different items in PWM
- One address pin with 4 logic levels to allow four I2C slave addresses
- QFN-24 (4mm × 4mm) package

Quick Start



Figure 1: Photo of IS31FL3730 Evaluation Board

Recommended Equipment

- 5.0V, 2A power supply
- Audio source(i.e. MP3 player, Notebook PC, etc)
- 8Ω speaker

Absolute Maximum Ratings

- ≤ 5.5V power supply

Caution: Do not exceed the conditions listed above, otherwise the board will be damaged.

Procedure

The IS31FL3730 evaluation board is fully assembled and tested. Follow the steps listed below to verify board operation.

Caution: Do not turn on the power supply until all connections are completed.

- 1) Connect an 8Ω speaker to the “SPK” connector.
- 2) Connect the audio source to the “AUDIO IN” connector.
- 3) Connect the DC power to the connector (DC IN).
- 4) Turn on the power supply and pay attention to the supply current. If the current exceeds 1A, please check for circuit fault.
- 5) Turn on the audio signal.
- 6) Modulation of the audio signal utilized to obtain better sound output performance

Evaluation Board Ordering Information

Part No.	IC Package
IS31FL3730-QFLS2-EB	QFN-20, Lead-free

Table1: Ordering Information

For pricing, delivery, and ordering information, please contacts ISSI's analog marketing team at analog_mkt@issi.com or (408) 969-6600.

Evaluation Board Operation

The IS31FL3730 evaluation board has six display modes. Press MODE button to switch configurations.

- 1) (Default Mode) A little man is dancing and change postures according to music rhythm.
- 2) Display Chinese characters '赵, 钱, 孙, 李', etc one by one, then move to left to display more characters.
- 3) Display Hart shape, Butterfly, Lock, Little Man, etc graphics with breathing dimming effect.
- 4) Flying butterfly.
- 5) Hart shape animation.
- 6) Combined shapes animation.

***Note:**

IS31FL3730 solely controls the FxLED function on the evaluation board.

Software Control

JP1 default setting is close circuit. If it is set to open, the on-board MCU will stop working. The I2C pins are set to High Impedance. External I2C signals can be connected to TP3 to control the IS31FL3730 LED driver.

Please refer to the datasheet to get more information about IS31FL3730.

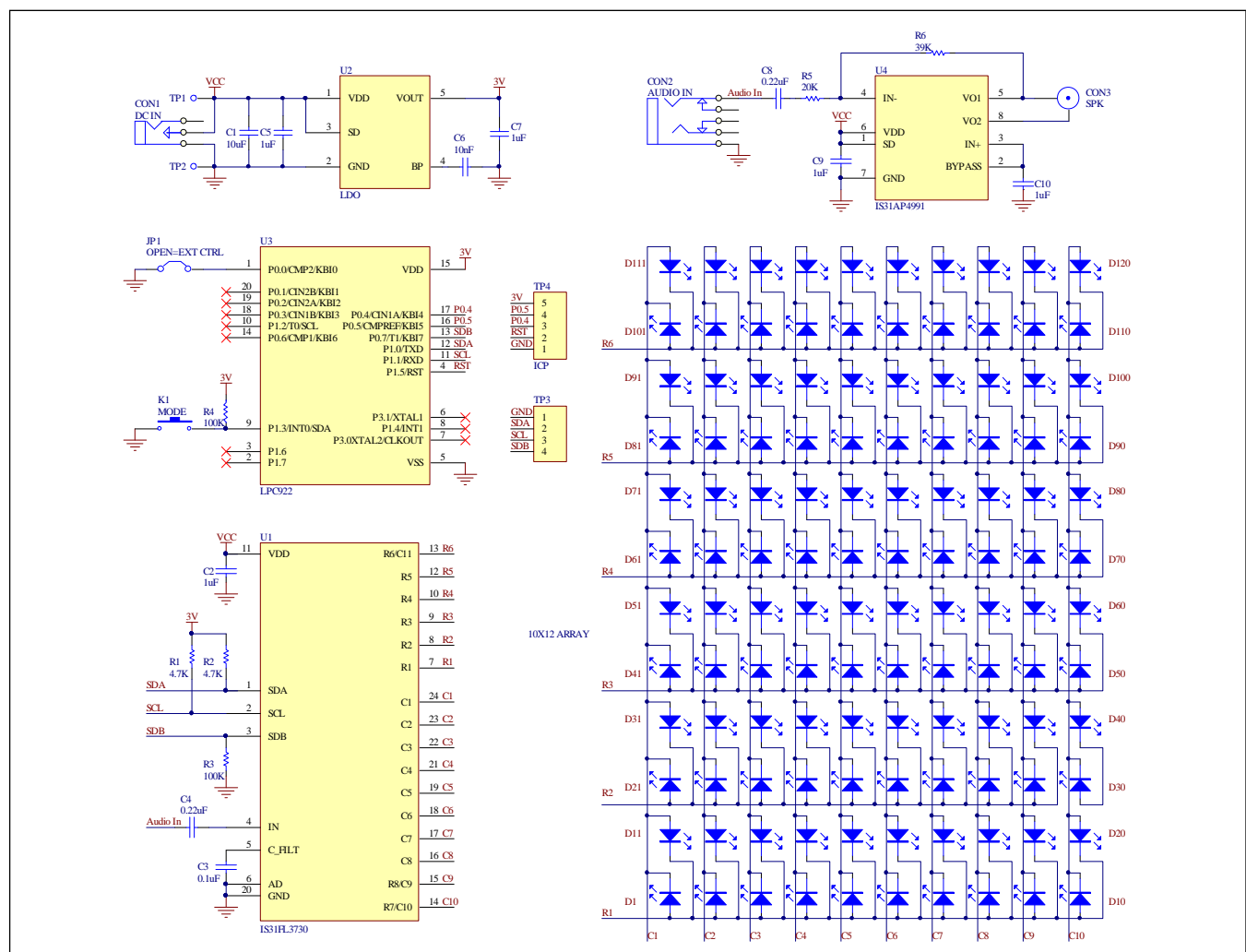


Figure 2: IS31FL3730 Application Schematic

Bill of Materials

Name	Symbol	Description	Qty	Supplier	Part No.
LED Driver	U1	Array FxLED Driver	1	ISSI	IS31FL3730
LDO	U2	Low-dropout Regulator	1	PAM	PAM3101
MCU	U3	Microcontroller	1	NXP	LPC922
Audio Amplifier	U4	Class-AB Audio Amplifier	1	ISSI	IS31AP4991
Diodes	D1~D120	Diode, LED Blue, SMD	120	Everlight	19-217/BHC-ZL 1M2RY/3T
Resistors	R1,R2	RES,4.7k,1/16W,±5%,SMD	2		
Resistors	R3,R4	RES,100k,1/16W,±5%,SMD	2		
Resistor	R5	RES,20k,1/16W,±5%,SMD	1		
Resistor	R6	RES,39k,1/16W,±5%,SMD	1		
Capacitor	C1	CAP,10μF,16V,±20%,SMD	1		
Capacitors	C2,C5,C7,C9, C10	CAP, 1μF,16V,±20%,SMD	5		
Capacitor	C3	CAP, 0.1μF,16V,±20%,SMD	1		
Capacitors	C4,C8	CAP, 0.22μF,16V,±20%,SMD	2		
Capacitor	C6	CAP,10nF,16V,±20%,SMD	1		
Button	K1	Button SMD	1		

Table 2: Bill of Materials, refer to Figure 2 above.

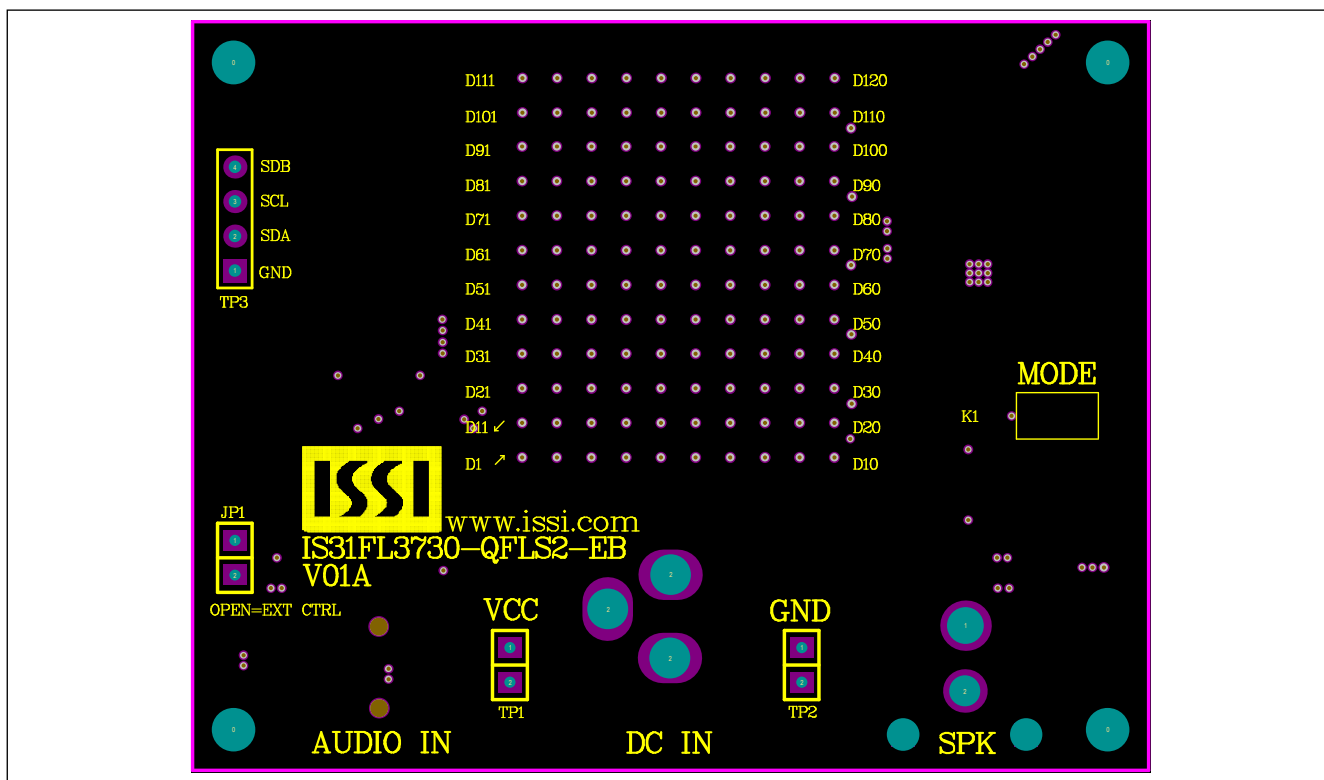


Figure 3: Board Component Placement Guide -Top Layer

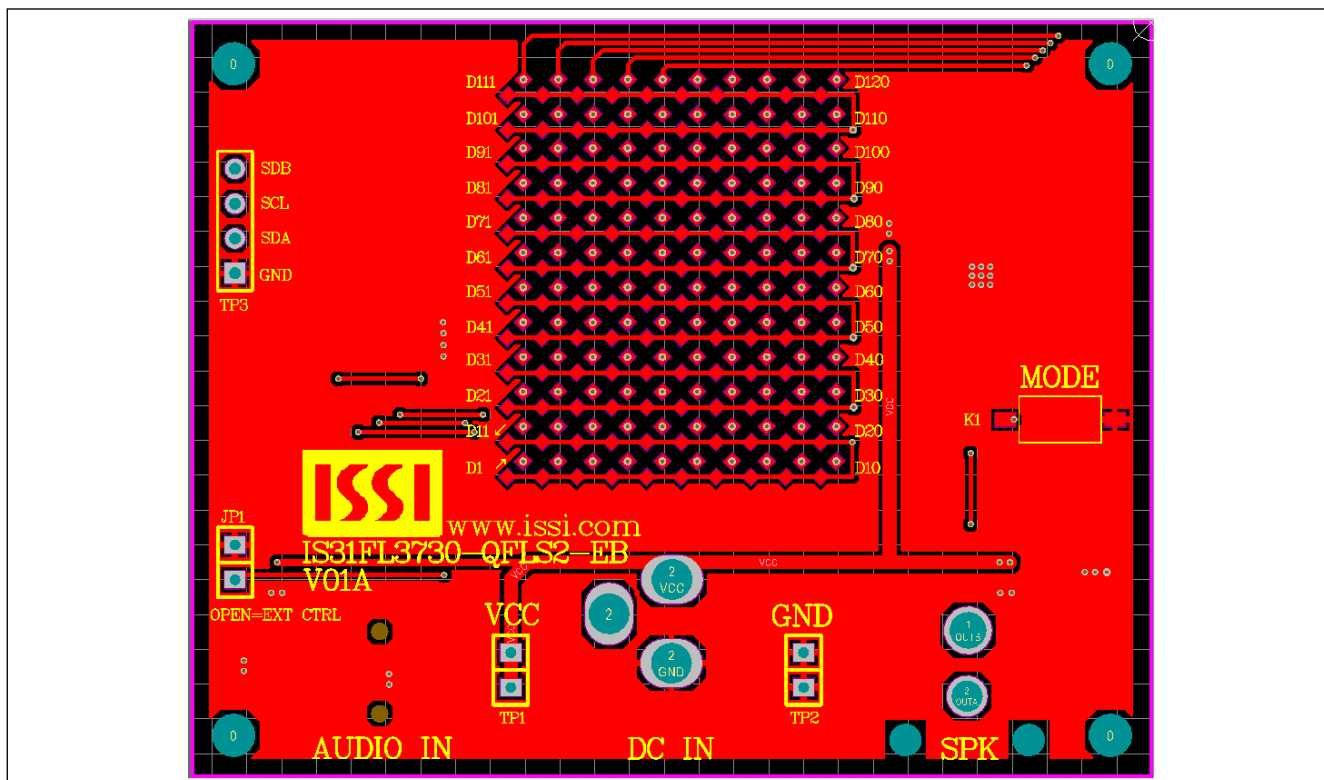


Figure 4: Board PCB Layout- Top Layer

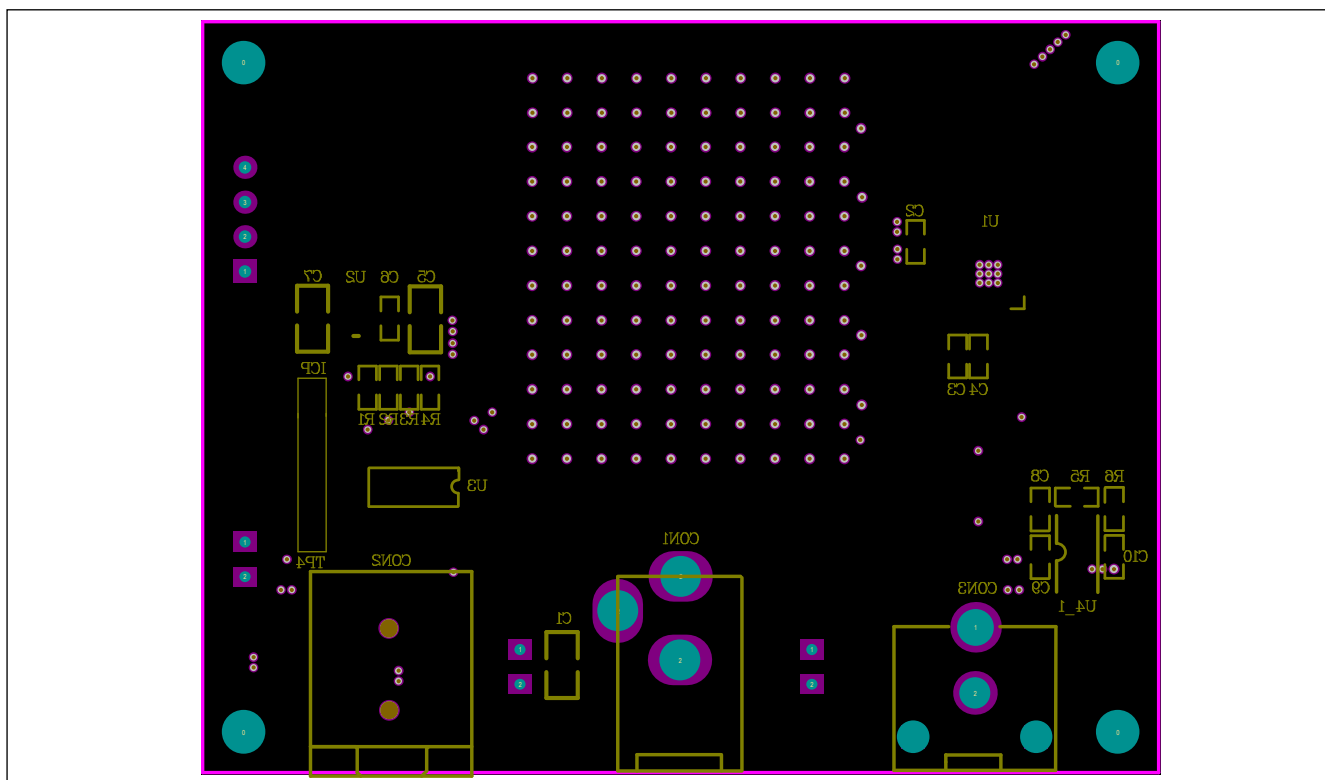


Figure 5: Board Component Placement Guide -Bottom Layer

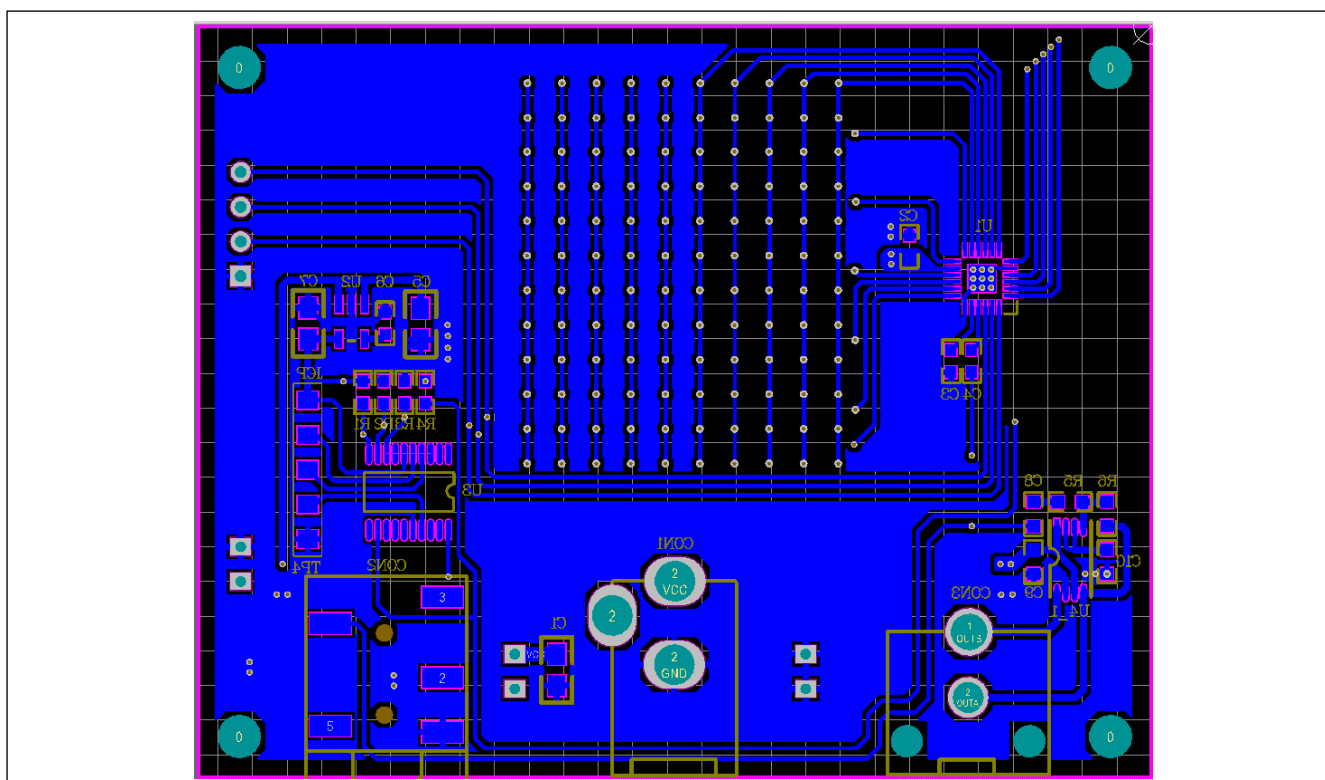


Figure 6: Board PCB Layout-Bottom Layer

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