

TRENCHSTOP™ IGBT 7

Low Loss Duopack: IGBT with Trench and Fieldstop technology

Features:

TRENCHSTOP™ IGBT 7 technology offering

- Very low V_{CEsat}
- Low turn-off losses
- Short tail current
- Reduced EMI
- Humidity robust design
- Very soft, fast recovery anti-parallel diode
- Maximum junction temperature 175°C
- Qualified according to JEDEC for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models:
<http://www.infineon.com/igbt7/>

Applications:

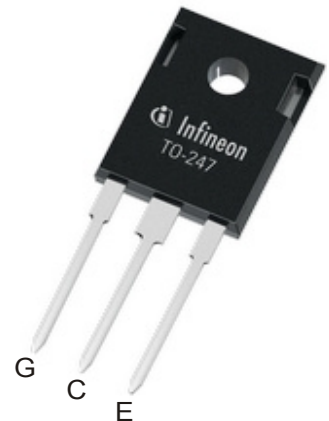
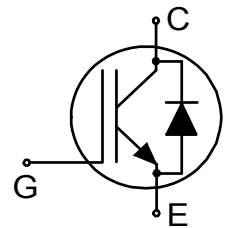
- Drives
 - Servo
 - GPD
- Industrial Power Supplies
 - Industrial UPS
 - Residential UPS
- Energy Generation
 - Solar Central Inverter
 - Solar String Inverter
 - Solar Pump

Product Validation:

Qualified for industrial applications according to the relevant tests of JEDEC47/20/22

Package pin definition:

- Pin C & backside - Collector
- Pin E - Emitter
- Pin G - Gate



Key Performance and Package Parameters

Type	V_{CE}	I_C	$V_{CEsat}, T_{vj}=25^{\circ}\text{C}$	T_{vjmax}	Marking	Package
IKW50N65ET7	650V	50A	1.35V	175°C	K50EET7	PG-TO247-3

Table of Contents

Description 1

Table of Contents 2

Maximum Ratings 3

Thermal Resistance 3

Electrical Characteristics 4

Electrical Characteristics Diagrams 7

Package Drawing14

Testing Conditions15

Revision History16

Disclaimer17

TRENCHSTOP™ IGBT 7

Maximum Ratings

For optimum lifetime and reliability, Infineon recommends operating conditions that do not exceed 80% of the maximum ratings stated in this datasheet.

Parameter	Symbol	Value	Unit
Collector-emitter voltage, $T_{vj} \geq 25^{\circ}\text{C}$	V_{CE}	650	V
DC collector current, limited by T_{vjmax} $T_c = 25^{\circ}\text{C}$ value limited by bondwire $T_c = 100^{\circ}\text{C}$	I_C	80.0 59.7	A
Pulsed collector current, t_p limited by $T_{vjmax}^{1)}$	I_{Cpuls}	150.0	A
Turn off safe operating area $V_{CE} \leq 650\text{V}$, $T_{vj} \leq 175^{\circ}\text{C}$, $t_p = 1\mu\text{s}^{2)}$	-	150.0	A
Diode forward current, limited by T_{vjmax} $T_c = 25^{\circ}\text{C}$ value limited by bondwire $T_c = 100^{\circ}\text{C}$	I_F	80.0 50.0	A
Diode pulsed current, t_p limited by $T_{vjmax}^{1)}$	I_{Fpuls}	150.0	A
Gate-emitter voltage Transient Gate-emitter voltage ($t_p \leq 10\mu\text{s}$, $D < 0.010$)	V_{GE}	± 20 ± 30	V
Short circuit withstand time $V_{GE} = 15.0\text{V}$, $V_{CC} \leq 400\text{V}$ Allowed number of short circuits < 1000 Time between short circuits: $\geq 1.0\text{s}$ $T_{vj} = 150^{\circ}\text{C}$	t_{SC}	3	μs
Power dissipation $T_c = 25^{\circ}\text{C}$ Power dissipation $T_c = 100^{\circ}\text{C}$	P_{tot}	273.0 136.0	W
Operating junction temperature	T_{vj}	$-40 \dots +175$	$^{\circ}\text{C}$
Storage temperature	T_{stg}	$-55 \dots +150$	$^{\circ}\text{C}$
Soldering temperature, wave soldering 1.6mm (0.063in.) from case for 10s		260	$^{\circ}\text{C}$
Mounting torque, M3 screw Maximum of mounting processes: 3	M	0.6	Nm

Thermal Resistance

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	

R_{th} Characteristics

IGBT thermal resistance, junction - case	$R_{th(j-c)}$		-	-	0.55	K/W
Diode thermal resistance, junction - case	$R_{th(j-c)}$		-	-	0.80	K/W
Thermal resistance junction - ambient	$R_{th(j-a)}$		-	-	40	K/W

¹⁾ Defined by design. Not subject to production test.

²⁾ Clamped inductive load current test for each device, $I_C=150\text{A}$, $V_{CC}=400\text{V}$, $T_c=25^{\circ}\text{C}$, $V_{GE}=20\text{V}$, $L=80\mu\text{H}$, $R_G=10\Omega$.

TRENCHSTOP™ IGBT 7

Electrical Characteristic, at $T_{vj} = 25^{\circ}\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Static Characteristic						
Collector-emitter saturation voltage	V_{CEsat}	$V_{GE} = 15.0V, I_C = 50.0A$ $T_{vj} = 25^{\circ}C$ $T_{vj} = 125^{\circ}C$ $T_{vj} = 175^{\circ}C$	- - -	1.35 1.50 1.60	1.65 - -	V
Diode forward voltage	V_F	$V_{GE} = 0V, I_F = 50.0A$ $T_{vj} = 25^{\circ}C$ $T_{vj} = 125^{\circ}C$ $T_{vj} = 175^{\circ}C$	- - -	1.65 1.60 1.55	2.00 - -	V
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C = 0.50mA, V_{CE} = V_{GE}$	4.3	5.0	5.7	V
Zero gate voltage collector current	I_{CES}	$V_{CE} = 650V, V_{GE} = 0V$ $T_{vj} = 25^{\circ}C$ $T_{vj} = 175^{\circ}C$	- -	- 1000	40 -	μA
Gate-emitter leakage current	I_{GES}	$V_{CE} = 0V, V_{GE} = 20V$	-	-	100	nA
Transconductance	g_{fs}	$V_{CE} = 20V, I_C = 50.0A$	-	26.0	-	S

Electrical Characteristic, at $T_{vj} = 25^{\circ}\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Dynamic Characteristic						
Input capacitance	C_{ies}	$V_{CE} = 25V, V_{GE} = 0V$ $f = 1000kHz$	-	3050	-	pF
Output capacitance	C_{oes}		-	92	-	
Reverse transfer capacitance	C_{res}		-	31	-	
Gate charge	Q_G	$V_{CC} = 520V, I_C = 50.0A,$ $V_{GE} = 15V$	-	290.0	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L_E		-	13.0	-	nH
Short circuit collector current Max. 1000 short circuits Time between short circuits: $\geq 1.0s$	$I_{C(SC)}$	$V_{GE} = 15.0V, V_{CC} \leq 400V,$ $t_{SC} \leq 3\mu s$ $T_{vj} = 150^{\circ}C$	-	255	-	A

Switching Characteristic, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic, at $T_{vj} = 25^{\circ}\text{C}$						
Turn-on delay time	$t_{d(\text{on})}$	$T_{vj} = 25^{\circ}\text{C}$, $V_{CC} = 400\text{V}$, $I_C = 50.0\text{A}$, $V_{GE} = 0.0/15.0\text{V}$, $R_{G(\text{on})} = 9.0\Omega$, $R_{G(\text{off})} = 9.0\Omega$, $L\sigma = 32\text{nH}$, $C\sigma = 30\text{pF}$ $L\sigma$, $C\sigma$ from Fig. E Energy losses include “tail” and diode reverse recovery.	-	26	-	ns
Rise time	t_r		-	20	-	ns
Turn-off delay time	$t_{d(\text{off})}$		-	350	-	ns
Fall time	t_f		-	14	-	ns
Turn-on energy	E_{on}		-	1.20	-	mJ
Turn-off energy	E_{off}		-	0.85	-	mJ
Total switching energy	E_{ts}		-	2.05	-	mJ

TRENCHSTOP™ IGBT 7

Turn-on delay time	$t_{d(on)}$	$T_{vj} = 25^{\circ}\text{C}$, $V_{CC} = 400\text{V}$, $I_C = 25.0\text{A}$, $V_{GE} = 0.0/15.0\text{V}$, $R_{G(on)} = 9.0\Omega$, $R_{G(off)} = 9.0\Omega$, $L\sigma = 32\text{nH}$, $C\sigma = 30\text{pF}$ $L\sigma$, $C\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	24	-	ns
Rise time	t_r		-	11	-	ns
Turn-off delay time	$t_{d(off)}$		-	370	-	ns
Fall time	t_f		-	12	-	ns
Turn-on energy	E_{on}		-	0.51	-	mJ
Turn-off energy	E_{off}		-	0.38	-	mJ
Total switching energy	E_{ts}		-	0.89	-	mJ

Diode Characteristic, at $T_{vj} = 25^{\circ}\text{C}$

Diode reverse recovery time	t_{rr}	$T_{vj} = 25^{\circ}\text{C}$, $V_R = 400\text{V}$, $I_F = 50.0\text{A}$, $di_F/dt = 1720\text{A}/\mu\text{s}$	-	93	-	ns
Diode reverse recovery charge	Q_{rr}		-	1.05	-	μC
Diode peak reverse recovery current	I_{rrm}		-	21.0	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	-260	-	A/ μs
Diode reverse recovery time	t_{rr}	$T_{vj} = 25^{\circ}\text{C}$, $V_R = 400\text{V}$, $I_F = 25.0\text{A}$, $di_F/dt = 2340\text{A}/\mu\text{s}$	-	62	-	ns
Diode reverse recovery charge	Q_{rr}		-	0.74	-	μC
Diode peak reverse recovery current	I_{rrm}		-	25.0	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	-490	-	A/ μs

Switching Characteristic, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic, at $T_{vj} = 175^{\circ}\text{C}$						
Turn-on delay time	$t_{d(\text{on})}$	$T_{vj} = 175^{\circ}\text{C}$, $V_{CC} = 400\text{V}$, $I_C = 50.0\text{A}$, $V_{GE} = 0.0/15.0\text{V}$, $R_{G(\text{on})} = 9.0\Omega$, $R_{G(\text{off})} = 9.0\Omega$, $L\sigma = 32\text{nH}$, $C\sigma = 30\text{pF}$ $L\sigma$, $C\sigma$ from Fig. E Energy losses include “tail” and diode reverse recovery.	-	30	-	ns
Rise time	t_r		-	23	-	ns
Turn-off delay time	$t_{d(\text{off})}$		-	410	-	ns
Fall time	t_f		-	30	-	ns
Turn-on energy	E_{on}		-	1.91	-	mJ
Turn-off energy	E_{off}		-	1.40	-	mJ
Total switching energy	E_{ts}		-	3.31	-	mJ
Turn-on delay time	$t_{d(\text{on})}$	$T_{vj} = 175^{\circ}\text{C}$, $V_{CC} = 400\text{V}$, $I_C = 25.0\text{A}$, $V_{GE} = 0.0/15.0\text{V}$, $R_{G(\text{on})} = 9.0\Omega$, $R_{G(\text{off})} = 9.0\Omega$, $L\sigma = 32\text{nH}$, $C\sigma = 30\text{pF}$ $L\sigma$, $C\sigma$ from Fig. E Energy losses include “tail” and diode reverse recovery.	-	27	-	ns
Rise time	t_r		-	14	-	ns
Turn-off delay time	$t_{d(\text{off})}$		-	450	-	ns
Fall time	t_f		-	40	-	ns
Turn-on energy	E_{on}		-	0.88	-	mJ
Turn-off energy	E_{off}		-	0.69	-	mJ
Total switching energy	E_{ts}		-	1.57	-	mJ

TRENCHSTOP™ IGBT 7

Diode Characteristic, at $T_{vj} = 175^{\circ}\text{C}$

Diode reverse recovery time	t_{rr}	$T_{vj} = 175^{\circ}\text{C},$ $V_R = 400\text{V},$ $I_F = 50.0\text{A},$ $di_F/dt = 1680\text{A}/\mu\text{s}$	-	140	-	ns
Diode reverse recovery charge	Q_{rr}		-	2.70	-	μC
Diode peak reverse recovery current	I_{rrm}		-	33.0	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	-290	-	$\text{A}/\mu\text{s}$
Diode reverse recovery time	t_{rr}	$T_{vj} = 175^{\circ}\text{C},$ $V_R = 400\text{V},$ $I_F = 25.0\text{A},$ $di_F/dt = 2000\text{A}/\mu\text{s}$	-	105	-	ns
Diode reverse recovery charge	Q_{rr}		-	1.95	-	μC
Diode peak reverse recovery current	I_{rrm}		-	34.0	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	-415	-	$\text{A}/\mu\text{s}$

TRENCHSTOP™ IGBT 7

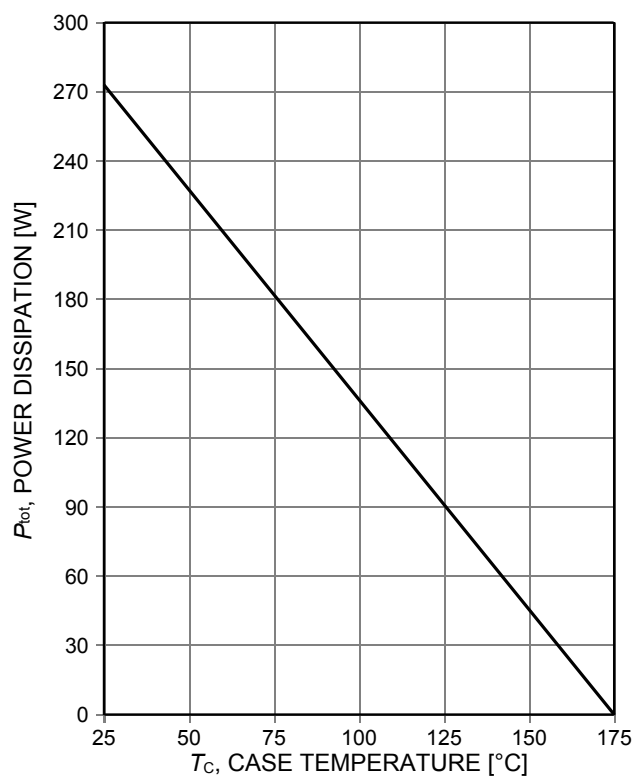


Figure 1. **Power dissipation as a function of case temperature**
($T_{vj} \leq 175^\circ\text{C}$)

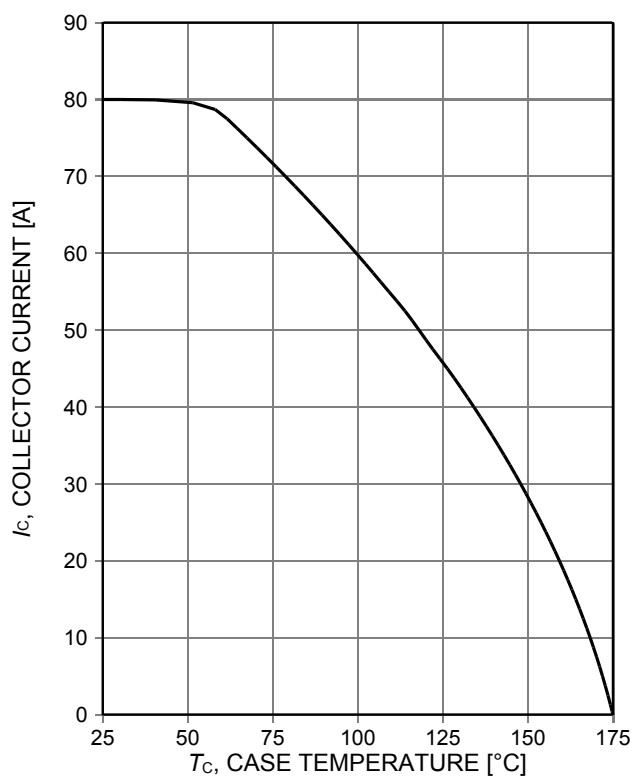


Figure 2. **Collector current as a function of case temperature**
($V_{GE} \geq 15\text{V}$, $T_{vj} \leq 175^\circ\text{C}$)

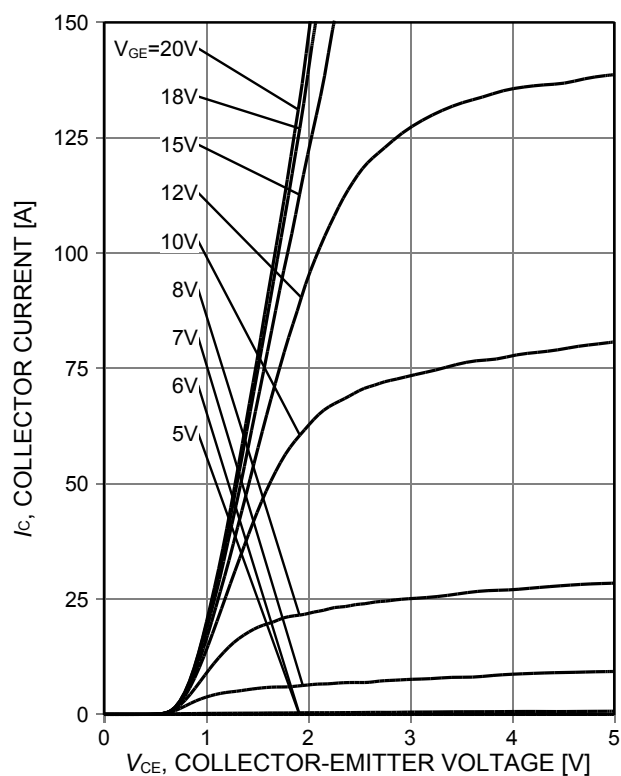


Figure 3. **Typical output characteristic**
($T_{vj} = 25^\circ\text{C}$)

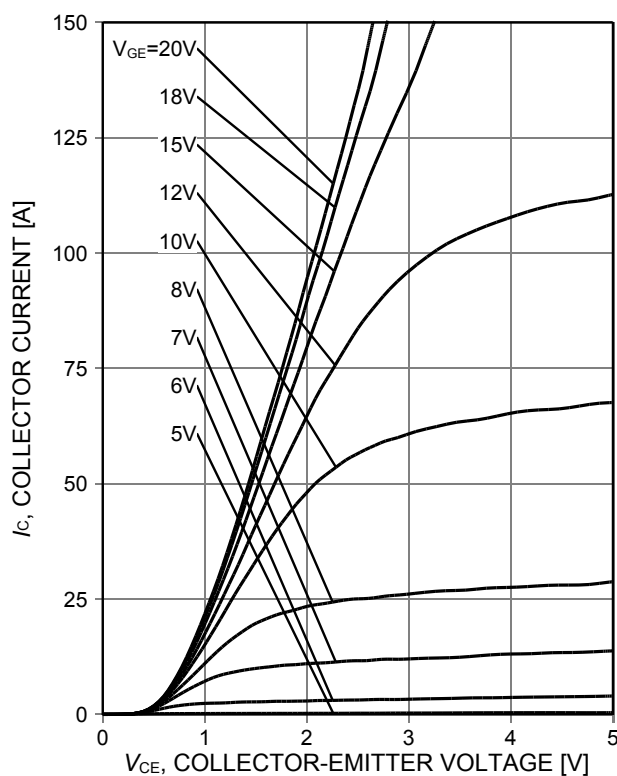


Figure 4. **Typical output characteristic**
($T_{vj} = 175^\circ\text{C}$)

TRENCHSTOP™ IGBT 7

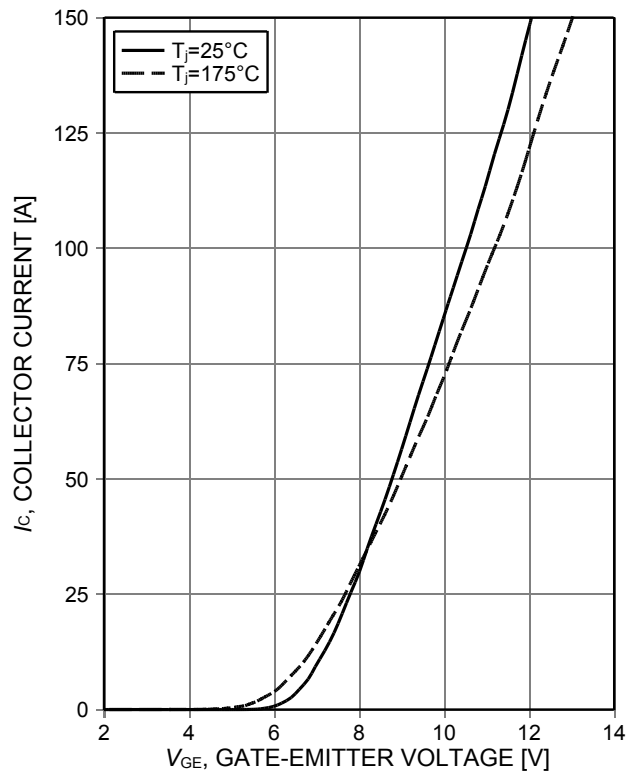


Figure 5. **Typical transfer characteristic**
($V_{CE}=20V$)

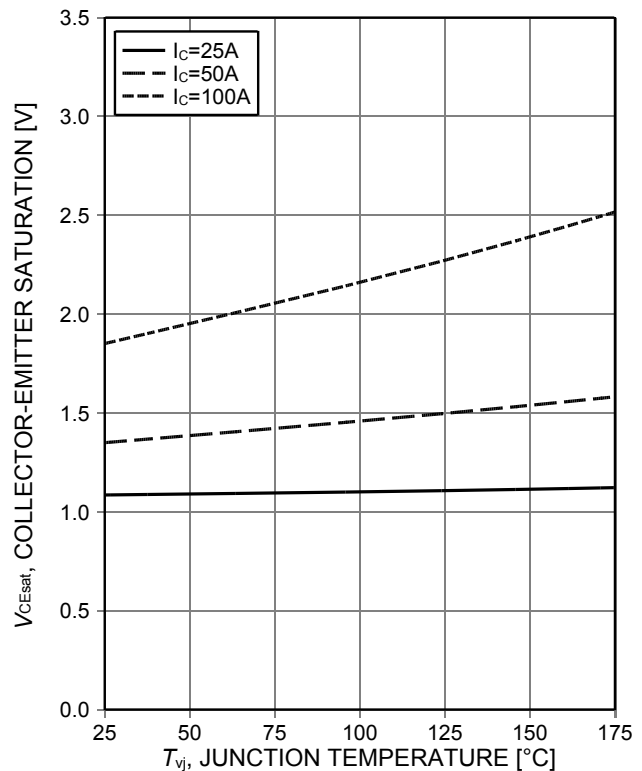


Figure 6. **Typical collector-emitter saturation voltage as a function of junction temperature**
($V_{GE}=15V$)

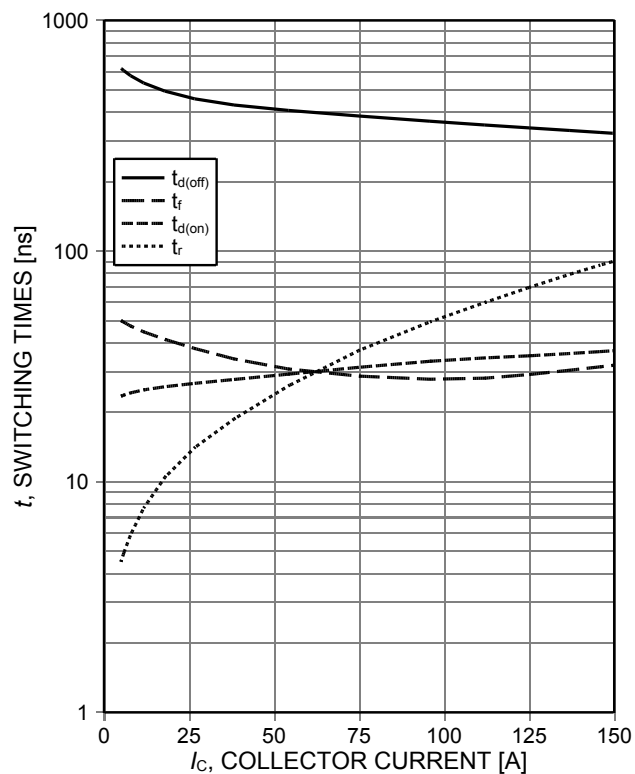


Figure 7. **Typical switching times as a function of collector current**
(inductive load, $T_{vj}=175^{\circ}C$, $V_{CE}=400V$, $V_{GE}=15/0V$, $R_G=9\Omega$, Dynamic test circuit in Figure E)

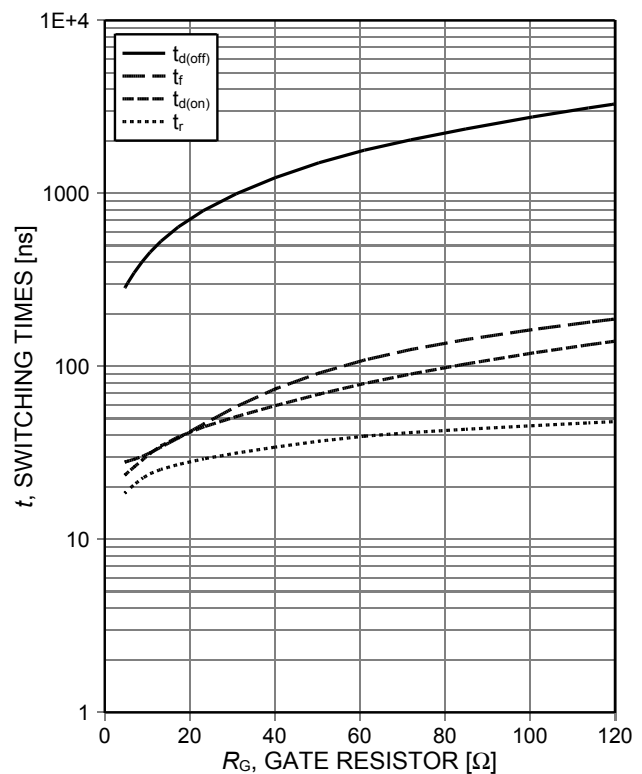


Figure 8. **Typical switching times as a function of gate resistor**
(inductive load, $T_{vj}=175^{\circ}C$, $V_{CE}=400V$, $V_{GE}=15/0V$, $I_C=50A$, Dynamic test circuit in Figure E)

TRENCHSTOP™ IGBT 7

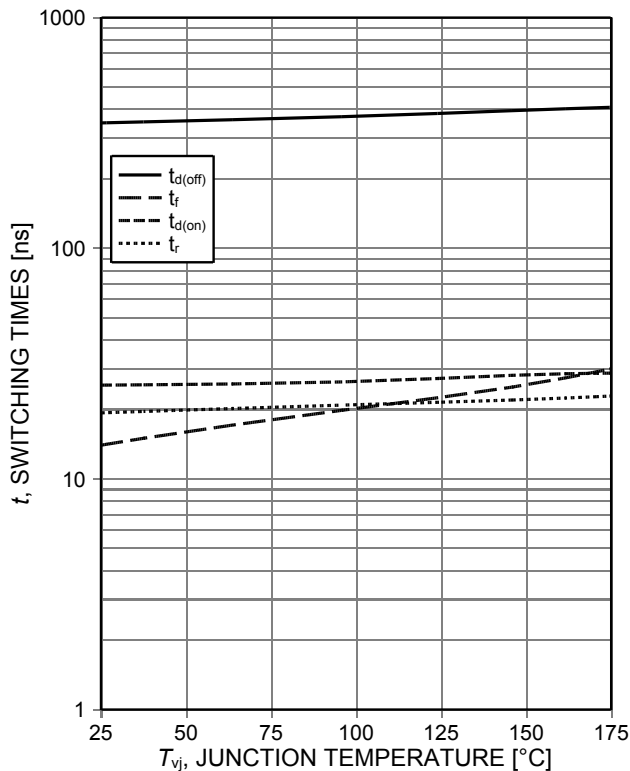


Figure 9. **Typical switching times as a function of junction temperature**
(inductive load, $V_{CE}=400V$, $V_{GE}=15/0V$, $I_C=50A$, $r_G=9\Omega$, Dynamic test circuit in Figure E)

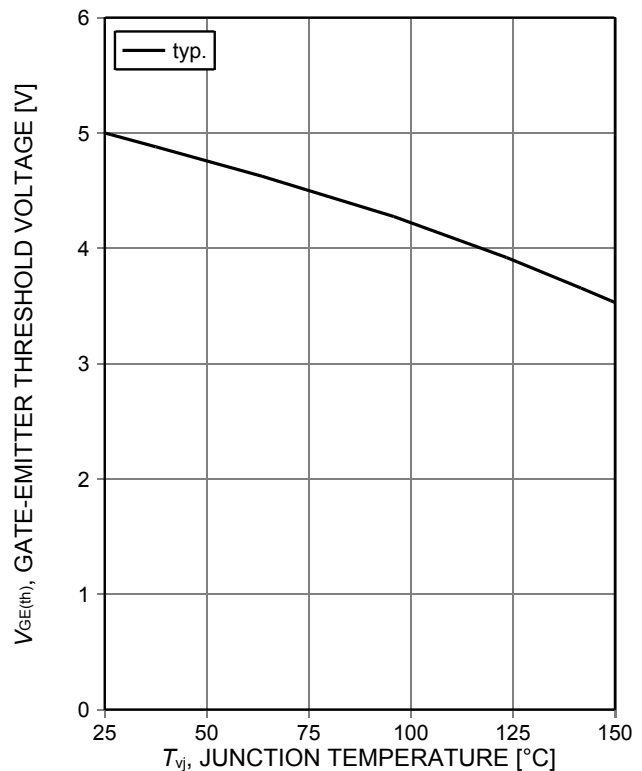


Figure 10. **Gate-emitter threshold voltage as a function of junction temperature**
($I_C=0.5mA$)

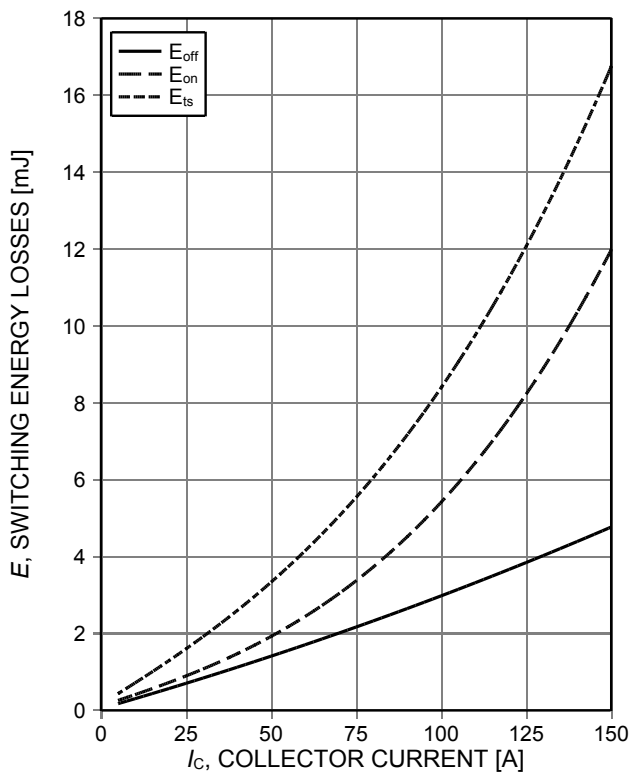


Figure 11. **Typical switching energy losses as a function of collector current**
(inductive load, $T_{vj}=175^\circ C$, $V_{CE}=400V$, $V_{GE}=15/0V$, $R_G=9\Omega$, Dynamic test circuit in Figure E)

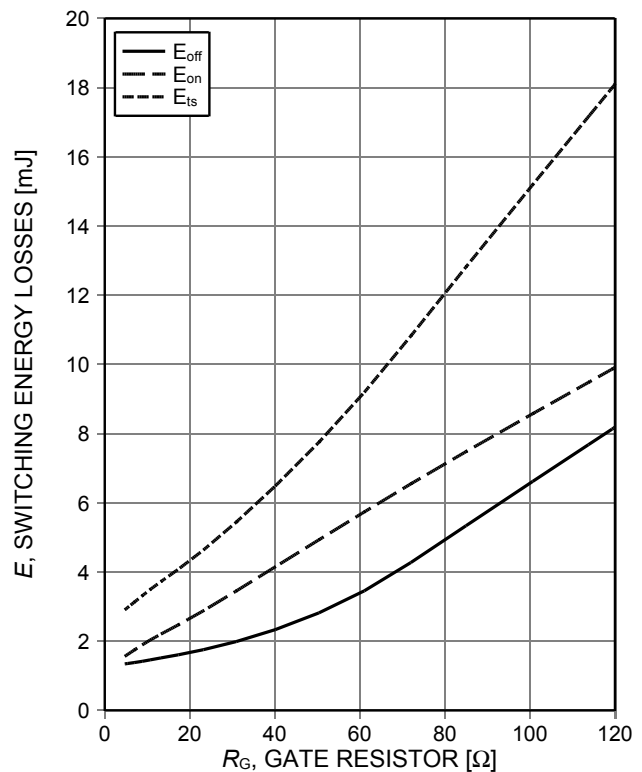


Figure 12. **Typical switching energy losses as a function of gate resistor**
(inductive load, $T_{vj}=175^\circ C$, $V_{CE}=400V$, $V_{GE}=15/0V$, $I_C=50A$, Dynamic test circuit in Figure E)

TRENCHSTOP™ IGBT 7

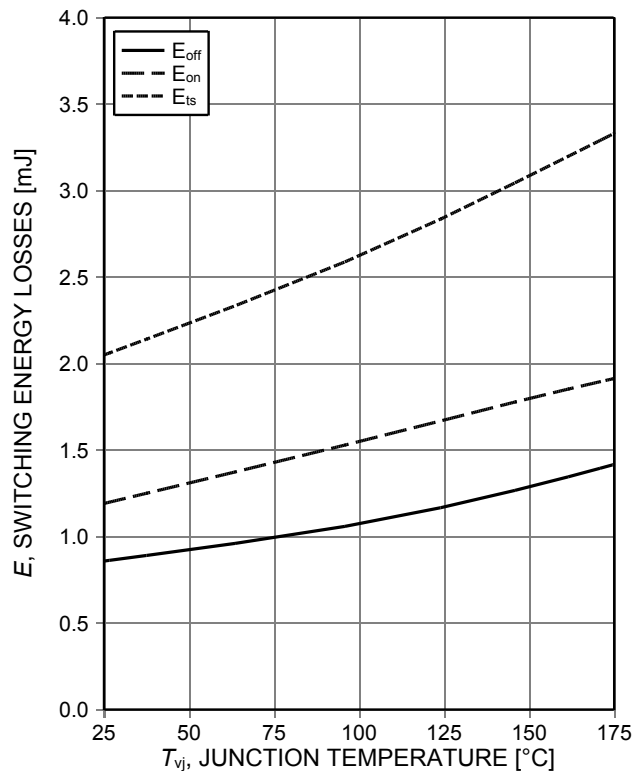


Figure 13. **Typical switching energy losses as a function of junction temperature**
(inductive load, $V_{CE}=400V$, $V_{GE}=15/0V$, $I_C=50A$, $R_G=9\Omega$, Dynamic test circuit in Figure E)

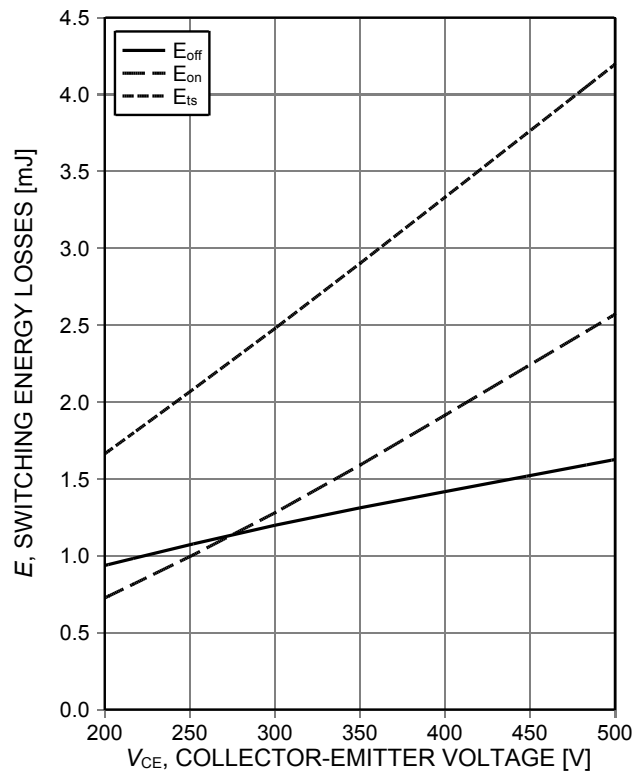


Figure 14. **Typical switching energy losses as a function of collector emitter voltage**
(inductive load, $T_{vj}=175^\circ C$, $V_{GE}=15/0V$, $I_C=50A$, $R_G=9\Omega$, Dynamic test circuit in Figure E)

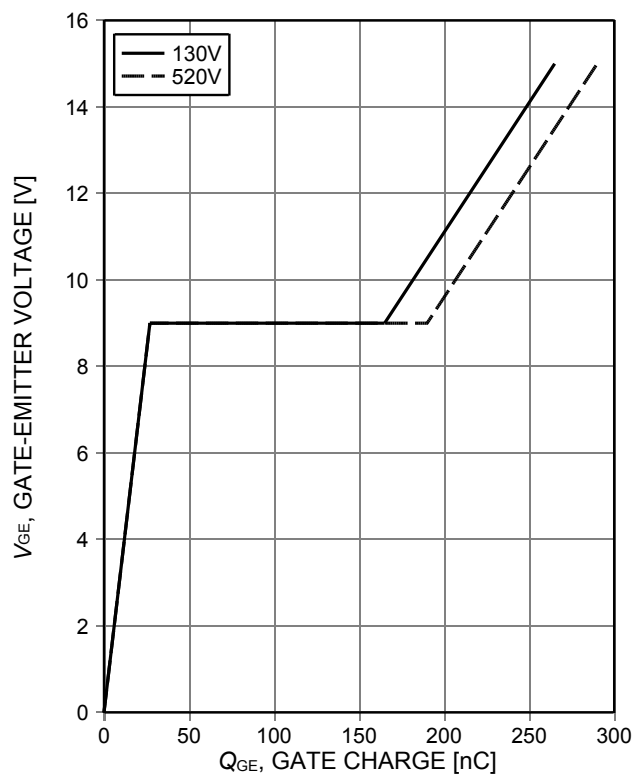


Figure 15. **Typical gate charge**
($I_C=50A$)

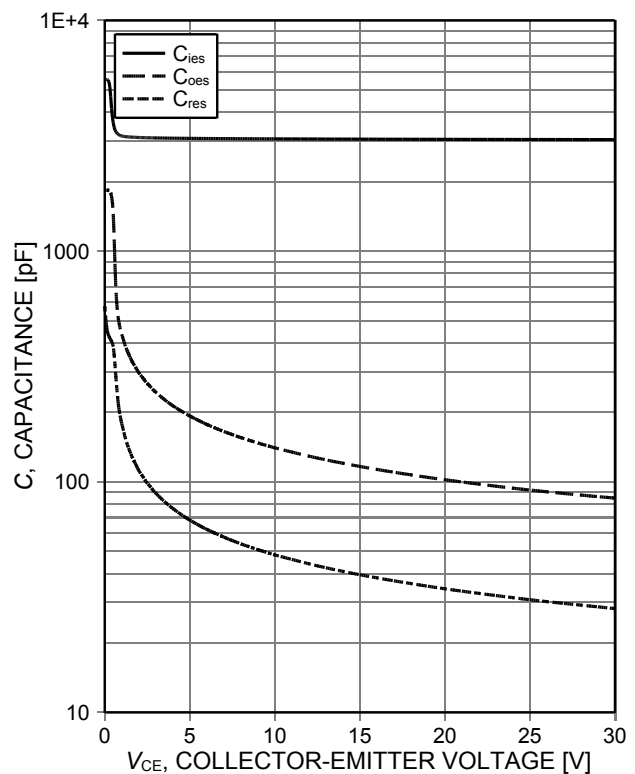


Figure 16. **Typical capacitance as a function of collector-emitter voltage**
($V_{GE}=0V$, $f=1MHz$)

TRENCHSTOP™ IGBT 7

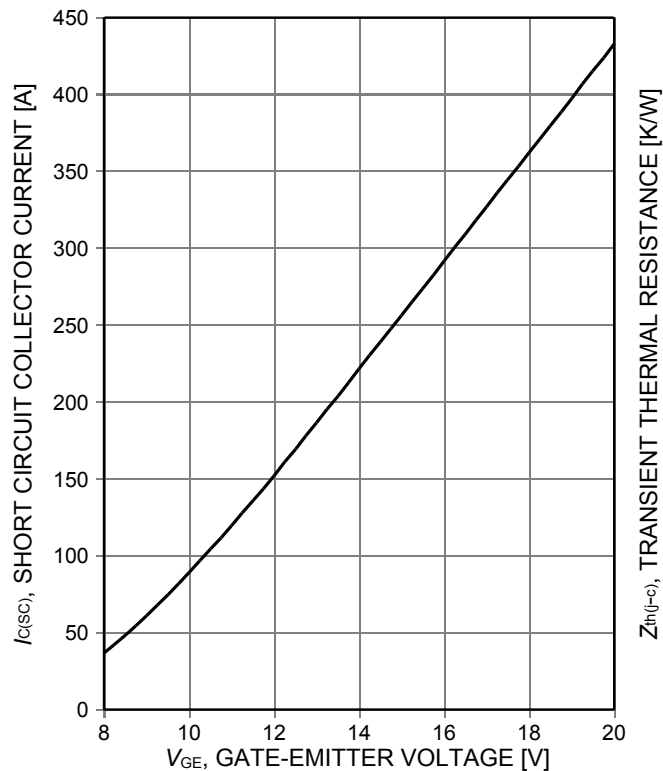


Figure 17. Typical short circuit collector current as a function of gate-emitter voltage ($V_{CE}=400V$, $T_{vj}=150^{\circ}C$)

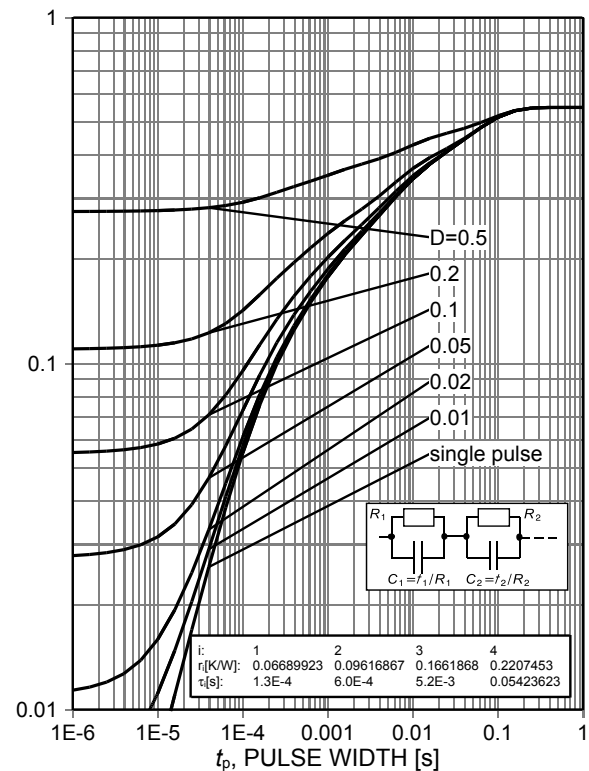


Figure 18. IGBT transient thermal resistance ($D=t_p/T$)

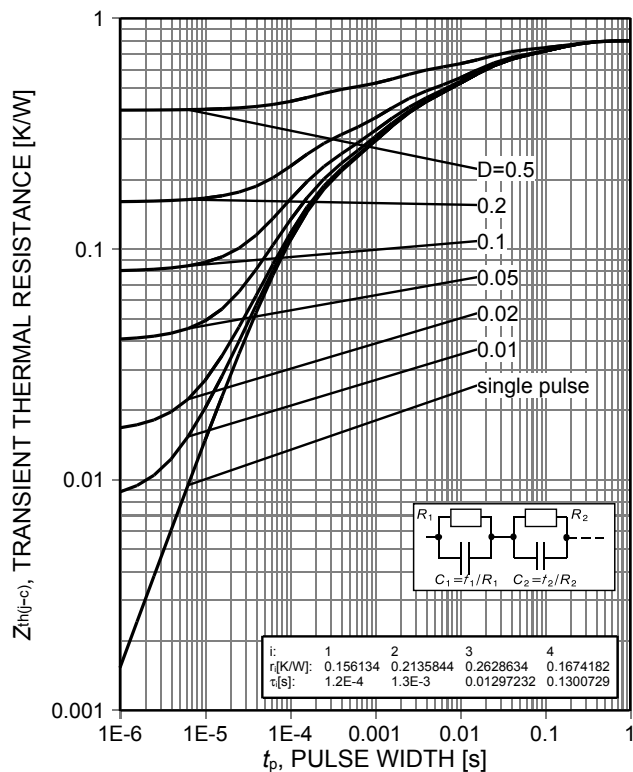


Figure 19. Diode transient thermal impedance as a function of pulse width ($D=t_p/T$)

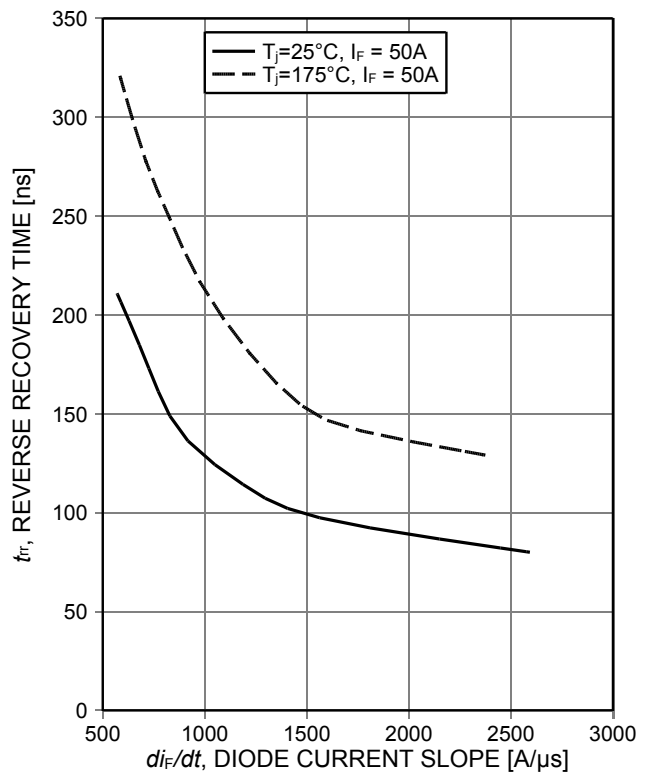


Figure 20. Typical reverse recovery time as a function of diode current slope ($V_R=400V$)

TRENCHSTOP™ IGBT 7

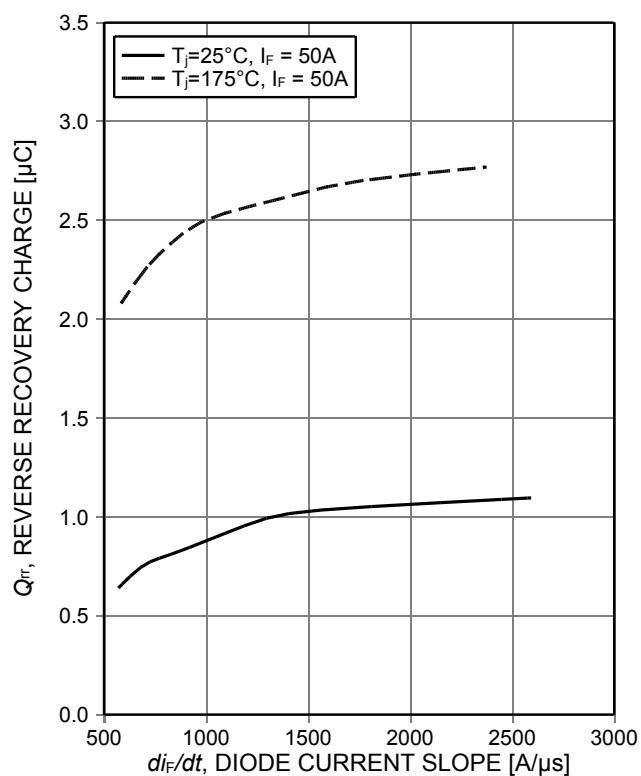


Figure 21. Typical reverse recovery charge as a function of diode current slope ($V_R=400\text{V}$)

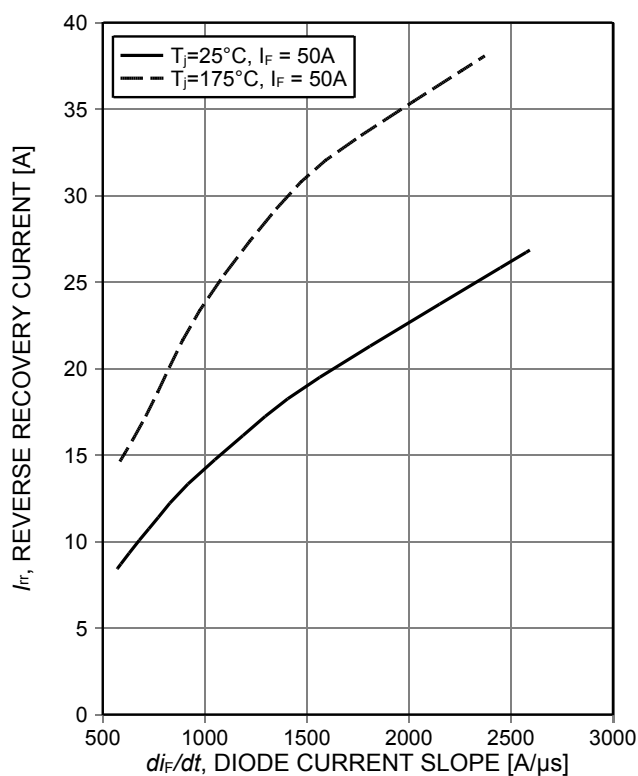


Figure 22. Typical reverse recovery current as a function of diode current slope ($V_R=400\text{V}$)

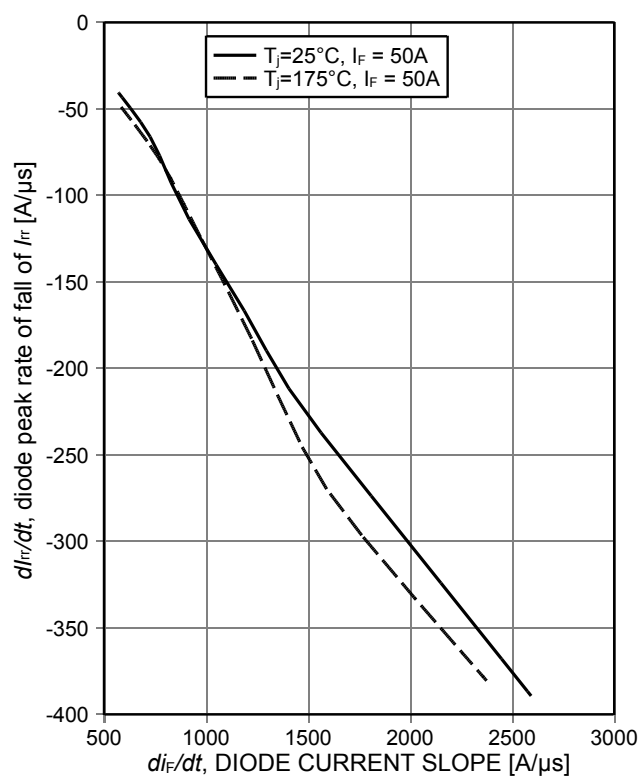


Figure 23. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope ($V_R=400\text{V}$)

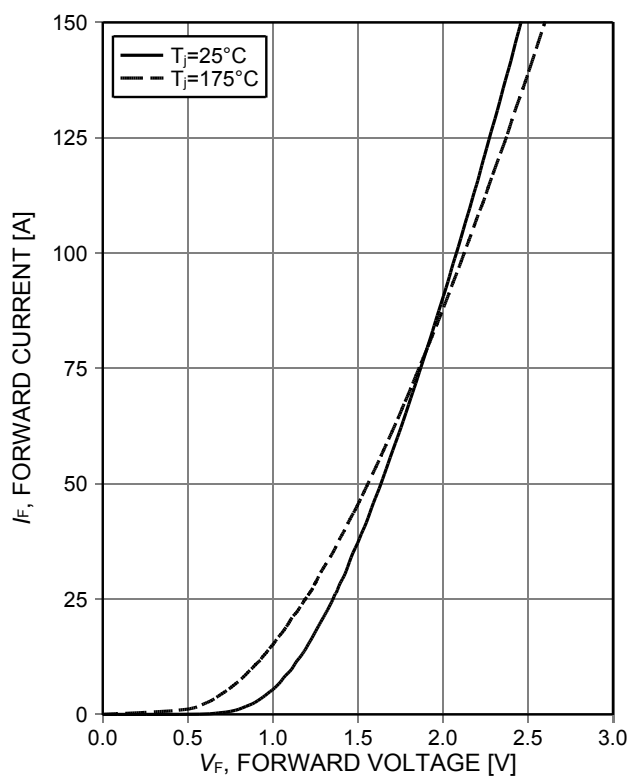


Figure 24. Typical diode forward current as a function of forward voltage

TRENCHSTOP™ IGBT 7

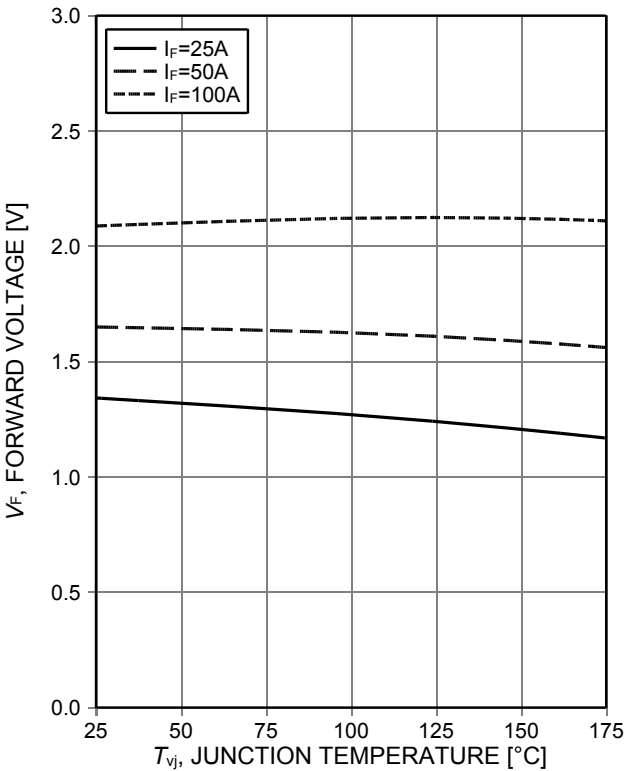
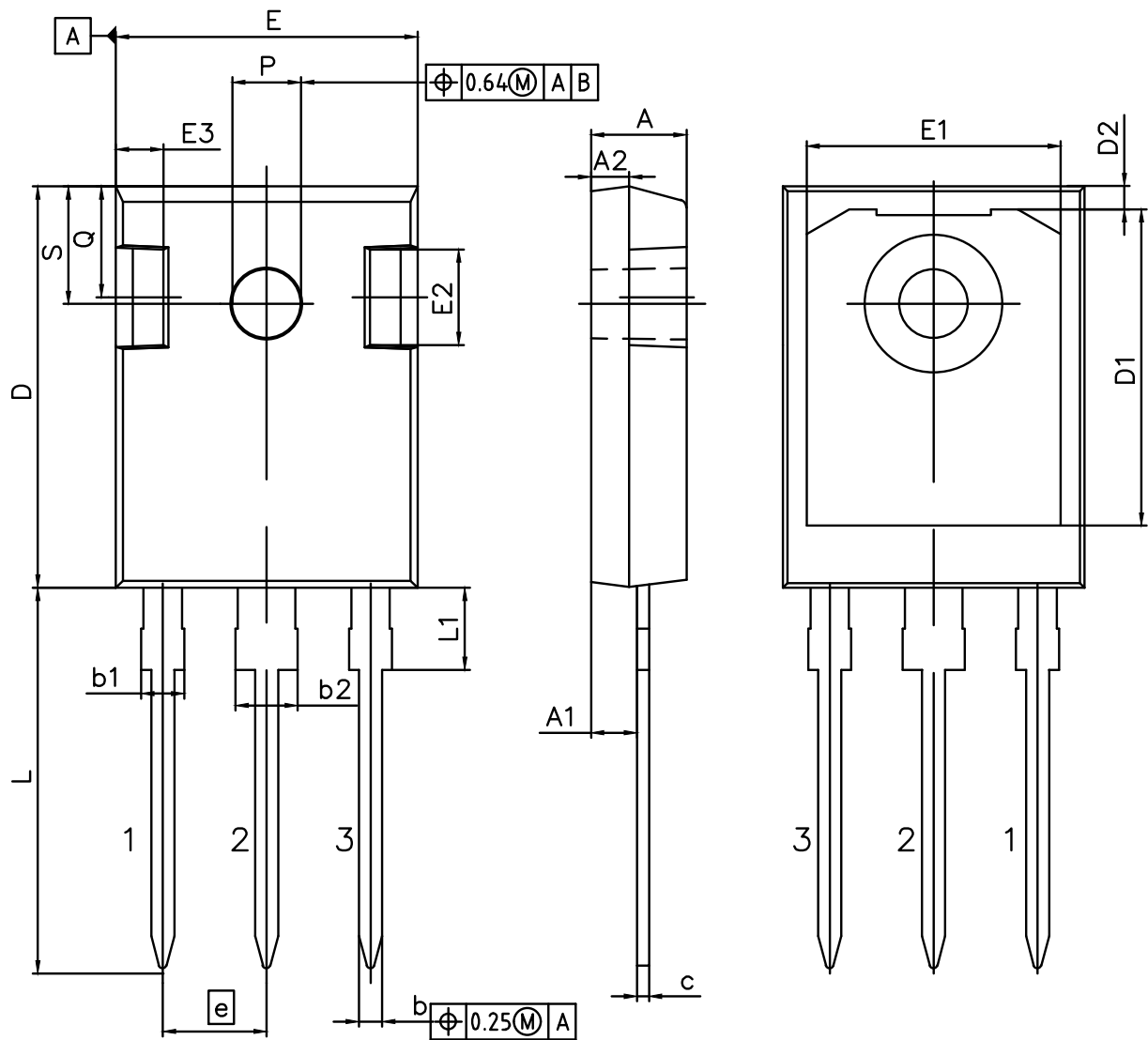


Figure 25. Typical diode forward voltage as a function of junction temperature

Package Drawing PG-TO247-3



DIMENSIONS	MILLIMETERS	
	MIN.	MAX.
A	4.70	5.30
A1	2.20	2.60
A2	1.50	2.50
b	1.00	1.40
b1	1.60	2.41
b2	2.57	3.43
c	0.38	0.89
D	20.70	21.50
D1	13.08	17.65
D2	0.51	1.35
E	15.50	16.30
E1	12.38	14.15
E2	3.40	5.10
E3	1.00	2.60
e	5.44	
L	19.80	20.40
L1	3.85	4.50
P	3.50	3.70
Q	5.35	6.25
S	6.04	6.30

DOCUMENT NO.
Z8B00003327

REVISION
06

SCALE 3:1
0 1 2 3 4 5mm

EUROPEAN PROJECTION

ISSUE DATE
25.07.2018

Testing Conditions

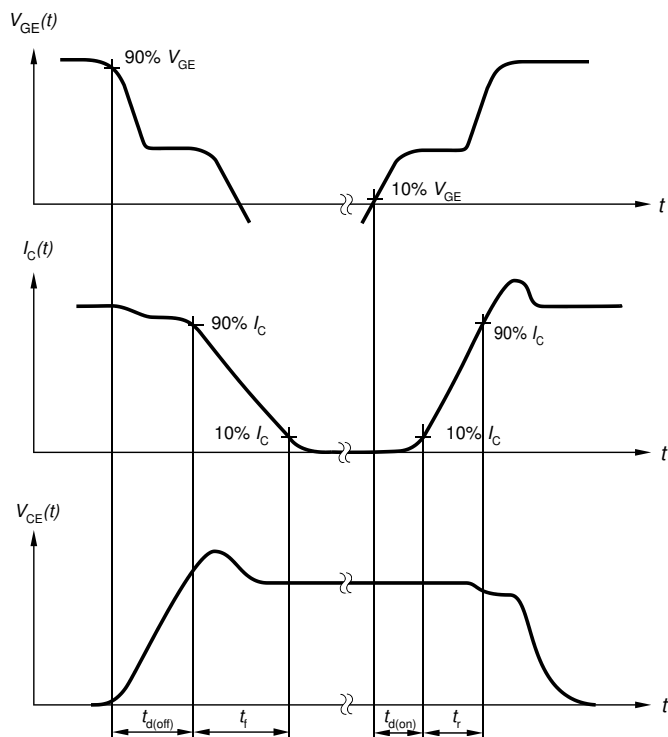


Figure A. Definition of switching times

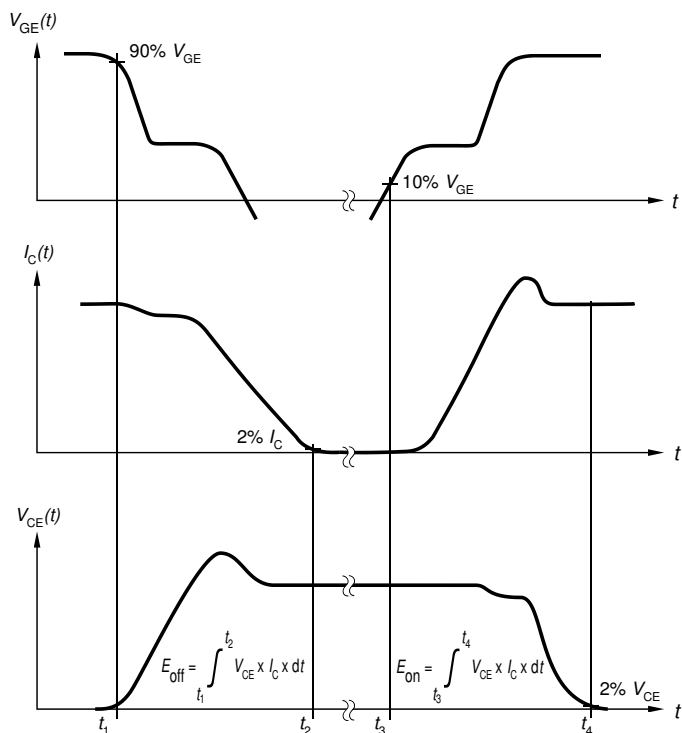


Figure B. Definition of switching losses

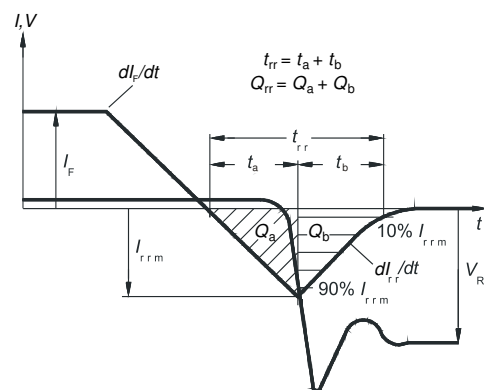


Figure C. Definition of diode switching characteristics

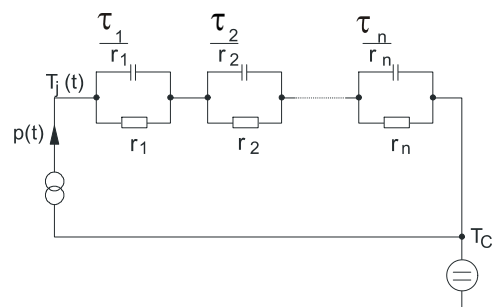


Figure D. Thermal equivalent circuit

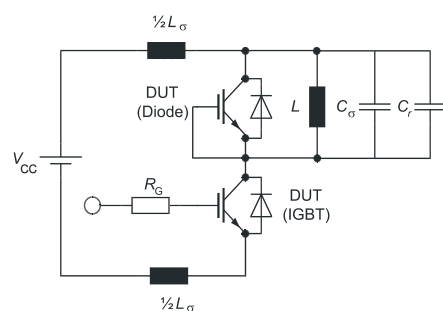


Figure E. **Dynamic test circuit**
 Parasitic inductance L_σ ,
 parasitic capacitor C_σ ,
 relief capacitor C_r ,
 (only for ZVT switching)

Revision History

IKW50N65ET7**Revision: 2020-05-12, Rev. 2.1**

Previous Revision

Revision	Date	Subjects (major changes since last revision)
0.1	2019-10-25	Target Data Sheet
1.1	2020-04-20	Preliminary data sheet
2.1	2020-05-12	Final data sheet

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