

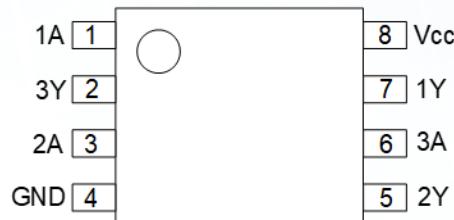
Description

The 74LVC3G04 is a triple inverter gate with standard push-pull outputs. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using I_{OFF} . The I_{OFF} circuitry disables the output preventing damaging current backflow when the device is powered down.

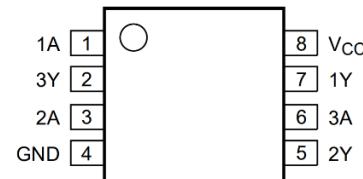
Each of the inverters performs the positive Boolean function:

$$Y = \overline{A}$$

Pin Assignments



SSOP-8



VSSOP-8

Features

- Wide Supply Voltage Range from 1.65V to 5.5V
- $\pm 24\text{mA}$ Output Drive at 3.3V
- CMOS Low-Power Consumption
- I_{OFF} Supports Partial Power Down Mode Operation
- Inputs Accept up to 5.5V
- ESD Protection Tested per JESD 22
Exceeds 2000V Human Body Model (A114)
Exceeds 1000V Charged Device Model (C101)
- Latch-Up Exceeds 100mA per JESD 78, Class I
- Range of Package Options
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](#) or your local Diodes representative.**

<https://www.diodes.com/quality/product-definitions/>

Applications

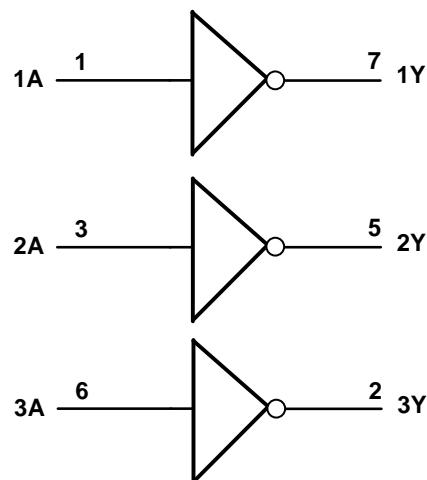
- Voltage-level shifting
- General-purpose logic
- Power down signal isolation
- Wide array of products such as:
 - PCs, networking, notebooks, netbooks, PDAs
 - Computer peripherals, hard drives, CD/DVD ROM
 - TV, DVD, DVR, set top boxes
 - Cell phones, personal navigation/GPS
 - MP3 players, cameras, video recorders

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Pin Descriptions

Pin Name	Pin NO.	Description
1A	1	Data Input
3Y	2	Data Output
2A	3	Data Input
GND	4	Ground
2Y	5	Data Output
3A	6	Data Input
1Y	7	Data Output
Vcc	8	Supply Voltage

Logic Diagram



Function Table

Inputs	Output
A	Y
H	L
L	H

Absolute Maximum Ratings (Notes 4, 5)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
Vcc	Supply Voltage Range	-0.5 to 6.5	V
Vi	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High Impedance or I _{OFF} State	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to V _{cc} + 0.5	V
I _{IK}	Input Clamp Current V _i < 0	-50	mA
I _{OK}	Output Clamp Current V _o < 0	-50	mA
I _O	Continuous Output Current	±50	mA
I _{CC} , I _{GND}	Continuous Current Through V _{cc} or GND	±100	mA
T _J	Junction Temperature	+150	°C
T _{STG}	Storage Temperature	-65 to +150	°C

Notes:

- 4. Stresses beyond the absolute maximum can result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.
- 5. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.

Recommended Operating Conditions (Note 6)

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	Operating Voltage	Operating	1.65	5.5	V
		Data Retention Only	1.5	—	V
V _{IH}	High-Level Input Voltage	V _{CC} = 1.65V to 1.95V	0.65 X V _{CC}	—	V
		V _{CC} = 2.3V to 2.7V	1.7	—	
		V _{CC} = 3V to 3.6V	2	—	
		V _{CC} = 4.5V to 5.5V	0.7 X V _{CC}	—	
V _{IL}	Low-Level Input Voltage	V _{CC} = 1.65V to 1.95V	—	0.35 X V _{CC}	V
		V _{CC} = 2.3V to 2.7V	—	0.7	
		V _{CC} = 3V to 3.6V	—	0.8	
		V _{CC} = 4.5V to 5.5V	—	0.3 X V _{CC}	
V _I	Input Voltage	—	0	5.5	V
V _O	Output Voltage	—	0	V _{CC}	V
I _{OH}	High-Level Output Current	V _{CC} = 1.65V	—	-4	mA
		V _{CC} = 2.3V	—	-8	
		V _{CC} = 3V	—	-16	
			—	-24	
I _{OL}	Low-Level Output Current	V _{CC} = 4.5V	—	-32	mA
		V _{CC} = 1.65V	—	4	
		V _{CC} = 2.3V	—	8	
		V _{CC} = 3V	—	16	
			—	24	
Δt/ΔV	Input Transition Rise or Fall Rate	V _{CC} = 4.5V	—	32	ns/V
		V _{CC} = 1.8V ± 0.15V, 2.5V ± 0.2V	—	20	
		V _{CC} = 1.65V to 2.7V	—	10	
T _A	Operating Free-Air Temperature	V _{CC} = 2.7V to 5V	—	5	°C
		—	-40	+125	

Note: 6. Unused inputs should be held at V_{CC} or Ground for device proper operation.

Electrical Characteristics

Symbol	Parameter	Test Conditions	V _{cc}	-40°C to +85°C		-40°C to +125°C		Unit
				Min	Max	Min	Max	
V _{OH}	High-Level Output Voltage	I _{OH} = -100µA	1.65V to 5.5V	V _{cc} – 0.1	—	V _{cc} – 0.1	—	V
		I _{OH} = -4mA	1.65V	1.2	—	1.2	—	
		I _{OH} = -8mA	2.3V	1.9	—	1.9	—	
		I _{OH} = -16mA	3V	2.4	—	2.4	—	
		I _{OH} = -24mA		2.3	—	2.3	—	
		I _{OH} = -32mA	4.5V	3.8	—	3.8	—	
V _{OL}	Low-Level Output Voltage	I _{OL} = 100µA	1.65V to 5.5V	—	0.1	—	0.1	V
		I _{OL} = 4mA	1.65V	—	0.45	—	0.45	
		I _{OL} = 8mA	2.3V	—	0.3	—	0.3	
		I _{OL} = 16mA	3V	—	0.4	—	0.4	
		I _{OL} = 24mA		—	0.55	—	0.55	
		I _{OL} = 32mA	4.5V	—	0.55	—	0.75	
I _I	Input Current	V _I = 5.5V or GND	0 to 5.5V	—	±5	—	±5	µA
I _{OFF}	Power Down Leakage Current	V _I or V _O = 5.5V	0	—	±10	—	±10	µA
I _{CC}	Supply Current	V _I = 5.5V or GND, I _O = 0	1.65V to 5.5V	—	10	—	10	µA
ΔI _{CC}	Additional Supply Current	Input at V _{cc} – 0.6 V	3V to 5.5V	—	500	—	500	µA
C _I	Input Capacitance	V _I = V _{cc} or GND	3.3V	—	3.5 (Typ)	—	—	pF

Package Characteristics

Symbol	Parameter	Package	Test Conditions	Min	Typ	Max	Unit
θ _{JA}	Thermal Resistance Junction-to-Ambient	SSOP-8	Note 7	—	130	—	°C/W
		VSSOP-8	Note 7	—	155	—	°C/W
θ _{JC}	Thermal Resistance Junction-to-Case	SSOP-8	Note 7	—	36	—	°C/W
		VSSOP-8	Note 7	—	38	—	°C/W

Note: 7. Test condition: Device mounted on JEDEC 2s2p High-K board, FR-4 substrate PCB, 2oz copper with minimum recommended pad layout.

Operating Characteristics (T_A = +25°C, V_{cc} = 3.3V)

Symbol	Parameter	Test Conditions	V _{cc} = 1.8V	V _{cc} = 2.5V	V _{cc} = 3.3V	V _{cc} = 5V	Unit
			Typ	Typ	Typ	Typ	
C _{PD}	Power Dissipation Capacitance	f = 10MHz 1 Input Switching	16	16	18	22	pF

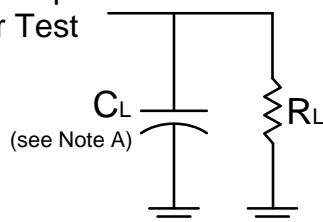
Switching Characteristics

Figure 1

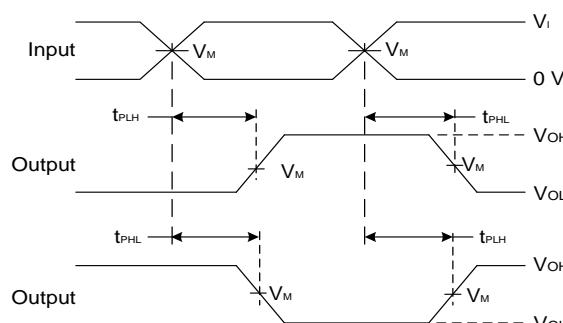
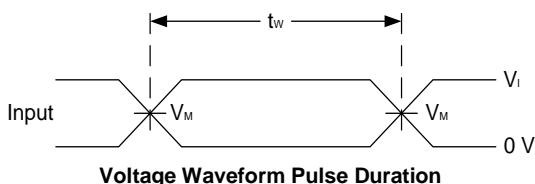
Parameter	From Input	To Output	V _{cc}	T _A = -40°C to +85°C		T _A = -40°C to +125°C		Unit
				Min	Max	Min	Max	
t _{PD}	A	Y	1.8V ± 0.15V	3.2	7.9	3.2	8.9	ns
			2.5V ± 0.2V	1.5	4.4	1.5	5.4	
			3.3V ± 0.3V	1.4	4.1	1.4	5.1	
			5.0V ± 0.5V	1.1	3.2	1.1	3.8	

Parameter Measurement Information (Notes B, C, D)

From Output
Under Test



V _{cc}	Inputs		V _M	C _L	R _L
	V _I	t _r /t _f			
1.8V ± 0.15V	V _{cc}	≤2ns	V _{cc} / 2	30pF	1kΩ
2.5V ± 0.2V	V _{cc}	≤2ns	V _{cc} / 2	30pF	500Ω
3.3V ± 0.3V	3V	≤2.5ns	1.5V	50pF	500Ω
5.0V ± 0.5V	V _{cc}	≤2.5ns	V _{cc} / 2	50pF	500Ω



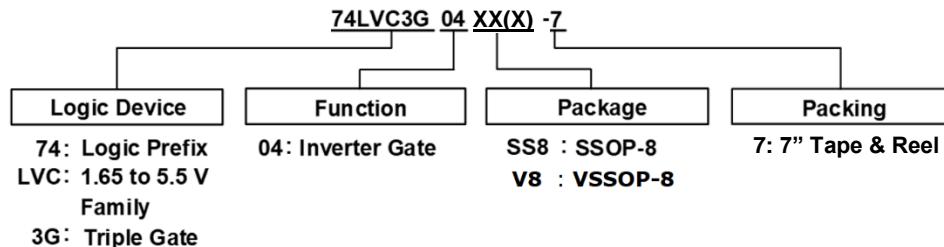
Voltage Waveform Propagation Delay Times
Inverting and Noninverting Outputs

Figure 1. Load Circuit and Voltage Waveforms

Notes:

- Includes test lead and test apparatus capacitance.
- All pulses are supplied at pulse repetition rate ≤ 10MHz.
- Inputs are measured separately one transition per measurement.
- t_{PLH} and t_{PHL} are the same as t_{PD}.

Ordering Information



Part Number	Package Code	Package	Packing	
			Qty.	Carrier
74LVC3G04SS8-7	SS8	SSOP-8	3000	7" Tape and Reel (Note 8)
74LVC3G04V8-7	V8	VSSOP-8	3000	7" Tape and Reel (Note 8)

Note: 8. The taping orientation is located on our website at <http://www.diodes.com/package-outlines.html>.

Marking Information

SSOP-8



Y : Year : 0 to 9
 W : Week : A to Z : 1 to 26 week;
 a to z : 27 to 52 week; z represents
 52 and 53 week
 X : Internal Code

Part Number	Package	Identification Code
74LVC3G04SS8-7	SSOP-8	S3G04

VSSOP-8



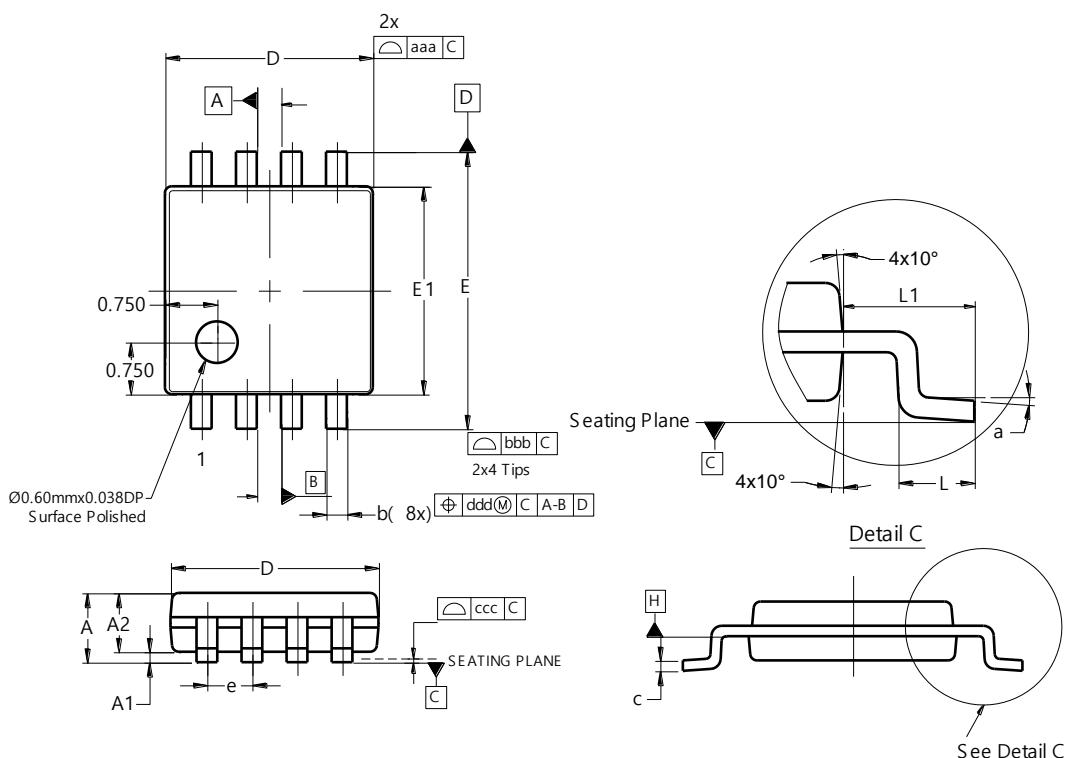
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Part Number	Package	Identification Code
74LVC3G04V8-7	VSSOP-8	V3G04

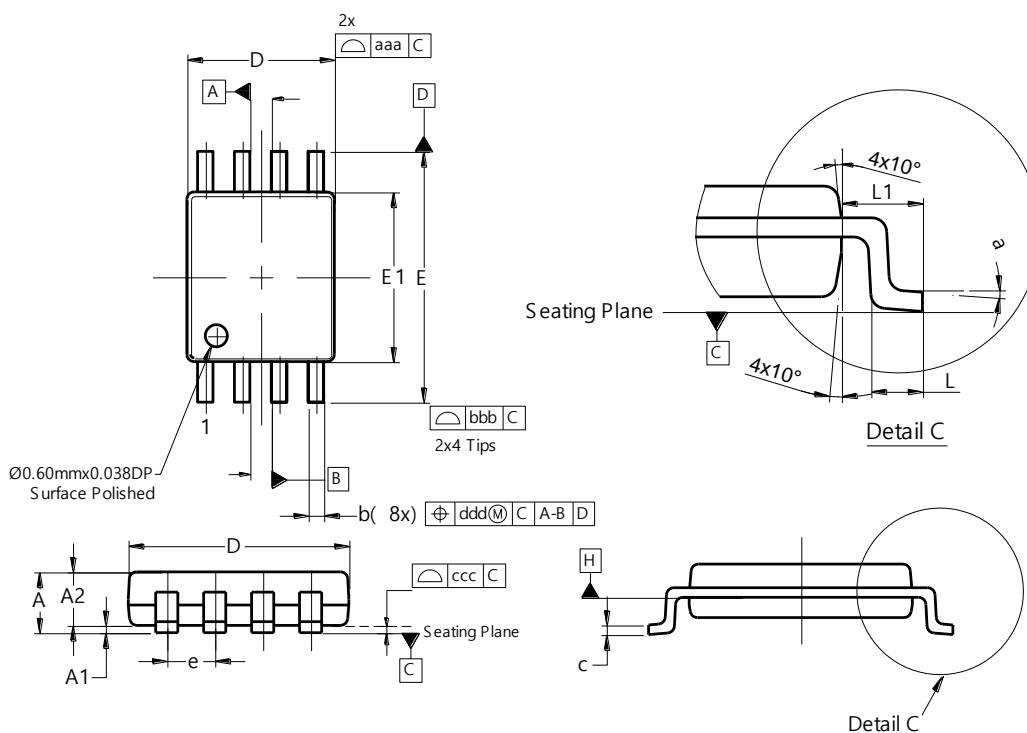
Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SSOP-8



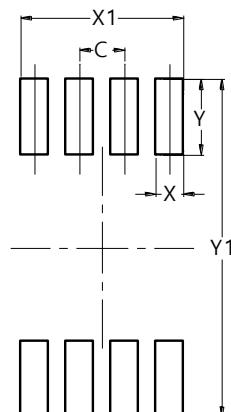
VSSOP-8



Suggested Pad Layout

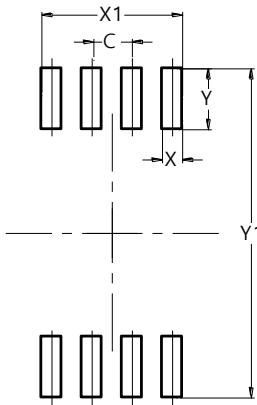
Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SSOP-8



Dimensions	Value (in mm)
C	0.650
X	0.400
X1	2.350
Y	1.100
Y1	4.900

VSSOP-8



Dimensions	Value (in mm)
C	0.500
X	0.250
X1	1.750
Y	0.750
Y1	4.050

Mechanical Data

SSOP-8

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Mate Tin Plated Leads, Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.0169 grams (Approximate)

VSSOP-8

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Mate Tin Plated Leads, Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.011 grams (Approximate)

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