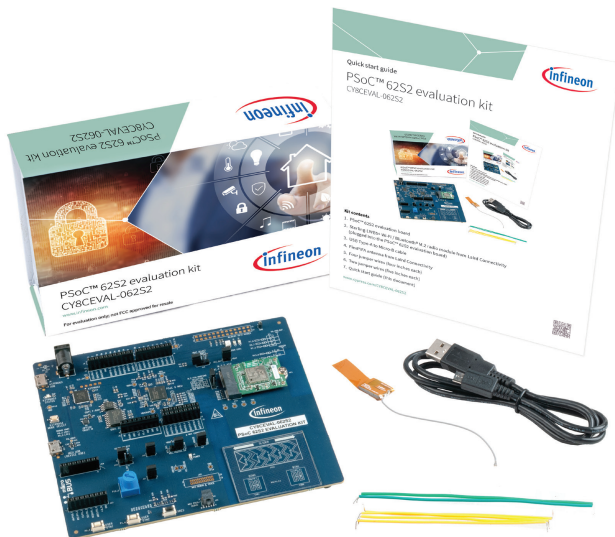


Quick start guide

PSoC™ 62S2 evaluation kit

CY8CEVAL-062S2



Kit contents

1. PSoC™ 62S2 evaluation board
2. Sterling-LWB5+ Wi-Fi / Bluetooth® M.2 radio module from Laird Connectivity (plugged into the PSoC™ 62S2 evaluation board)
3. USB Type-A to Micro-B cable
4. FlexPIFA antenna from Laird Connectivity
5. Four jumper wires (four inches each)
6. Two jumper wires (five inches each)
7. Quick start guide (this document)



Before you start

1. Ensure that you have the following:
 - PC with USB port
 - UART terminal software such as Tera Term or Minicom
2. Visit the [kit website](#) to download and install the required software.
3. Ensure that jumper J18 is at position 3-5 to select 3.3 V.
4. Connect the KitProg3 USB connector (J9) to your PC.
5. Wait for the driver installation to complete.

Connect the kit with the UART terminal software

1. Open the UART terminal software and connect to the kit's USB-UART COM port with the following settings:
 - Baud rate: 115200, Data: 8 bit, Parity: None, Stop bit: 1 bit, Flow control: None
2. Press the XRES switch (SW1) to reset the device.

Run the pre-programmed code example

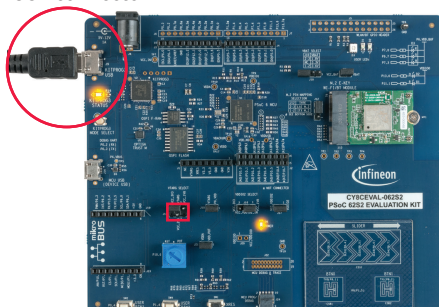
1. Observe the “Hello World!!!” message on the serial terminal and confirm that the user LED blinks at 1 Hz.
2. Press the **Enter** key to pause or resume blinking the user LED.

Next Steps

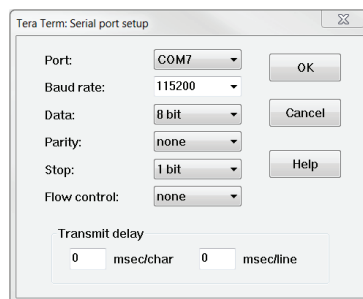
Visit the [kit website](#) for information on code examples supported for this kit and kit documentation.

Note: The Sterling-LWB5+ M.2 radio module from Laird Connectivity requires an external antenna for wireless connectivity. The FlexPIFA antenna is not connected to the module by default. Follow the instructions in the “Kit operation” chapter of the kit guide to connect the antenna to the module.

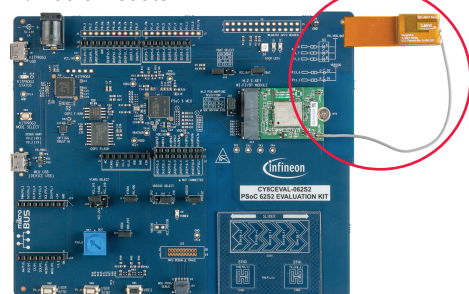
USB cable connected to the KitProg3 USB connector



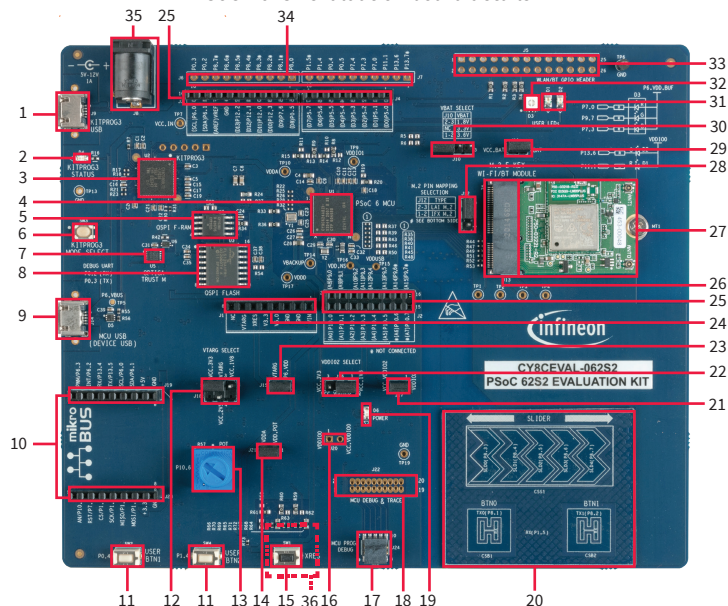
USB-UART COM port setup



PIFA antenna connected to Sterling-LWB5+ M.2 radio module



PSoC™ 62S2 evaluation board details

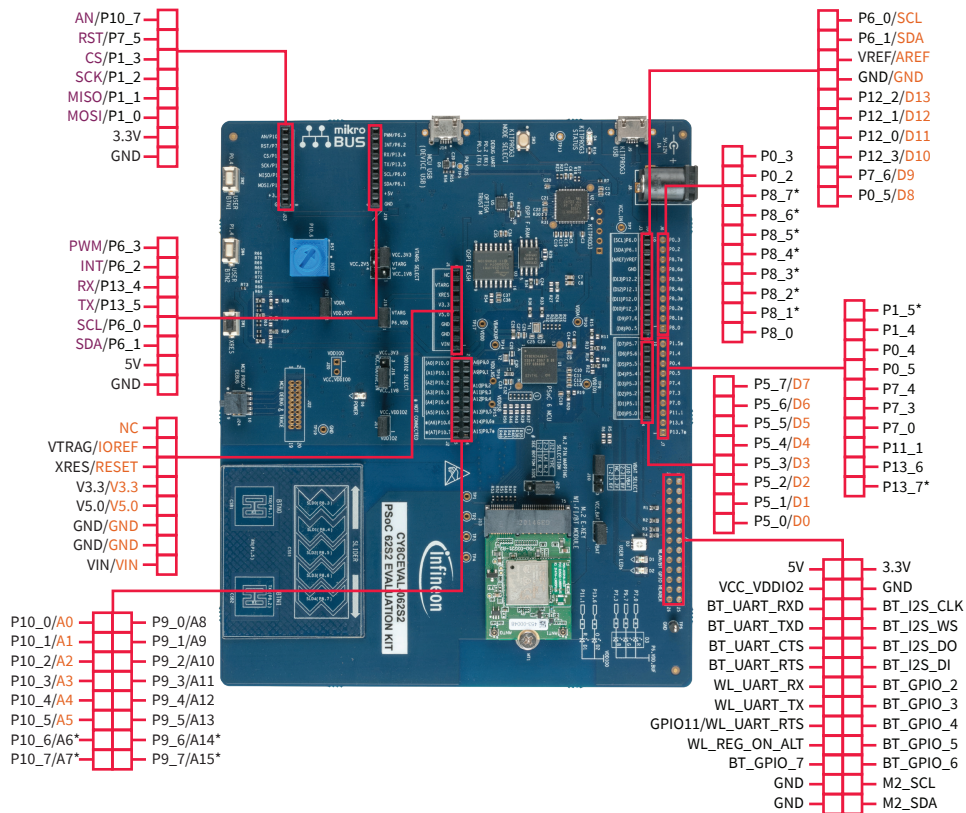


- | | |
|---|--|
| 1 KitProg3 USB connector (J9) | 19 Power LED (D6) |
| 2 KitProg3 status LED (D4) | 20 CAPSENSE™ slider (CSS1) and buttons (CSB1, CSB2) |
| 3 KitProg3 (PSoC™ 5LP) programmer and debugger (CY8C5868TI-LP039, U2) | 21 PSoC™ 6 MCU VDDIO2 current measurement jumper (J17) |
| 4 PSoC™ 6 MCU (CY8C624ABZI-S2D44, U1) | 22 PSoC™ 6 MCU VDDIO2 power selection jumper (J16) |
| 5 QSPI F-RAM (CY15B104QSN, U4) | 23 PSoC™ 6 MCU VTARG current measurement jumper (J15) |
| 6 KitProg3 programming mode selection button (SW3) | 24 Power header compatible with Arduino Uno R3 (J1) |
| 7 OPTIGA™ Trust M security controller (SLS25AIA, U5) | 25 I/O headers compatible with Arduino Uno R3 (J2, J3, J4) |
| 8 512-Mbit serial NOR flash memory (S25FL512S, U3) | 26 M.2 interface connector (J13) |
| 9 PSoC™ 6 MCU USB device connector (J14) | 27 Sterling-LWB5+ M.2 module from Laird Connectivity |
| 10 Headers compatible with mikroBUS by Mikroelektronika (J19, J23) | 28 Custom M.2 interface selection jumper (J12) |
| 11 PSoC™ 6 MCU user buttons (SW2, SW4) | 29 VBAT current measurement jumper (J11) |
| 12 System power (VTARG) selection jumper (J18) | 30 VBAT power selection jumper (J10) |
| 13 Potentiometer (R57) | 31 PSoC™ 6 MCU user LEDs (D1, D2) |
| 14 Potentiometer connection jumper (J21) | 32 RGB LED (D3) |
| 15 PSoC™ 6 MCU reset button (SW1) | 33 GPIO header (J5) for AIROC™ Wi-Fi & Bluetooth® combo chips* |
| 16 PSoC™ 6 MCU VDDIO0 current measurement jumper (J20)* | 34 PSoC™ 6 MCU I/O headers (J6, J7)* |
| 17 PSoC™ 6 MCU 10-pin SWD/JTAG program and debug header (J24) | 35 External power supply VIN connector (J8) |
| 18 PSoC™ 6 MCU debug and trace header (J22)* | 36 microSD card holder (J27)** |

* Footprint only, not populated on the board

** Component is located at the bottom side of the board

PSoC™ 62S2 evaluation board pinout details



LEGEND

■ I/Os compatible with Arduino Uno R3
 ■ PSoC™ 6 MCU I/Os
 ■ Interface compatible with mikroBUS by Mikroelektronika

Note: *Not connected

See the kit guide available at www.cypress.com/CY8CEVAL-062S2 for details.

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