

MAXIM INTEGRATED Power Management ICs



Power Management

Maxim Integrated

RoHS Compliant This product is RoHS compliant.

NON-VOLATILE RAM CONTROLLERS

◆ Surface Mount Device
For quantities greater than listed, call for quote.

| MOUSER STOCK NO. | Maxim Integrated Part No. | Package | CE Controller | Smart Socket with Int. Batt. | Li Batt. Monitor | Power Fail Out | Supply Voltage (Typ.) (V) | I _{CC} (Max.) (mA) | RAMs Controlled | Price Each | | |
|------------------|---------------------------|----------|---------------|------------------------------|------------------|----------------|---------------------------|-----------------------------|-----------------|------------|------|------|
| | | | | | | | | | | 1 | 25 | 100 |
| 700-DS1210 | DS1210+ | PDIP-8 | Yes | No | No | No | 5 | 5 | 1 | 5.23 | 2.65 | 2.44 |
| 700-DS1210N | DS1210N+ | PDIP-8 | Yes | No | No | No | 5 | 5 | 1 | 6.14 | 3.12 | 2.87 |
| 700-DS1210S | DS1210S+ | SOIC-16W | Yes | No | No | No | 5 | 5 | 1 | 5.63 | 2.86 | 2.63 |
| 700-DS1210SN | DS1210SN+ | SOIC-16W | Yes | No | No | No | 5 | 5 | 1 | 6.91 | 3.51 | 3.23 |
| 700-DS1218S | DS1218S+ | SOIC-8 | Yes | No | No | No | 5 | 5 | 1 | 14.94 | 7.58 | 6.97 |
| 700-DS1312S | DS1312S+ | SOIC-16W | Yes | No | Yes | No | 5 | 0.4 | 1 | 17.34 | 8.80 | 8.09 |
| 700-DS1312S-2 | DS1312S-2+ | SOIC-8 | Yes | No | Yes | No | 5 | 0.4 | 1 | 13.80 | 7.00 | 6.44 |
| 700-DS1314S | DS1314S+ | SOIC-16W | Yes | No | Yes | No | 3.3 | 0.2 | 1 | 14.17 | 7.19 | 6.61 |
| 700-DS1314S-2 | DS1314S-2+ | SOIC-8 | Yes | No | Yes | No | 3.3 | 0.2 | 1 | 18.11 | 9.19 | 8.45 |
| 700-DS1321S | DS1321S+ | SOIC-16 | Yes | No | Yes | No | 5 | 1.5 | 4 | 11.49 | 5.83 | 5.36 |
| 700-DS1218 | DS1218+ | PDIP-8 | Yes | No | No | No | 5 | 5 | 1 | 13.26 | 6.72 | 6.19 |

BATTERY BACKUP CIRCUIT ICs

◆ Surface Mount Device
For quantities greater than listed, call for quote.

| MOUSER STOCK NO. | Maxim Integrated Part No. | Package | Reset Thresh. (V) | Reset Out | ICC (µA) (Max.) | Features | Price Each | | |
|-------------------|---------------------------|----------|-----------------------|---------------------------------------------|-----------------|--------------------------------------------------------------------------|------------|------|------|
| | | | | | | | 1 | 25 | 100 |
| 700-MAX696EWE | MAX696EWE+ | SOIC-16W | 1.2 to 1.8 | Active High and Low/Push-Pull | 4000 | Adj RS, Batt on, Pwr Fail, WD | 13.77 | 6.99 | 6.43 |
| 700-MAX696CPE | MAX696CPE+ | PDIP-16 | 1.2 to 1.8 | Active High/Active Low/Push-Pull | 4000 | Adj. RS, Batt On, Low Line, Pwr Fail, WD | 9.06 | 6.04 | 4.41 |
| 700-MAX696CWE | MAX696CWE+ | SOIC-16W | 1.2 to 1.8 | Active High/Active Low/Push-Pull | 4000 | Adj. RS, Batt On, Low Line, Pwr Fail, WD | 9.98 | 6.65 | 4.82 |
| 700-MAX16021LTEL | MAX16021LTEL+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.47 |
| 700-MAX16021LTEM | MAX16021LTEM+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.47 |
| 700-MAX16021LTER | MAX16021LTER+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.32 |
| 700-MAX16021LTES | MAX16021LTES+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.32 |
| 700-MAX16021LTET | MAX16021LTET+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.47 |
| 700-MAX16021LTEV | MAX16021LTEV+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.47 |
| 700-MAX16021LTEW | MAX16021LTEW+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.47 |
| 700-MAX16021LTEY | MAX16021LTEY+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.47 |
| 700-MAX16021LTEZ | MAX16021LTEZ+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.47 |
| 700-MAX16021PTEL | MAX16021PTEL+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.47 |
| 700-MAX16021PTM | MAX16021PTM+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.47 |
| 700-MAX16021PTER | MAX16021PTER+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.47 |
| 700-MAX16021PTES | MAX16021PTES+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.32 |
| 700-MAX16021PTET | MAX16021PTET+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.47 |
| 700-MAX16021PTEV | MAX16021PTEV+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.47 |
| 700-MAX16021PTEW | MAX16021PTEW+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.47 |
| 700-MAX16021PTEY | MAX16021PTEY+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.47 |
| 700-MAX16021PTEZ | MAX16021PTEZ+ | TQFN-16 | 1.2 to 5.5 | Active High/Active Low/Open Drain/Push-Pull | 5 | Batt Fresh, Chip Enable, Batt On, Low Line, Man Reset, Pwr Fail Comp, WD | 8.94 | 5.96 | 4.47 |
| 700-MAX704SCSA | MAX704SCSA+ | SOIC-8 | 2.5 to 3.0 | Active Low/Push-Pull | 50 | Adj Reset, Man Reset, Pwr Fail | 7.96 | 3.84 | 3.53 |
| 700-MAX704RESA | MAX704RESA+ | SOIC-8 | 2.5 to 3.0 | Active Low/Push-Pull | 50 | Adj RS, Manual RS, Pwr Fail | 9.86 | 5.00 | 4.60 |
| 700-MAX6366PKA29T | MAX6366PKA29T | SOT-8 | 2.5 to 3.3 | Push-Pull/Active Low/Open Drain | 30 | Manual RS, Batt on, Adj. RS, WD | 4.65 | 3.51 | 3.07 |
| 700-MAX795SCSA | MAX795SCSA+ | SOIC-8 | 2.5 to 3.3 | Active Low/Open Drain | 50 | Batt on, Chip Enable Gate | 9.20 | 4.67 | 4.29 |
| 700-MAX793RCSE | MAX793RCSE+ | SOIC-16 | 2.5 to 3.3 | Active High and Lo/OD and PP | 60 | Adj RS, Batt on, Pwr Fail, WD | 9.41 | 6.58 | 4.39 |
| 700-MAX793SCSE | MAX793SCSE+ | SOIC-16 | 2.5 to 3.3 | Active High and Lo/OD and PP | 60 | Adj RS, Batt on, Pwr Fail, WD | 9.40 | 4.77 | 4.39 |
| 700-MAX793SESE | MAX793SESE+ | SOIC-16 | 2.5 to 3.3 | Active High and Lo/OD and PP | 60 | Adj RS, Batt on, Pwr Fail, WD | 12.23 | 8.56 | 5.71 |
| 700-MAX793TCSE | MAX793TCSE+ | SOIC-16 | 2.5 to 3.3 | Active High and Lo/OD and PP | 60 | Adj RS, Batt on, Pwr Fail, WD | 9.40 | 4.77 | 4.39 |
| 700-MAX793TESE | MAX793TESE+ | SOIC-16 | 2.5 to 3.3 | Active High and Lo/OD and PP | 60 | Adj RS, Batt on, Pwr Fail, WD | 12.23 | 6.20 | 5.71 |
| 700-MAX6361LUT26T | MAX6361LUT26T | SOT-23-6 | 2.5 to 3.3/3.3 | Active High/Open Drain | 50 | Manual Reset | 3.77 | 2.84 | 2.48 |
| 700-MAX704CPA | MAX704CPA+ | PDIP-8 | 2.5 to 3.3/3.3 to 5.5 | Active Low/Push-Pull | 350 | Adj Reset, Man Reset, Pwr Fail | 6.71 | 3.41 | 3.13 |
| 700-MAX704CSA | MAX704CSA+ | SOIC-8 | 2.5 to 3.3/3.3 | Active Low/Push-Pull | 350 | Adj Reset, Man Reset, Pwr Fail | 6.71 | 3.41 | 3.13 |
| 700-MAX704TCSA | MAX704TCSA+ | SOIC-8 | 3.0 to 3.5 | Active Low/Push-Pull | 50 | Adj Reset, Man Reset, Pwr Fail | 7.57 | 3.84 | 3.53 |
| 700-MAX690TESA | MAX690TESA+ | SOIC-8 | 3.0 to 3.5 | Active Low/Push-Pull | 50 | Adj RS, Pwr Fail, WD | 10.63 | 5.39 | 4.96 |
| 700-MAX806TCSA | MAX806TCSA+ | SOIC-8 | 3.0 to 3.5 | Active Low/Push-Pull | 50 | Adj RS, Manual RS, Pwr Fail | 12.00 | 8.00 | 5.80 |
| 700-MAX818LCSA | MAX818LCSA+ | SOIC-8 | 3.3 to 5.5 | Active Low/Push-Pull | 45 | Batt Fresh, Chip Enable, WD | 7.68 | 5.12 | 3.71 |
| 700-MAX817LESAs | MAX817LESAs | SOIC-8 | 3.3 to 5.5 | Active Low/Push-Pull | 60 | Adj RS, Batt Fresh, Pwr Fail, WD | 8.54 | 5.69 | 4.13 |
| 700-MAX791CPE | MAX791CPE+ | PDIP-16 | 3.3 to 5.5 | Active Low/Push-Pull | 150 | Adj RS, Batt on, Pwr Fail, WD | 9.31 | 4.72 | 4.35 |
| 700-MAX791CSE | MAX791CSE+ | SOIC-16 | 3.3 to 5.5 | Active Low/Push-Pull | 150 | Adj RS, Batt on, Pwr Fail, WD | 9.80 | 4.97 | 4.57 |
| 700-MAX703CSA | MAX703CSA+ | SOIC-8 | 3.3 to 5.5 | Active Low/Push-Pull | 350 | Adj Reset, Man Reset, Pwr Fail | 6.71 | 3.41 | 3.13 |
| 700-MXD1210CSA | MXD1210CSA+ | SOIC-8 | 3.3 to 5.5 | - | 500 | Chip Enable Gate | 6.40 | 3.25 | 2.99 |
| 700-MAX692EPA | MAX692EPA+ | PDIP-8 | 4.0 to 4.5 | Active Low/Push-Pull | 5000 | Adj RS, Pwr Fail, WD | 12.12 | 8.08 | 5.86 |
| 700-MAX693EWE | MAX693EWE+ | SOIC-16W | 4.0 to 4.5 | Active High/Active Low/Push-Pull | 5000 | Adj RS, Pwr Fail, WD | 14.47 | 9.65 | 7.00 |
| 700-MAX807LCUE | MAX807LCUE+ | TSOP-16 | 4.0 to 5.0 | Active High/Active Low/Push-Pull | 110 | Adj RS, Batt On, Chip Enable, Low Line, Manual RS, Pwr Fail, WD | 9.40 | 6.58 | 4.39 |
| 700-MAX691ACPE | MAX691ACPE+ | PDIP-16 | 4.5 to 5.0 | Active High and Low/Push-Pull | 100 | Adj RS, Batt on, Pwr Fail, WD | 8.58 | 4.38 | 4.01 |
| 700-MAX690ACPA | MAX690ACPA+ | PDIP-8 | 4.5 to 5.0 | Active Low/Push-Pull | 350 | Adj Reset, Pwr Fail, WD | 7.63 | 3.87 | 3.56 |
| 700-MAX690ACSA | MAX690ACSA+ | SOIC-8 | 4.5 to 5.0 | Active Low/Push-Pull | 350 | Adj Reset, Pwr Fail, WD | 8.03 | 4.07 | 3.75 |
| 700-MAX690AESAs | MAX690AESAs | SOIC-8 | 4.5 to 5.0 | Active Low/Push-Pull | 350 | Adj Reset, Pwr Fail, WD | 11.23 | 5.70 | 5.24 |
| 700-MAX690CPA | MAX690CPA+ | PDIP-8 | 4.5 to 5.0 | Active Low/Push-Pull | 5000 | Adj Reset, Pwr Fail, WD | 7.94 | 4.03 | 3.71 |
| 700-MAX690EPA | MAX690EPA+ | PDIP-8 | 4.5 to 5.0 | Active Low/Push-Pull | 5000 | Adj Reset, Pwr Fail, WD | 11.54 | 5.86 | 5.39 |
| 700-MAX691ACSE | MAX691ACSE+ | SOIC-16 | 4.5 to 5.0 | Active High and Low/Push-Pull | 100 | Adj RS, Batt on, Pwr Fail, WD | 9.06 | 4.59 | 4.23 |
| 700-MAX691ACWE | MAX691ACWE+ | SOIC-16W | 4.5 to 5.0 | Active High and Low/Push-Pull | 100 | Adj RS, Batt on, Pwr Fail, WD | 9.00 | 4.59 | 4.20 |
| 700-MAX691AEPE | MAX691AEPE+ | PDIP-16 | 4.5 to 5.0 | Active High and Low/Push-Pull | 100 | Adj RS, Batt on, Pwr Fail, WD | 10.74 | 5.48 | 5.01 |
| 700-MAX691AESE | MAX691AESE+ | SOIC-16 | 4.5 to 5.0 | Active High and Low/Push-Pull | 100 | Adj RS, Batt on, Pwr Fail, WD | 10.74 | 5.48 | 5.01 |
| 700-MAX691AEWE | MAX691AEWE+ | SOIC-16W | 4.5 to 5.0 | Active High and Low/Push-Pull | 100 | Adj RS, Batt on, Pwr Fail, WD | 10.74 | 5.48 | 5.01 |
| 700-MAX692ACSA | MAX692ACSA+ | SOIC-8 | 4.5 to 5.0 | Active Low/Push-Pull | 350 | Adj Reset, Pwr Fail, WD | 8.42 | 4.07 | 3.75 |

