

# CYPRESS Buffers

RoHS Compliant This product is RoHS compliant.



ANALOG ICs

Cypress

## BUFFERS

Cypress delivers high-performance, mixed-signal, programmable solutions that provide customers with rapid time-to-market and exceptional system value. Cypress offerings include the flagship PSoC® programmable system-on-chip families and derivatives such as PowerPSoC® solutions for high-voltage and LED lighting applications, CapSense® touch sensing and TrueTouch™ solutions for touchscreens. Cypress is the world leader in USB controllers, including the high-performance West Bridge® solution that enhances connectivity and performance in multimedia handsets. Cypress is also a leader in high-performance memories and programmable timing devices. Cypress serves numerous markets including consumer, mobile handsets, computation, data communications, automotive, industrial and military.

For quantities greater than listed, call for quote..

MOUSER STOCK NO.	Cypress Part No.	Package	Operating Voltage		Output Frequency		Core Voltage	No. of PLL	No. of Outputs	Input Signal Type	Temperature Range (°C)	Price Each			
			Min.	Max.	Min.	Max.						1	25	50	
<b>Clock Fanout</b>															
727-CY2CC8100XI	CY2CC8100XI	SSOP-20	2.38	3.45	0	650	2.5/3.3	0	10	LVC MOS	-40 to +85	5.20	4.68	4.40	
727-CY2DP814ZXC	CY2DP814ZXC	TSSOP-16	3.13	3.45	0	450	3.3	0	4	LVDS/LVPECL/LVTTL	0 to 70	5.70	5.40	4.95	
<b>High Speed, Low Voltage Programmable Skew Clock</b>															
727-CY7B9911V-5JXC	CY7B9911V-5JXC	PLCC-32	3.0	3.6	3.75	110	3.3	1	8	LV TTL	0 to 70	31.15	23.48	22.69	
<b>Low Skew Clock</b>															
727-CY7B9910-2SXC	CY7B9910-2SXC	SOIC-24	4.5	5.5	15	80	5.0	1	8	TTL	0 to 70	13.05	9.83	9.50	
727-CY7B9910-5SXC	CY7B9910-5SXC	SOIC-24	4.5	5.5	15	80	5.0	1	8	TTL	0 to 70	8.35	6.30	6.09	
727-CY7B9910-5SXI	CY7B9910-5SXI	SOIC-24	4.5	5.5	15	80	5.0	1	8	TTL	-40 to +85	9.86	9.28	8.41	
<b>Low Voltage Programmable Skew Clock</b>															
727-CY7B991V-2JXC	CY7B991V-2JXC	PLCC-32	3.0	3.6	3.75	80	3.3	1	8	LV TTL	0 to 70	19.92	15.00	14.50	
727-CY7B991V-5JXI	CY7B991V-5JXI	PLCC-32	3.0	3.6	3.75	80	3.3	1	8	LV TTL	0 to 70	15.85	11.93	11.53	
727-CY7B991V-7JXC	CY7B991V-7JXC	PLCC-32	3.0	3.6	3.75	80	3.3	1	8	LV TTL	0 to 70	13.44	10.13	9.79	
<b>Non-Zero Delay</b>															
727-CY2304NZZXC-1	CY2304NZZXC-1	TSSOP-8	3.0	3.6	0	140	3.3	0	4	LVC MOS/TTL	0 to 70	4.20	3.15	3.05	
727-CY2304NZZXI-1	CY2304NZZXI-1	TSSOP-8	3.0	3.6	0	140	3.3	0	4	LVC MOS/TTL	-40 to +85	5.20	3.83	3.70	
727-CY2309NZSXC-1H	CY2309NZSXC-1H	SOIC-16	3.0	3.6	0	133	3.3	0	9	LVC MOS/LVTTL	0 to 70	5.61	4.13	3.99	
727-CY2309NZSXI-1H	CY2309NZSXI-1H	SOIC-16	3.0	3.6	0	133	3.3	0	9	LVC MOS/LVTTL	-40 to +85	6.00	4.50	4.35	
727-CY29940AXC	CY29940AXC	TQFP-32	2.38	3.45	0	200	2.5/3.3	0	18	LVPECL/LVC MOS	0 to 70	7.90	5.85	5.66	
727-CY29942AXC	CY29942AXC	TQFP-32	2.38	3.45	0	200	2.5/3.3	1	18	LVC MOS/LVTTL	0 to 70	6.96	6.06	5.85	
727-CY29942AXI	CY29942AXI	TQFP-32	2.38	3.45	0	200	2.5/3.3	0	18	LVC MOS/LVTTL	-40 to +85	7.41	6.24	6.05	
727-CY29946AXI	CY29946AXI	TQFP-32	2.38	3.6	0	200	2.5/3.3	0	10	LVC MOS/LVTTL	-40 to +85	7.20	5.33	5.15	
727-CY29972AXI	CY29972AXI	TQFP-52	3.0	3.6	0	125	3.3	1	12	Xtal/LVC MOS/LVTTL	-40 to +85	3.90	3.51	3.30	
<b>Programmable Skew Clock</b>															
727-CY7B991-2JXC	CY7B991-2JXC	PLCC-32	4.5	5.5	3.75	80	5.0	1	8	TTL	0 to 70	19.90	15.00	14.50	
727-CY7B991V-5JXI	CY7B991V-5JXI	PLCC-32	3.0	3.6	3.75	80	3.3	1	8	LV TTL	-40 to +85	19.70	14.85	14.36	
727-CY7B991-5JXI	CY7B991-5JXI	PLCC-32	4.5	5.5	3.75	80	5.0	1	8	TTL	-40 to +85	26.10	23.49	22.08	
<b>Zero Delay</b>															
727-CY2300SXC	CY2300SXC	SOIC-8	3.0	3.6	10	166.67	3.3	1	4	LVC MOS/LVTTL	0 to 70	6.41	5.64	5.30	
727-CY2302SXC-1	CY2302SXC-1	SOIC-8	3.13	5.5	10	133	3.3/5.0	1	2	LVC MOS/LVTTL	0 to 70	2.42	2.40	2.34	
727-CY2302SXI-1	CY2302SXI-1	SOIC-8	3.13	5.5	10	133	3.3/5.0	1	2	LVC MOS/LVTTL	-40 to +85	2.79	2.65	2.43	
727-CY2303SXC	CY2303SXC	SOIC-8	3.0	3.6	10	166.67	3.3	1	3	LVC MOS/LVTTL	0 to 70	4.75	4.50	4.13	
727-CY2304SXC-1	CY2304SXC-1	SOIC-8	3.0	3.6	10	133	3.3	0	4	LVC MOS/LVTTL	0 to 70	2.26	2.17	2.05	
727-CY2304SXC2	CY2304SXC-2	SOIC-8	3.0	3.6	10	133	3.3	0	4	LVC MOS/LVTTL	0 to 70	5.00	4.40	4.00	
727-CY2304SXI-1	CY2304SXI-1	SOIC-8	3.0	3.6	10	133	3.3	0	4	LVC MOS/LVTTL	-40 to +85	6.53	5.74	5.40	
727-CY2305CSXC-1	CY2305CSXC-1	SOIC-8	3.0	3.6	10	133	3.3	1	5	LVC MOS/LVTTL	0 to 70	1.56	1.54	1.52	
727-CY2305CSXC-1H	CY2305CSXC-1H	SOIC-8	3.0	3.6	10	133	3.3	1	5	LVC MOS/LVTTL	0 to 70	1.96	1.94	1.90	
727-CY2305CSXI-1H	CY2305CSXI-1H	SOIC-8	3.0	3.6	10	133	3.3	1	5	LVC MOS/LVTTL	-40 to +85	2.31	2.30	2.22	
727-CY2305SXC-1	CY2305SXC-1	SOIC-8	3.0	3.6	10	133	3.3	1	5	LVC MOS/LVTTL	0 to 70	1.30	.98	.94	
727-CY2305SXC-1H	CY2305SXC-1H	SOIC-8	3.0	3.6	10	133	3.3	1	5	LVC MOS/LVTTL	0 to 70	1.32	1.15	1.11	
727-CY2305SXI-1	CY2305SXI-1	SOIC-8	3.0	3.6	10	133	3.3	1	5	LVC MOS/LVTTL	-40 to +85	1.96	1.31	1.26	
727-CY2305SXI-1H	CY2305SXI-1H	SOIC-8	3.0	3.6	10	133	3.3	1	5	LVC MOS/LVTTL	-40 to +85	4.40	3.80	3.60	
727-CY2308SXC-1	CY2308SXC-1	SOIC-16	3.0	3.6	10	133	3.3	1	8	LVC MOS/LVTTL	0 to 70	3.34	2.51	2.42	
727-CY2308SXC-1H	CY2308SXC-1H	SOIC-16	3.0	3.6	10	133	3.3	1	8	LVC MOS/LVTTL	0 to 70	2.98	2.62	2.38	
727-CY2308SXC-2	CY2308SXC-2	SOIC-16	3.0	3.6	10	133	3.3	1	8	LVC MOS/LVTTL	0 to 70	3.10	2.33	2.25	
727-CY2308SXC3	CY2308SXC-3	SOIC-16	3.0	3.6	10	133	3.3	1	8	LVC MOS/LVTTL	0 to 70	4.58	4.03	3.66	
727-CY2308SXC4	CY2308SXC-4	SOIC-16	3.0	3.6	10	133	3.3	1	8	LVC MOS/LVTTL	0 to 70	4.58	4.03	3.66	
727-CY2308SXI-1	CY2308SXI-1	SOIC-16	3.0	3.6	10	133	3.3	1	8	LVC MOS/LVTTL	-40 to +85	6.30	6.30	6.09	
727-CY2308SXI-1H	CY2308SXI-1H	SOIC-16	3.0	3.6	10	133	3.3	1	8	LVC MOS/LVTTL	-40 to +85	2.81	2.48	2.33	
727-CY2308SXI-2	CY2308SXI-2	SOIC-16	3.0	3.6	10	133	3.3	1	8	LVC MOS/LVTTL	-40 to +85	9.45	8.32	7.82	
727-CY2308SXI-4	CY2308SXI-4	SOIC-16	3.0	3.6	10	133	3.3	1	8	LVC MOS/LVTTL	-40 to +85	9.45	8.32	7.82	
727-CY2308ZXI1H	CY2308ZXI-1H	TSSOP-16	3.0	3.6	10	133	3.3	1	8	LVC MOS/LVTTL	-40 to +85	5.00	4.40	4.00	
727-CY2309SXC-1	CY2309SXC-1	SOIC-16	3.0	3.6	10	133	3.3	1	9	LVC MOS/LVTTL	0 to 70	2.63	2.29	2.22	
727-CY2309SXC-1H	CY2309SXC-1H	SOIC-16	3.0	3.6	10	133	3.3	1	9	LVC MOS/LVTTL	0 to 70	2.98	2.62	2.38	
727-CY2309SXI1	CY2309SXI-1	SOIC-16	3.0	3.6	10	133	3.3	1	9	LVC MOS/LVTTL	-40 to +85	8.75	7.70	7.00	
727-CY2309ZXC-1H	CY2309ZXC-1H	TSSOP-16	3.0	3.6	10	133	3.3	1	9	LVC MOS/LVTTL	0 to 70	5.37	3.63	3.51	
727-CY23EP05SXC-1	CY23EP05SXC-1	SOIC-8	2.3	3.6	10	220	2.5/3.3	1	5	LVC MOS/LVTTL	0 to 70	3.25	3.14	2.97	
727-CY23EP05SXC-1H	CY23EP05SXC-1H	SOIC-8	2.3	3.6	10	220	2.5/3.3	1	5	LVC MOS/LVTTL	0 to 70	4.44	3.84	3.71	
727-CY23EP05SXI-1	CY23EP05SXI-1	SOIC-8	2.3	3.6	10	220	2.5/3.3	1	5	LVC MOS/LVTTL	-40 to +85	5.18	4.55	4.28	
727-CY23EP05SXI-1H	CY23EP05SXI-1H	SOIC-8	2.3	3.6	10	220	2.5/3.3	1	5	LVC MOS/LVTTL	-40 to +85	6.30	5.54	5.21	
727-CY23EP09SXC-1	CY23EP09SXC-1	SOIC-16	2.3	3.6	10	220	2.5/3.3	1	9	LVC MOS/LVTTL	0 to 70	6.64	5.84	5.49	
727-CY23EP09SXC-1H	CY23EP09SXC-1H	SOIC-16	2.3	3.6	10	220	2.5/3.3	1	9	LVC MOS/LVTTL	0 to 70	5.34	5.20	4.92	
727-CY23EP09ZXC-1H	CY23EP09ZXC-1H	TSSOP-16	2.3	3.6	10	220	2.5/3.3	1	9	LVC MOS/LVTTL	0 to 70	3.85	3.72	3.59	
727-CY23EP09ZXI-1H	CY23EP09ZXI-1H	TSSOP-16	2.3	3.6	10	220	2.5/3.3	1	9	LVC MOS/LVTTL	-40 to +85	5.60	4.92	4.48	
727-CY23FP12OXC	CY23FP12OXC	SSOP-28	3.13	3.45	10	220	3.3	1	12	LVC MOS/LVTTL	0 to 70	9.00	6.75	6.53	
727-CY23S05SXC-1	CY23S05SXC-1	SOIC-8	3.0	3.6	10	133	3.3	1	5	LVC MOS/LVTTL	0 to 70	2.75	2.64	2.55	
727-CY23S05SXC-1H	CY23S05SXC-1H	SOIC-8	3.0	3.6	10	133	3.3	1	5	LVC MOS/LVTTL	0 to 70	2.75	2.64	2.55	
727-CY29352AXI	CY29352AXI	TQFP-32	2.38	3.45	16.67	200	2.5/3.3	1	11	LVPECL/LVC MOS	-40 to +85	7.51	7.11	6.52	