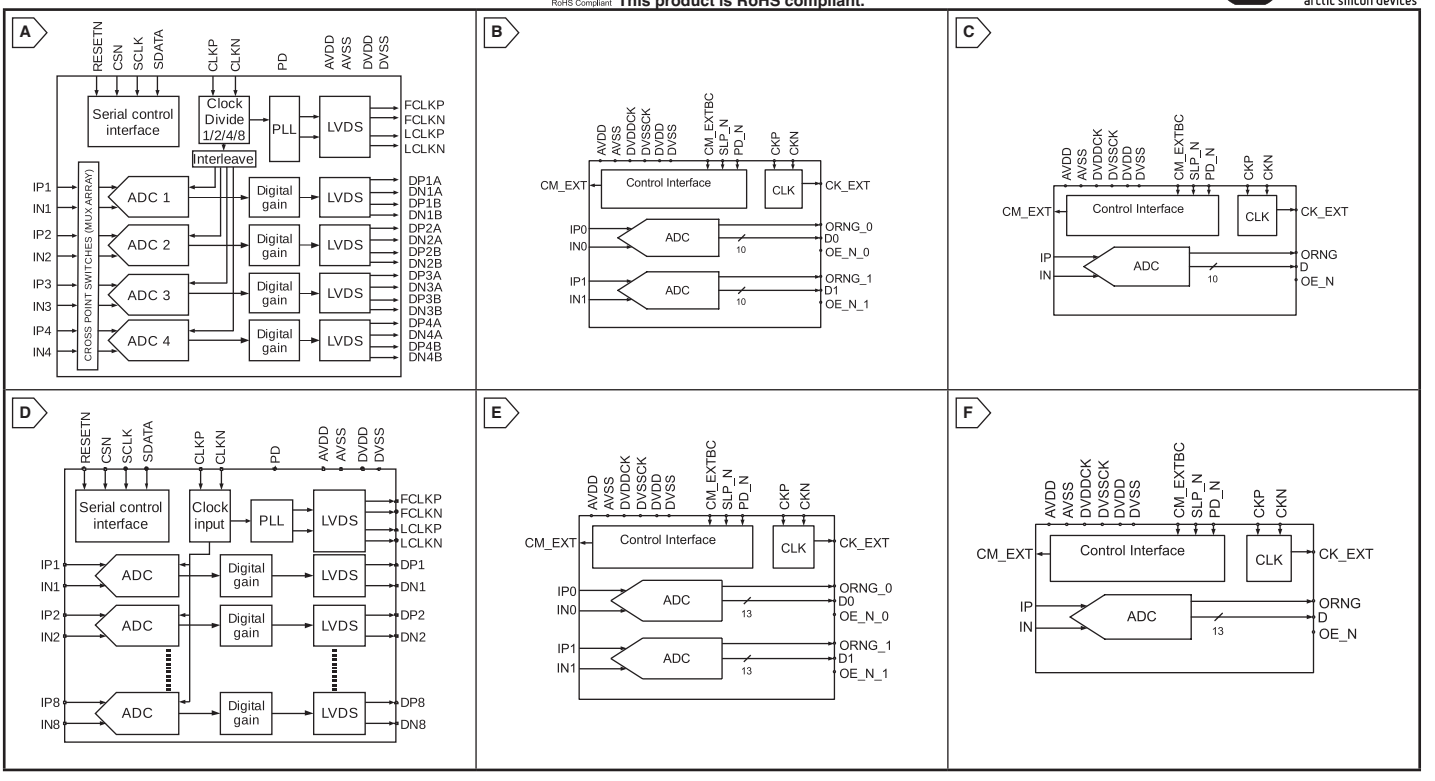
ARCTIC SILICON DEVICES Analog Digital Converters

RoHS Compliant This product is RoHS compliant.



ASD BLIZZARD FAMILY

Blizzard ADCs are pin compatible. The products within the family can be configured with the SPI interface. The ASD5020 modes can be chosen by SPI configuration. Additionally, ASD5020 can be configured as ASD5010. ASD5010 can not be configured as ASD5020.

- 1.80V Input Voltage

RoHS Compliant

For quantities greater than listed, call for quote..

MOUSER STOCK NO.	Arctic Silicon Part No.	Fig.	Package Type	Number of ADC Inputs	Conversion Rate	SNR	Max. Power Dissipation	Price Each 1	25	50
770-ASD5010L500INT	ASD5010L500INT	A	QFN-48	4	500MSPs	49.5dB	295mW	26.83	25.99	21.13
770-ASD5010L1000INT	ASD5010L1000INT	A	QFN-48	2	1000MSPs	49.8dB	710mW	36.48	35.33	28.61
770-ASD5020L640INT	ASD5020L640INT	A	QFN-48	2	640MSPs	70.0dB	490mW	52.16	50.23	40.30

DUAL ULTRA LOW POWER 20/40/65/80 MSPS, 10-BIT ANALOG-TO-DIGITAL CONVERTER

The ASD0400 and ASD0401 are high performance low power dual analog-to-digital converters (ADC). The ADC employs internal reference circuitry, a CMOS control interface and CMOS output data, and is based on a proprietary structure. Digital error correction is employed to ensure no missing codes in the complete full scale range. Idle modes with fast startup times exist.

- 1.80V Input Voltage

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MOUSER STOCK NO.	Arctic Silicon Part No.	Fig.	Package Type	Number of ADC Inputs	Conversion Rate	SNR	Max. Power Dissipation	Price Each 1	25	50
Dual Channel 10-bit 15 to 80MSPS ADC										
770-ASD0400L20-INT	ASD0400L20-INT	B	QFN-64	2	20MSPS	61.6dB	24mW	9.20	8.40	8.11
770-ASD0400L65-INT	ASD0400L65-INT	B	QFN-64	2	65MSPS	61.5dB	65mW	12.98	12.20	11.77
770-ASD0400L80-INT	ASD0400L80-INT	B	QFN-64	2	80MSPS	61.3dB	78mW	21.91	20.60	19.88
Single Channel 10-bit 15 to 80MSPS ADC										
770-ASD0401L20-INT	ASD0401L20-INT	C	QFN-40	1	20MSPS	61.6dB	15mW	5.11	4.80	4.63
770-ASD0401L65-INT	ASD0401L65-INT	C	QFN-40	1	65MSPS	61.5dB	38mW	7.87	7.40	7.14
770-ASD0401L80-INT	ASD0401L80-INT	C	QFN-40	1	80MSPS	61.3dB	46mW	8.94	8.40	8.11

ULTRA LOW POWER 20/40/65/80 MSPS, 13/12-BIT ANALOG-TO-DIGITAL CONVERTER

The ASD0500 and ASD0501 are high performance low power dual analog-to-digital converters (ADC). The ADC employs internal reference circuitry, a CMOS control interface and CMOS output data, and is based on a proprietary structure. Digital error correction is employed to ensure no missing codes in the complete full scale range. Idle modes with fast startup times exist.

- 1.80V Input Voltage

RoHS Compliant

For quantities greater than listed, call for quote..

MOUSER STOCK NO.	Arctic Silicon Part No.	Fig.	Package Type	Number of ADC Inputs	Conversion Rate	SNR	Max. Power Dissipation	Price Each 1	25	50
Dual Channel 13/12-bit 15 to 80MSPS ADC										
770-ASD0500L40-INT	ASD0500L40-INT	D	QFN-64	2	40MSPS	72dB	55mW	23.62	22.20	21.42
770-ASD0500L80-INT	ASD0500L80-INT	D	QFN-64	2	80MSPS	71.2dB	102mW	48.30	45.40	43.81
Single Channel 13/12-bit 15 to 80MSPS ADC										
770-ASD0501L20-INT	ASD0501L20-INT	E	QFN-40	1	20MSPS	72.1dB	19mW	10.64	10.00	9.65
770-ASD0501L65-INT	ASD0501L65-INT	E	QFN-40	1	65MSPS	71.5dB	50mW	21.28	20.00	19.30

OCTAL LOW POWER 20 TO 80MSPS 12/13-BIT ANALOG-TO-DIGITAL CONVERTER

ASD1000 is a high performance low power octal analog-to-digital converter (ADC). The ADC is based on a proprietary structure and employs internal reference circuitry, a serial control interface and serial LVDS output data. Data and frame synchronization output clocks are supplied for data capture at the receiver. Various modes and configuration settings can be applied to the ADC through the serial control interface (SPI). Each channel can be powered down independently and data format can be selected through this interface. A full chip idle mode can be set by a single external pin. Register settings determine the exact function of this external pin. There are two options for the serial LVDS outputs, 12-bit or 14-bit. In 12-bit mode, the LSB bit from the ADCs are removed in the output stream. In 14-bit mode, a '0' is added in the LSB position. ASD1000 is designed to easily interface with field-programmable gate arrays (FPGAs) from several vendors. The very low start up times for ASD1000 allows significant power reduction in duty-cycled systems, by utilizing the Sleep Modes or Power Down Mode when the receive path is idle.

- 1.80V Input Voltage

RoHS Compliant

For quantities greater than listed, call for quote..

MOUSER STOCK NO.	Arctic Silicon Part No.	Fig.	Package Type	Number of ADC Inputs	Conversion Rate	SNR	Max. Power Dissipation	Price Each 1	25	50
770-ASD1000L40-INT	ASD1000L40-INT	F	TQFP-80	8	40MSPS	71.5dB	280mW	28.94	27.20	26.25
770-ASD1000L80-INT	ASD1000L80-INT	F	TQFP-80	8	80MSPS	70dB	470mW	48.94	46.00	44.39

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